## Use cases of Programmable Dataplane (P4)

ECE/CS598HPN

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### Which paper(s) did you read?

• (A) BeauCoup: Network Monitoring

• (B) Elmo: Multicast

• (C) Both

• (D) Neither

### Network Monitoring

- Most popular usecase of programmable dataplanes.
- Lots of recent papers!
- Key challenges:
  - Dealing with small amount of memory.
  - Ensuring high line rate (small processing capability, limited memory access)
  - Supporting a wide variety of queries.

<sup>1</sup>[bo'ku] Adv. many, a lot.

### **BeauCoup:**<sup>1</sup>

Answering **MONY** network traffic queries,

#### **ONC** memory update at a time!

Xiaoqi Chen, Shir Landau-Feibish, Mark Braverman, Jennifer Rexford





Slides from Xiaoqi Chen

### Network traffic query



### Many network traffic queries





#### **One** memory update at a time?

- Constant memory update per packet, regardless of the number of queries?
- Game plan:
  - I. Each query uses only **o(I)** memory update per packet **on average**
  - 2. Combine many different queries, on average uses **O(I)**
  - 3. Coordinate, **at most O(I)** per packet

### BeauCoup's Approach

- Challenge: many queries, few memory updates
- Achieving **o(I)** memory access: coupon collectors

### The coupon collector problem

- 4 different coupons, collect all of them
- Random draws

A

• How many total draws are regired?



### Naïve Approach

#### Query: Select DstlP where distinct(SrclP)>130

- Map each ScrIP to a coupon
  - How many total coupons?
  - How many do you need to collect?
- Issues with this approach:
  - Too much memory
  - Each packet results in a coupon collection.
    - Exceed O(I) access when multiple such queries are combined.

### BeauCoup coupon collector



### BeauCoup coupon collector

### *f*(*SrcIP*) -> Coupon ?

- Generalization: (*m*, *p*, *n*)-coupon collector
- m\*p<1, most packets collect no coupon</li>



### Stacking queries: same attribute

# One hash function for each attribute

 $q_1: f(SrcIP) \to Coupon$ **q**<sub>6</sub>: **g**(**DstIP**) -> Coupon  $m_{s}=3, p_{s}=1/8$ **q**<sub>1</sub> #2 **h**<sub>1</sub>(SrcIP) -> 1/23/4 **q**<sub>6</sub> #2 **q**<sub>6</sub> #3 **h**<sub>2</sub>(*DstIP*) -> 1/2 3/4 1

Randomly break ties if a coupon needs to be collected for two different attributes

### System design

- Query compiler: finds coupon collector configurations
  - Stops near query thresholds, minimize error
  - Hardware limits (e.g., memory access limit)
  - Fairness across queries
- Data plane program: collect coupons into in-memory table
  - Simultaneously run **many** queries
  - At most **one** coupon per packet
  - Update queries on-the-fly

### Query compiler







### Installing queries into switches



### TCAM for selecting a coupon

Matc	h h <sub>A</sub> ( <i>SrcPort</i> )	Quer	y#,Coupon#	
Ma	atch h <sub>B</sub> (DstPort	) Qu	ery#,Coupon#	ŧ
	Match h <sub>c</sub> (Srcl	P)	Query#,Coupo	n#
	Match h <sub>D</sub> (D	stIP)	Query#,Cou	pon#
	000****	*	(6,1)	
	001****	*	(6,2)	
	010****	*	(6,3)	
	01101**	*	(8,1)	

Packet SrcPort: 25012 DstPort: 443 SrcIP: 10.0.1.15 DstIP: 162 249 4 107  $h_A(SrcPort)=101010$  No coupon  $m_B(DstPort)=111010$  No coupon  $h_B(SrcIP)=1010111...$  No coupon  $h_D(DstIP)=0101011...$  Collect coupon ( $q_6$ , #3)

### Coupon collector table in SRAM



• Clear rows after timeout

### Evaluation highlights

- How efficient is BeauCoup?
- Uses 4x~IOx fewer memory access than

the state-of-the-art to achieve the same accuracy.

• How much hardware resource?

On the Barefoot Tofino programmable switch, BeauCoup occupies **<50% of each resource** 

#### **BeauCoup:** Answering **many** network traffic queries, **one** memory update at a time!

- **Scalable**: built upon *coupon collectors,* runs many queries simultaneously
- **Versatile**: change queries on the fly, without recompiling P4 program
- Efficient: achieve the same accuracy using 4x-10x fewer memory accesses

Is this a good usecase of programmable dataplanes?

### What are the limitations?

### Elmo: Source Routed Multicast for Public Clouds

#### Muhammad Shahbaz

Lalith Suresh, Jennifer Rexford, Nick Feamster, Ori Rottenstreich, and Mukesh Hira









Slides from Muhammad Shahbaz











#### Limitations of <u>Native</u> Multicast





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#### **Restricted to Unicast-based Alternatives**





#### **Restricted to Unicast-based Alternatives**





Controller

### Need a scheme that <u>scales</u> to millions of groups <u>without</u>

excessive control, end-host CPU, and traffic overheads!



#### **Proposal: Source Routed Multicast**

#### Controller



#### **Proposal: Source Routed Multicast**

#### Controller



#### **Proposal: Source Routed Multicast**



#### A <u>Naïve</u> Source Routed Multicast



### Enabling Source Routed Multicast in Public Clouds

Key attributes:

- <u>Efficiently encode</u> multicast forwarding policy inside packets
- Process this encoding at hardware speed in the switches
- **Execute** tenants' applications **without modification**

A multicast group encoded as a list of **(Switch, Ports)** pairs

Switch	1:	[Ports	]
Switch	2:	[	•••]
Switch	3:	[	]
Switch	4:	[	.x]
Switch	5:	[.×	]



A multicast group encoded as a list of **(Switch, Ports)** pairs

Switch	1:	[Bitmap]
Switch	2:	[]
Switch	3:	[]
Switch	4:	[x]
Switch	5:	[.×]

2 Group switches into layers



More precisely: upstream leaf, upstream spine, core, downstream spine, downstream leaf

A multicast group encoded as a list of **(Switch, Ports)** pairs

Switch	1:	[Bitmap]
Switch	2:	[]
Switch	3:	[]
Switch	4:	[x]
Switch	5:	[.×]

3 Switches within a layer with same ports share a bitmap



3 Switches within a layer with same ports share a bitmap





A multicast group encoded as a list of **(Switch, Ports)** pairs

Switch 1: [Bitmap]	Core
Switch 2,3: []	Spine
Switch 4: [x]	Leaf
Switch 5: [.x]	

 Switches within a layer with N different ports share a bitmap



 Switches within a layer with N different ports share a bitmap



```
A multicast group encoded as
a list of (Switch, Ports) pairs
```

Switch 1: [Bitmap]	Core
Switch 2,3: []	Spine
Switch 4,5: [.xx]	Leaf

(4) Switches within a layer with N different ports share a bitmap



bitmap for larger groups



Core

Spine

Leaf

A multicast group encoded as a list of **(Switch, Ports)** pairs

Switch	1: [Bitmap]
Switch	2,3: []
Switch	4,5:
	[.xx]

Default Bitmap Switch Table Entries

1	Encode switch ports as a <b>bitmap</b>
2	Group switches into layers
3	Switches within a layer with: - <b>same</b> ports <b>share a bitmap</b>
4	- N different ports share a bitmap
5	Use <b>switch entries</b> and a <b>default</b> <b>bitmap</b> for <b>larger groups</b>

For a data center with:

- 628 switches
- 325 bytes header space

#### Supports **a Million** groups!



			Sender-specific leaf, spine, and core p-rules			Common downstrear		
Sender H <sub>a</sub>		type	<i>u</i> -leaf	<i>u</i> -spine	d-core	danina	dlaaf	
Outer header(s)	VXLAN	и	01 M	00   M	0011	<i>a</i> -spine	<i>a</i> -lear	Packet body
Sender H <sub>k</sub>			At $L_0$ : forward to $H_b$ and multipath to $P_c$	P <sub>0</sub> : multipath to C	C: forward to P <sub>2</sub> , P <sub>3</sub>	10:[P <sub>0</sub> ] 11:[P <sub>3</sub> ] 01:[P <sub>2</sub> ] Default	$ \begin{array}{c c} 11:[L_0,L_6] \\ \hline 10:[L_5] \\ \hline 01:[L_7] \\ Default \\ \end{array} $	
Outer header(s)	VXLAN	и	00 M	00   M	1001	D . femurend to I	L <sub>0</sub> : forward to H <sub>a</sub> , H <sub>b</sub>	Packet body
			At L <sub>5</sub> : multipath to $P_2$	P <sub>2</sub> : multipath to C	C: forward to P <sub>0</sub> , P <sub>3</sub>	$P_0$ : forward to $L_0$ $P_2$ : forward to $L_5$ $P_3$ : forward to $L_6$ , $L_7$	$L_5$ : forward to $H_k$ $L_6$ : forward to $H_m$ , $H_n$ $L_7$ : forward to $H_p$	

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Downstream spine and leaf assignments with varying degree of redundancy (R) and *s*-rules.

R = 0	R = 2	
<i>#s</i> -rules = 0	<i>#s</i> -rules = 1	#s-rules = 0, 1
(p) 10:[P <sub>0</sub> ] (p) 01:[P <sub>2</sub> ] (d) 11:[P <sub>3</sub> ]	(p) 10:[P <sub>0</sub> ] (p) 01:[P <sub>2</sub> ] (s) 11:[P <sub>3</sub> ]	(p) 10:[P <sub>0</sub> ] (p) 11:[P <sub>2</sub> ,P <sub>3</sub> ]
(p) 11:[L <sub>0</sub> ,L <sub>6</sub> ] (p) 10:[L <sub>5</sub> ] (d) 01:[L <sub>7</sub> ]	(p) 11:[L <sub>0</sub> ,L <sub>6</sub> ] (p) 10:[L <sub>5</sub> ] (s) 01:[L <sub>7</sub> ]	(p) 11:[L <sub>0</sub> ,L <sub>6</sub> ] (p) 11:[L <sub>5</sub> ,L <sub>7</sub> ]

#p-rules = 2 (and max two switches per p-rule)

legends: p-rule (p), s-rule (s), and default p-rule (d)

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						1	1	
Sender H <sub>a</sub>		type	<i>u</i> -leaf	u-spine	d-core	denine	dlaaf	
Outer header(s)	VXLAN	u	01 M	00   M	0011	<i>a</i> -spine	<i>a</i> -lear	Packet body
			At $L_0$ : forward to $H_b$	P <sub>0</sub> : multipath	C: forward	10:[P <sub>0</sub> ] 11:[P <sub>3</sub> ]	11:[L <sub>0</sub> ,L <sub>6</sub> ] 01:[L <sub>7</sub> ]	
Sender H <sub>k</sub>			and multipath to Po	to C	to $P_2$ , $P_3$	01:[P <sub>2</sub> ] Default	10:[L <sub>5</sub> ] Default	
Outer header(s)	VXLAN	u	00 M	00   M	1001	D , forward to I	$L_0$ : forward to $H_a$ , $H_b$	Packet body
			At $L_5$ : multipath to $P_2$	P <sub>2</sub> : multipath to C	C: forward to $P_0$ , $P_3$	$P_0$ : forward to $L_0$ $P_2$ : forward to $L_5$ $P_3$ : forward to $L_6$ , $L_7$	$L_5$ : forward to $H_k$ $L_6$ : forward to $H_m$ , $H_n$ $L_7$ : forward to $H_p$	

Sender-specific leaf, spine, and core p-rules

#### Common downstream spine and leaf *p*-rules

#### **Processing a Multicast Policy in <u>Elmo</u>**



#### **Processing a Multicast Policy in <u>Elmo</u>**



Implemented using P4 on a Barefoot Tofino Switch

#### Applications Run Without Performance Overhead



#### Conclusion

#### Elmo

Source Routed Multicast for Public Clouds

- <u>Designed</u> for multi-tenant data centers
- <u>Compactly encodes multicast</u> <u>policy</u> inside packets
- <u>Operates at hardware speed</u> using programmable data planes

Is this a good usecase of programmable dataplanes?

### What are the limitations?

### Other networking usecases

- Load balancing:
  - HULA: Scalable Load Balancing Using Programmable Data Planes, SOSR'16
- Congestion control:
  - Evaluating the Power of Flexible Packet Processing for Network Resource Allocation, NSDI'17
    - Support RCP and XCP on programmable switches
  - HPCC: High Precision Congestion Control, SIGCOMM'19
    - Obtain precise link information for congestion control
- A new protocols for more efficient L2 switching
  - The Deforestation of L2, SIGCOMM'I6
- And others...