

ECE483: Analog Integrated Circuit Design

Classroom	ECEB 3017
Class time	T/TR 11:00–12:20pm
Instructor:	Pavan Kumar Hanumolu, hanumolu@illinois.edu
Office:	CSL 413, Ph: 217-300-6579
Office hours:	Thursday 1:00pm-2:00pm in CSL 413
Textbook:	Gray, Hurst, Lewis, Meyer, <i>Analysis and Design of Analog Integrated Circuits</i> , Wiley, 5 th Edition
Prerequisites:	ECE342
Course website:	http://courses.engr.illinois.edu/ece483/
TA:	Ahmed Elmallah, elmalla2@illinois.edu
TA:	Karim Megawer, megawer2@illinois.edu
TA Office hours:	Wednesdays 5:00pm-6:00pm in ECEB 3013
TA Office hours:	Thursdays 5:00-6:00pm in ECEB 3015

Grading:

Homework	10%
Midterm I (Feb. 20)	20%
Midterm II (Mar. 27)	20%
Project (Due date: TBA)	20%
Final (May 8, 8:00-11:00am)	30%

Homework:

- (a) Each assignment is due on Friday 4pm. No late homework will be accepted.
- (b) Homework will not be graded in detail, but rather on a scale of 10 with 10 points awarded for mostly correct, 5 points for partially correct solutions, and 0 points for little or no effort.

Midterms/Final exam:

- (a) All exams will be closed book.
- (b) Final comprehensive exam will include everything covered during the semester.
- (c) If you need to reschedule any exam for a very good reason, you should let me know at least 15 days in advance.

Academic Honesty: Students should follow the UI standards of academic integrity described at http://www.admin.illinois.edu/policy/code/article1_part4_1-401.htm