

ECE483: Analog Integrated Circuit Design

Classroom	ECEB 1013
Class time	T/TR 11:00–12:20pm
Instructor:	Pavan Kumar Hanumolu, hanumolu@illinois.edu
Office:	CSL 413, Ph: 217-300-6579
Office hours:	Wednesday 5:00pm-6:00pm in ECEB 4026
Textbook:	Gray, Hurst, Lewis, Meyer, <i>Analysis and Design of Analog Integrated Circuits</i> , Wiley, 5 th Edition
Prerequisites:	ECE342
Course website:	http://courses.engr.illinois.edu/ece483/
TA:	Charlie Zhu, zhu68@illinois.edu
TA Office hours:	Tuesday 6:00pm-7:00pm in ECEB 4026
Discussion session:	Monday 5:00-6:00pm in ECEB 4026

Grading:

Homework	10%
Midterm I (Feb. 25, 6:00pm-8:00pm, ECEB 1002)	20%
Midterm II (Apr. 7, 6:00pm-8:00pm, 1002)	20%
Project (Due date: May 6, 5:00pm)	20%
Final (May 9, 8:00-11:00am)	30%

Homework:

- (a) Most of the homework assignments will be custom-designed problems related to topics covered in the class.
- (b) Each assignment is due at the beginning of Thursday's class. No late homework will be accepted.
- (c) Homework will not be graded in detail, but rather on a scale of 10 with 10 points awarded for mostly correct, 5 points for partially correct solutions, and 0 points for little or no effort.

Midterms/Final exam:

- (a) All exams will be closed book. One page of notes will be allowed.
- (b) Final comprehensive exam will include everything covered during the semester.
- (c) If you need to reschedule any exam for a very good reason, you should let me know at least 15 days in advance.

Academic Honesty: Students should follow the UI standards of academic integrity described at http://www.admin.illinois.edu/policy/code/article1_part4_1-401.htm