Project# Semester	
<b>Individual Report Evaluation Sheet</b>	
Introduction	/5
<ul> <li>Team project overview</li> </ul>	
<ul> <li>Individual responsibilities and role in the greater project</li> </ul>	
Individual Design Work	/10
<ul> <li>Design considerations</li> </ul>	
<ul><li>Changes since Design Review?</li></ul>	
• Diagrams	
Some examples:	
Schematics with components	
Pseudocode with flowcharts     PCP designs	
<ul><li>PCB designs</li><li>Testing/Verification</li></ul>	
Testing/verification     Test plans and procedures with specific timelines	
<ul> <li>Modular testing or System testing</li> </ul>	
Results (graphs, tables, diagrams, logs, etc.)	
<ul> <li>Contingency plans for failed tests</li> </ul>	
Conclusion	/5
<ul> <li>Self-assessment</li> </ul>	
<ul> <li>Individual workload vs total workload for the project</li> <li>Ahead or behind of schedule</li> </ul>	
<ul> <li>Plans for remaining work</li> </ul>	
<ul> <li>Specific timelines, goals, ambitious vs realistic objectives</li> </ul>	
<b>Document Quality</b>	/5
Cite all sources using IEEE citation style	<del></del>
• Follow the writing guide style requirements for final report	
Total	/ 25
Total	/ 25