

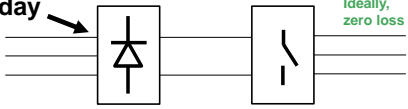
**ECE330: Power Circuits & Electromechanics**  
**Lecture 26. Rectifying sine waves into DC**





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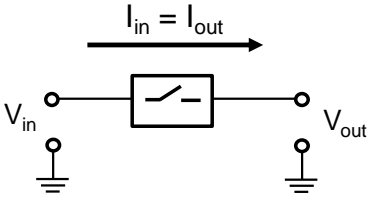
**Last time: Inverters**

**Today** → 

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**Rules for high efficiency**

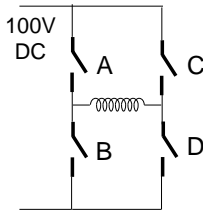


**100% Efficiency** Short circuit  $V_{in} = V_{out}$   
 Open circuit  $I_{in} = I_{out} = 0$

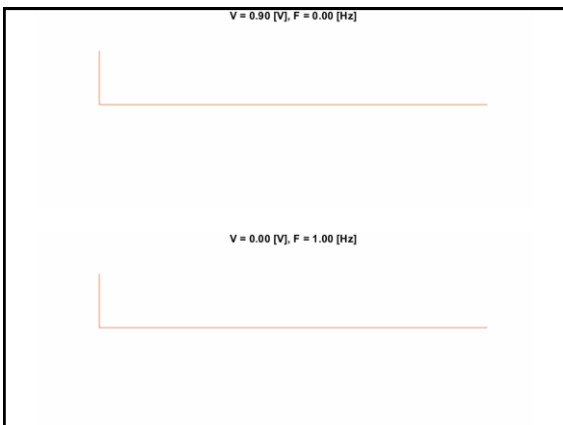
**The switch is the only device with 100% efficiency**

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**Full-bridge inverter**

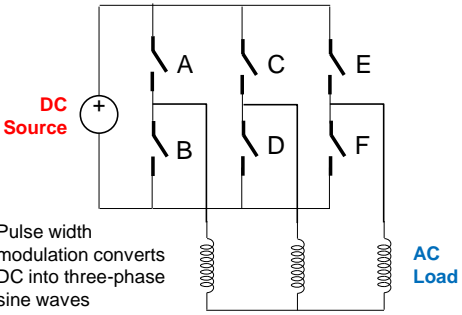


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**Six pulse inverter**



Pulse width modulation converts DC into three-phase sine waves

**Complicated control, but arbitrary voltage / frequency**

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### Six pulse rectifier

Powers DC load with no further control

How does it work?  
Uncontrolled. How many DC volts?

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### The ideal diode

If  $I_{in} = I_{out} \geq 0$ , then short circuit  $V_{in} = V_{out}$   
If  $V_{in} < V_{out}$ , then open circuit  $I_{in} = I_{out} = 0$

The diode is an automatic one-way switch  
100% efficient self-programming

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### Duality of inverters and rectifiers

One-switch inverter

One-switch rectifier

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### Analyzing the one-diode rectifier

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### Half-rectified sine wave

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### Duality of inverters and rectifiers

Full-bridge inverter

Full-bridge rectifier

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### An important lemma

$V_a$   $\rightarrow$   $\text{A}$   
 $V_b$   $\rightarrow$   $\text{B}$   
 $V_{out} = \max \{v_a, v_b\}$

*Proof.* Assume  $v_a > v_b$ . Want to show  $v_{out} = v_a$

- Since current  $> 0$ , at least one switch must be closed.
- If B is closed, A is open, then  $v_{out} = v_b$ . But  $v_a > v_b$ , so A closes.
- If A, B both closed, then current wants to flow  $a \rightarrow b$ . This closes B.
- If A is closed, B is open, then  $v_{out} = v_a$  as desired.  $\square$

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### An important lemma

$V_a$   $\rightarrow$   $\text{A}$   
 $V_b$   $\rightarrow$   $\text{B}$   
 $V_c$   $\rightarrow$   $\text{C}$   
 $V_d$   $\rightarrow$   $\text{D}$   
 $V_{out} = \max \{v_a, v_b, v_c, v_d\}$

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 $V_d$   $\rightarrow$   $\text{D}$   
 $V_{out} = \min \{v_a, v_b, v_c, v_d\}$

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### Mechanism of a full-bridge rectifier

$v_R(t)$   $v_s(t)$

$+\frac{1}{2}v_s(t)$   $\text{A}$   
 $-\frac{1}{2}v_s(t)$   $\text{C}$   
 $+\frac{1}{2}v_s(t)$   $\text{B}$   
 $-\frac{1}{2}v_s(t)$   $\text{D}$   
 $v_R(t)$

$v_R(t)$   $v_s(t)$

$+\frac{1}{2}v_s(t)$   $\text{A}$   
 $-\frac{1}{2}v_s(t)$   $\text{D}$   
 $+\frac{1}{2}v_s(t)$   $\text{B}$   
 $-\frac{1}{2}v_s(t)$   $\text{C}$   
 $v_R(t)$

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### Fully-rectified sine wave

$v_R(t)$

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### Sketch of the six pulse rectifier

$\max \{v_a, v_b, v_c\}$

$\min \{v_a, v_b, v_c\}$

$V_a$   $V_b$   $V_c$

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### Summary – Power Electronics

440V, 60 Hz

Arbitrary waveform

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