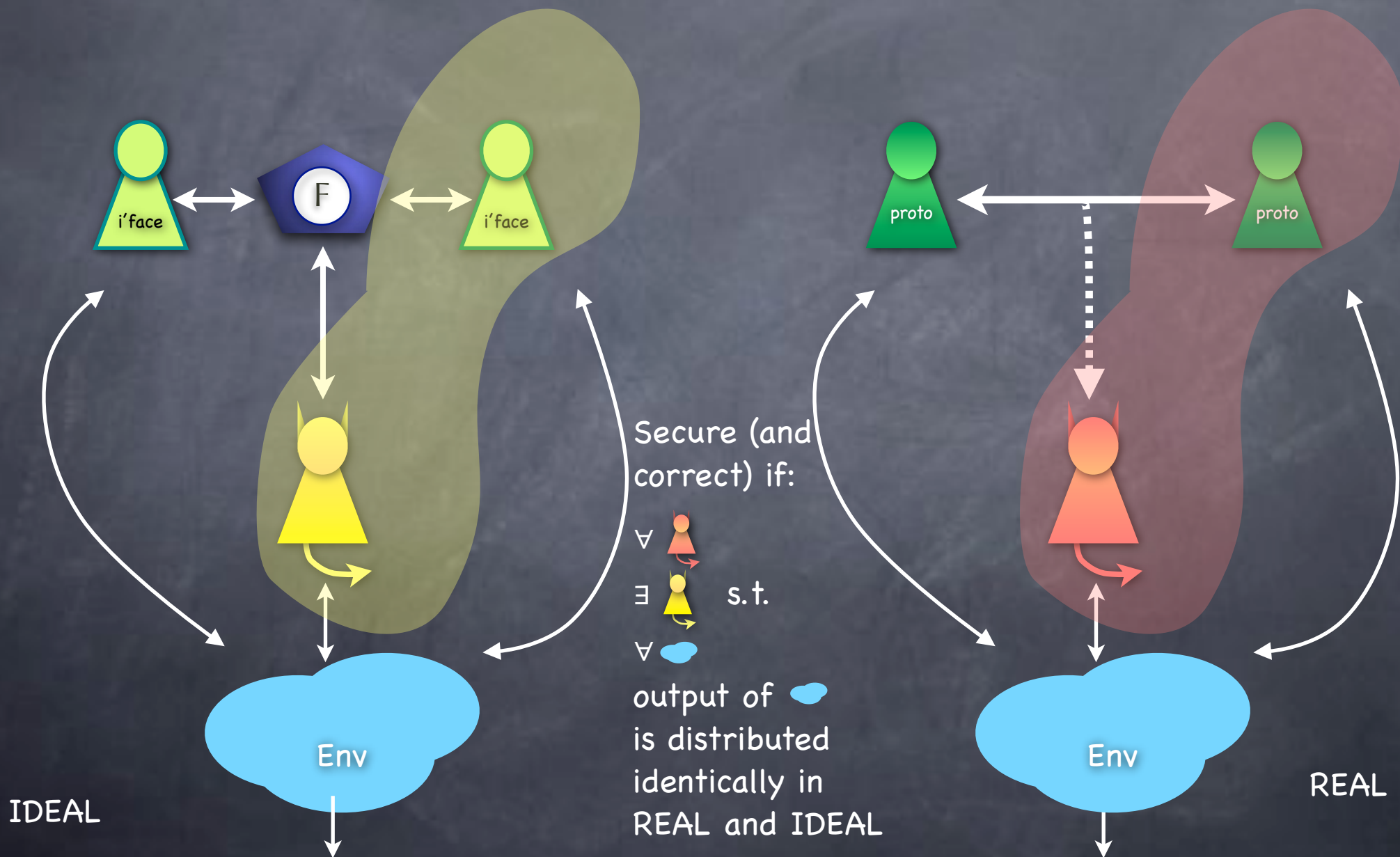


Secure 2-Party Computation

Lecture 12
Yao's Garbled Circuit

RECALL

SIM-Secure MPC



Passive Adversary

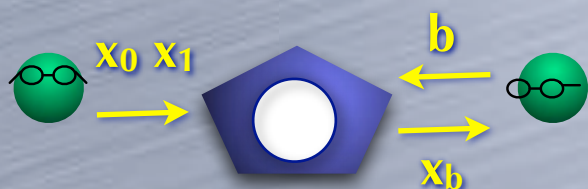
- Gets **only read access** to the internal state of the corrupted players (and can use that information in talking to environment)
 - Also called “Honest-But-Curious” adversary
 - Will require that **simulator also corrupts passively**
- Simplifies several cases
 - e.g. coin-tossing [**why?**], commitment [**coming up**]
- Oddly, sometimes security against a passive adversary is more demanding than against an active adversary
 - Active adversary: too pessimistic about what guarantee is available even in the IDEAL world
 - e.g. 2-party SFE for OR, with output going to only one party (trivial against active adversary; impossible without computational assumptions against passive adversary)

RECALL

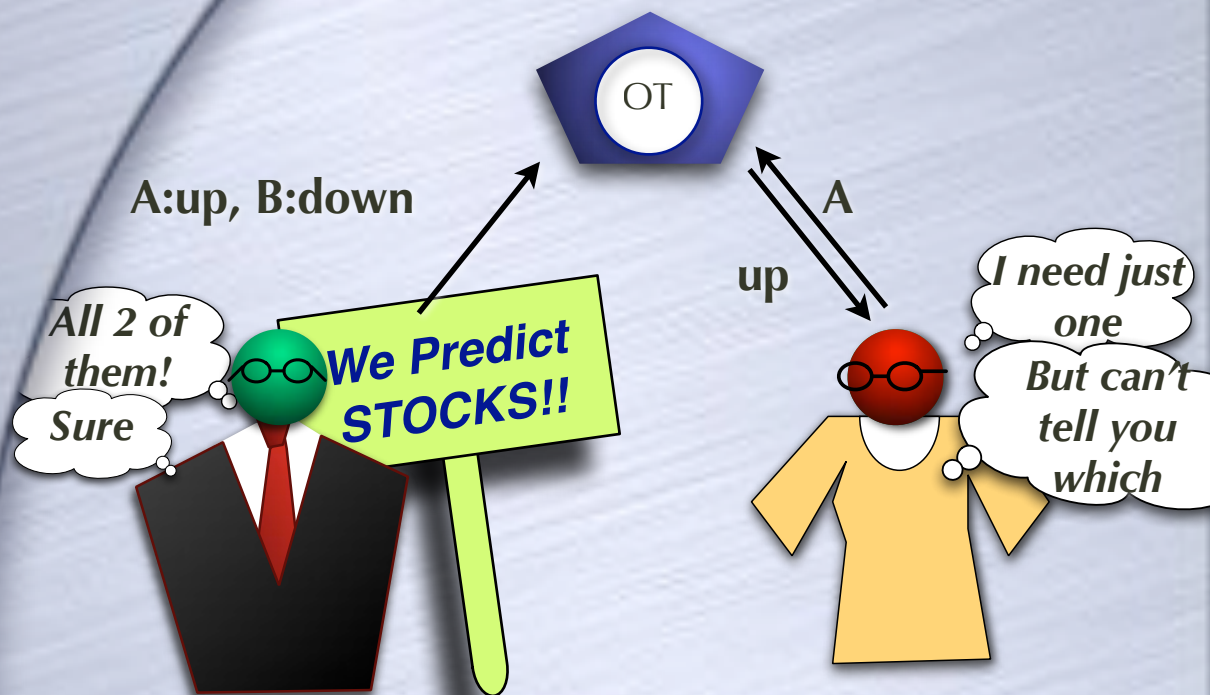
Oblivious Transfer

- Pick one out of two, without revealing which

- Intuitive property: transfer partial information “obliviously”



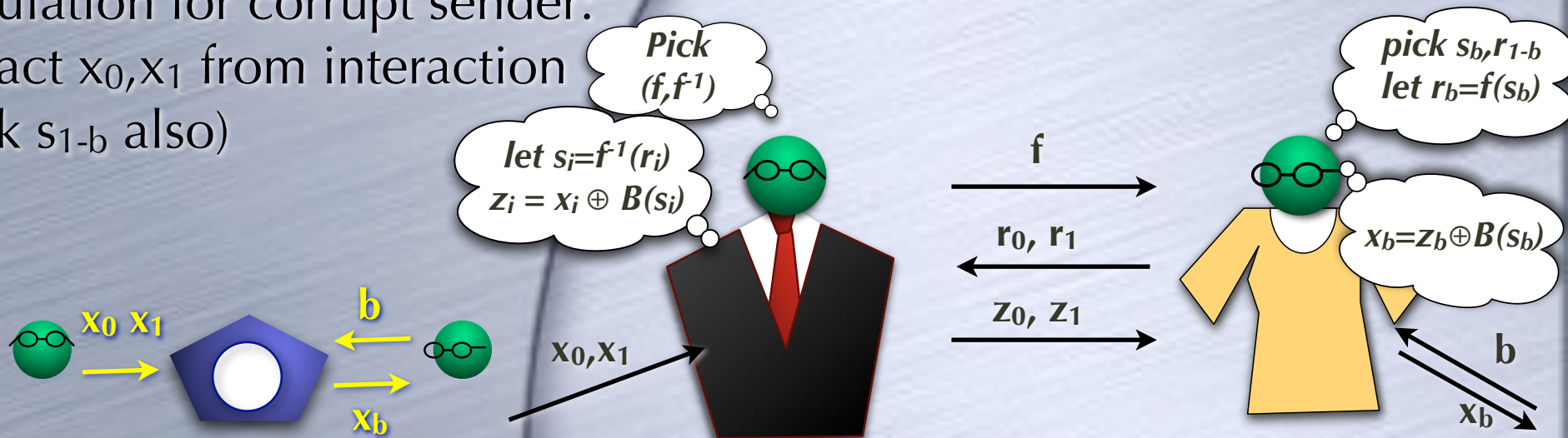
IDEAL World



RECALL

An OT Protocol (passive receiver corruption)

- Using a TOWP
 - Depends on receiver to pick x_0, x_1 as prescribed
- Simulation for passive corrupt receiver: simulate z_0, z_1 knowing only x_b (use random z_{1-b})
- Simulation for corrupt sender: Extract x_0, x_1 from interaction (pick s_{1-b} also)



2-Party (Passive) Secure Function Evaluation

2-Party (Passive)

Secure Function Evaluation

- Functionality takes $(X;Y)$ and outputs $f(X;Y)$ to Alice, $g(X;Y)$ to Bob

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 - Symmetric SFE from one-sided SFE (passive secure) [How?]
- So, for passive security, enough to consider one-sided SFE

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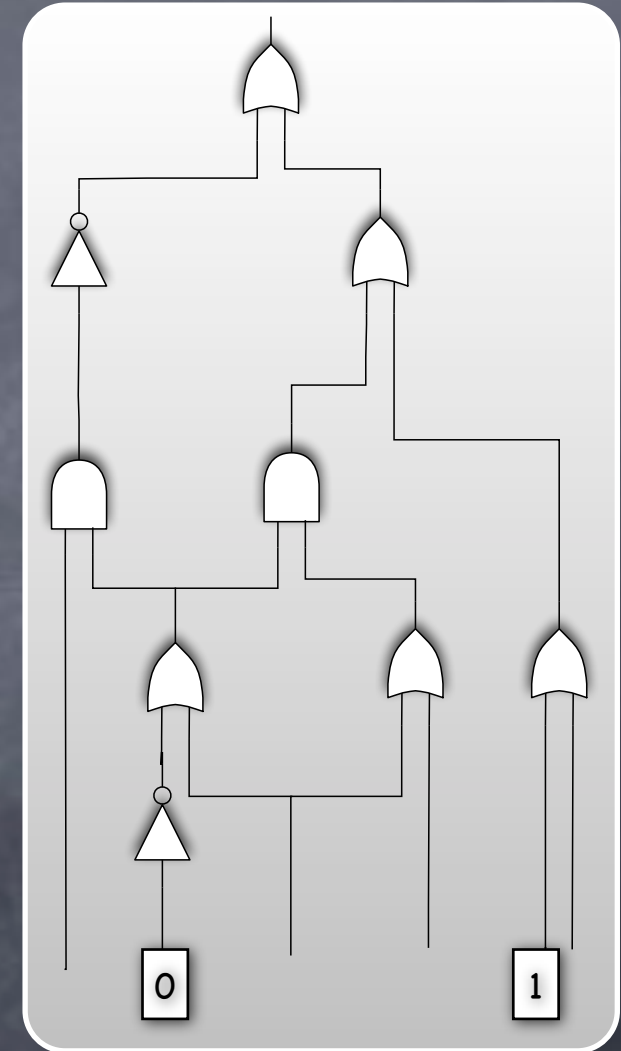
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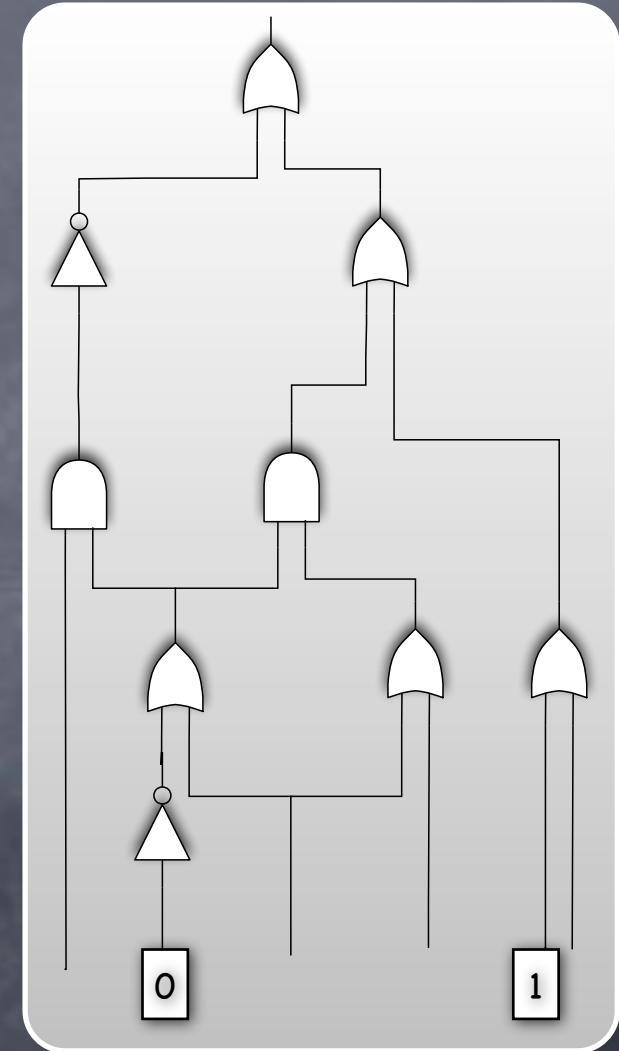
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- Can we do “general” deterministic, one-sided SFE (i.e., for all functions)?

Boolean Circuits



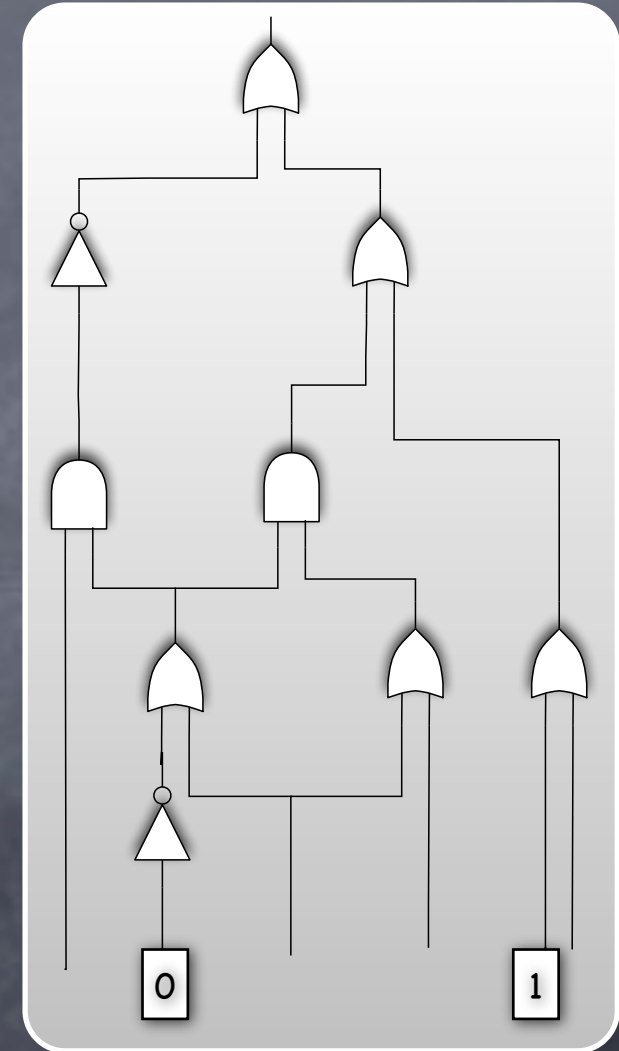
Boolean Circuits

- Directed acyclic graph



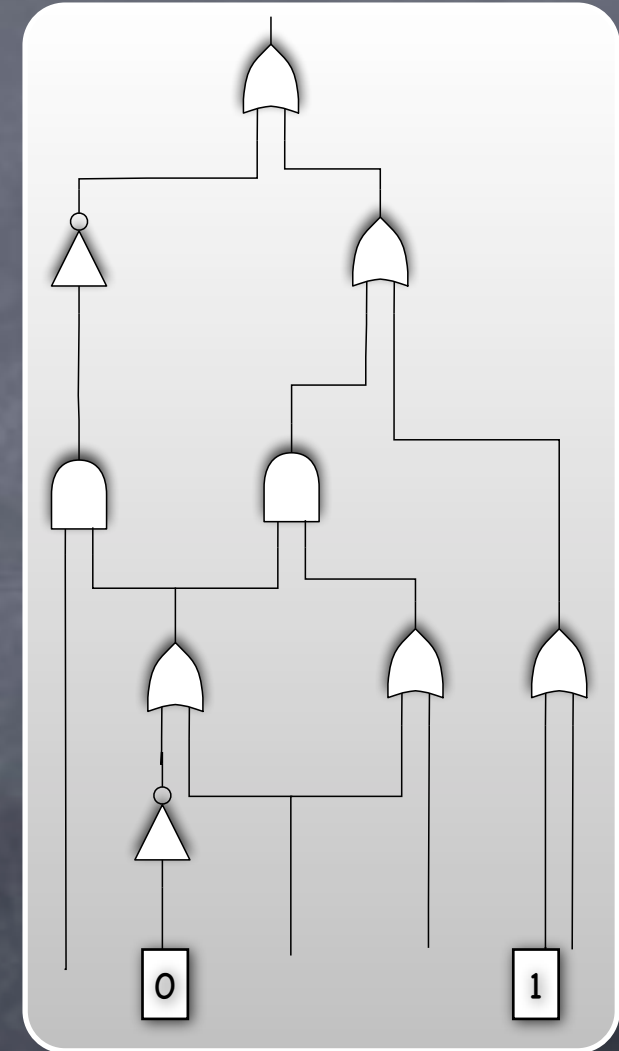
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 - Nodes: AND, OR, NOT, CONST gates, inputs, output(s)



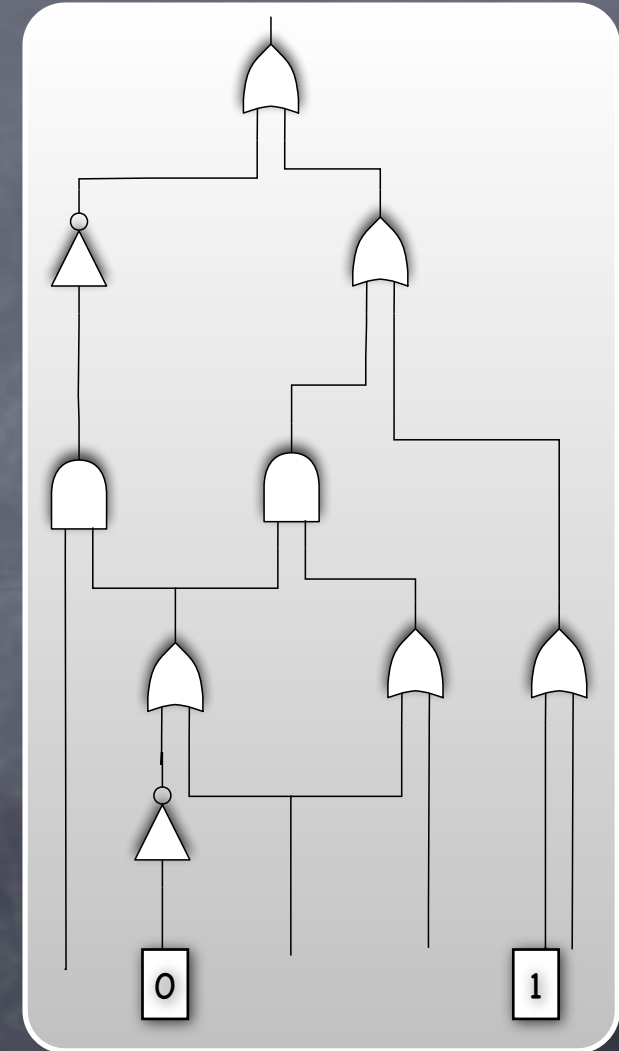
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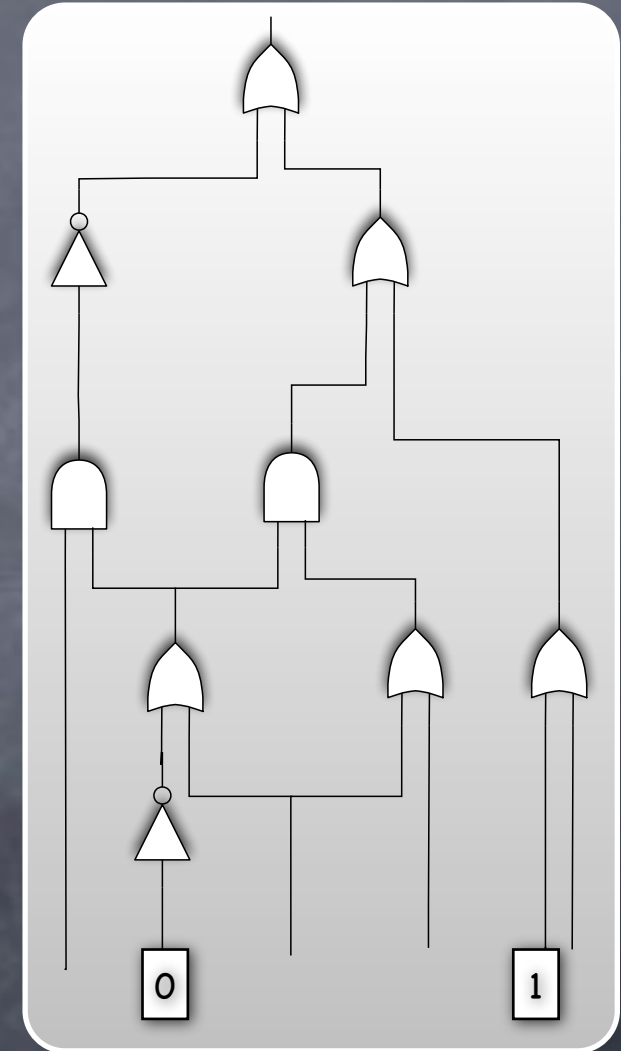
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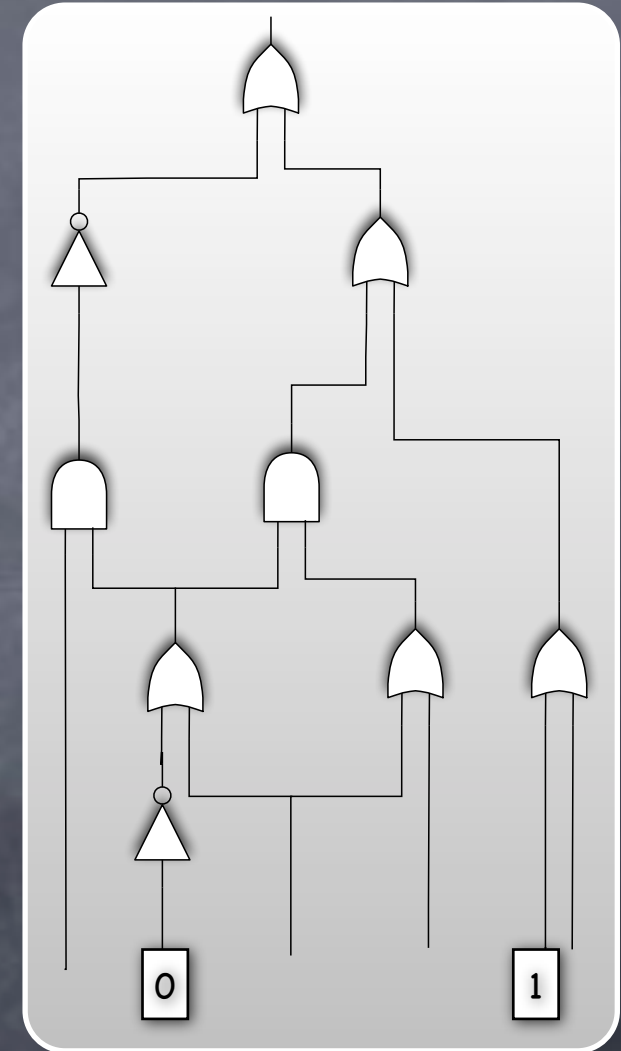
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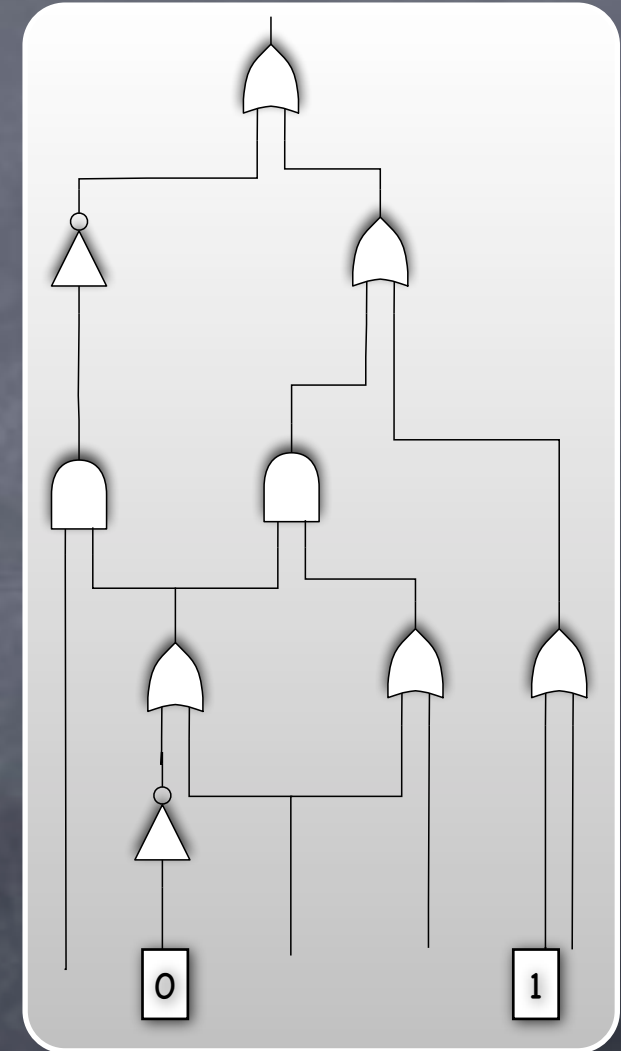
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 - Note: no memory gates



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	00	01	10	11
00	0	0	0	0
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 - Often problems already described as succinct programs/circuits

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 - Any ideas?

A Physical Protocol

	0	1
0	0	1
1	1	1

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- Alice prepares 4 boxes B_{xy} corresponding to 4 possible input scenarios, and 4 padlocks/keys $K_{x=0}$, $K_{x=1}$, $K_{y=0}$ and $K_{y=1}$

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11
1

00
0

10
1

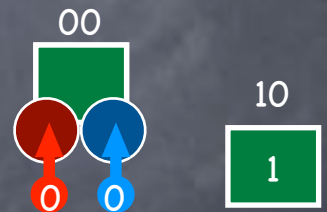
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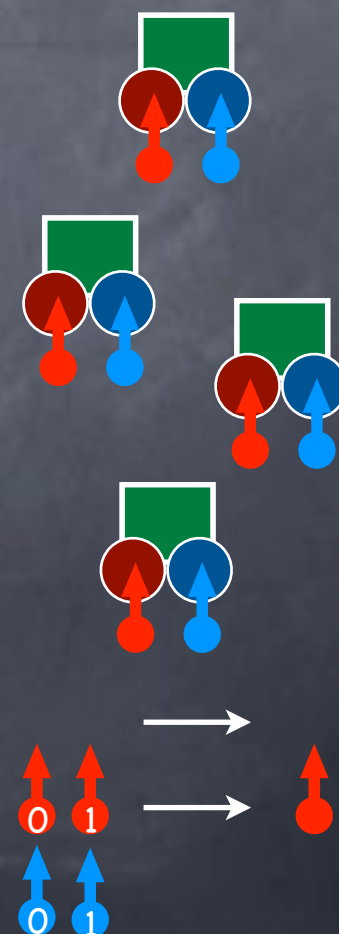
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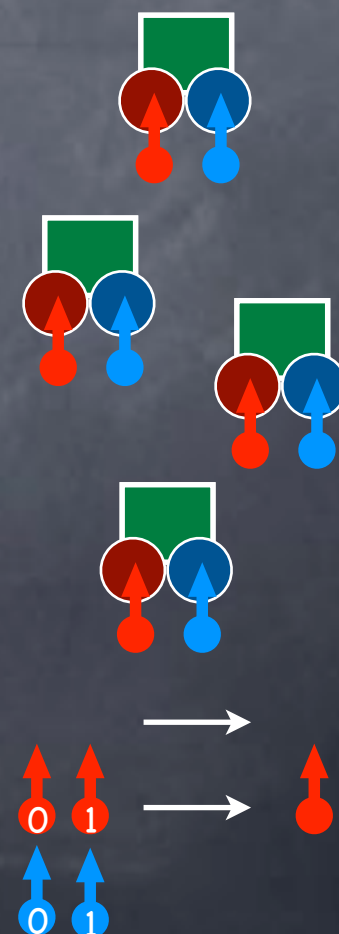
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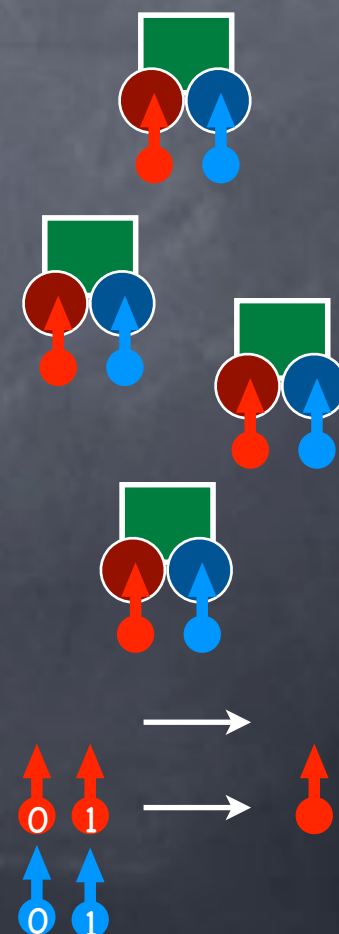
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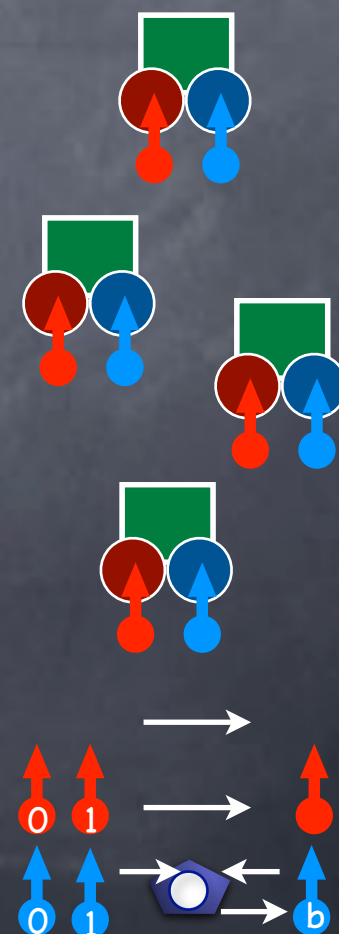
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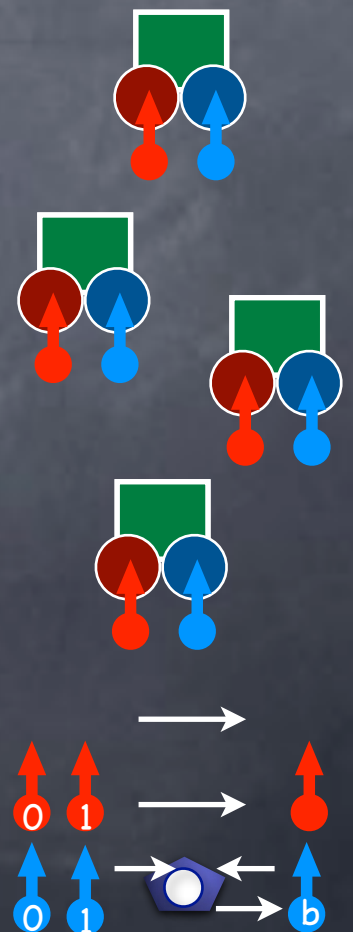
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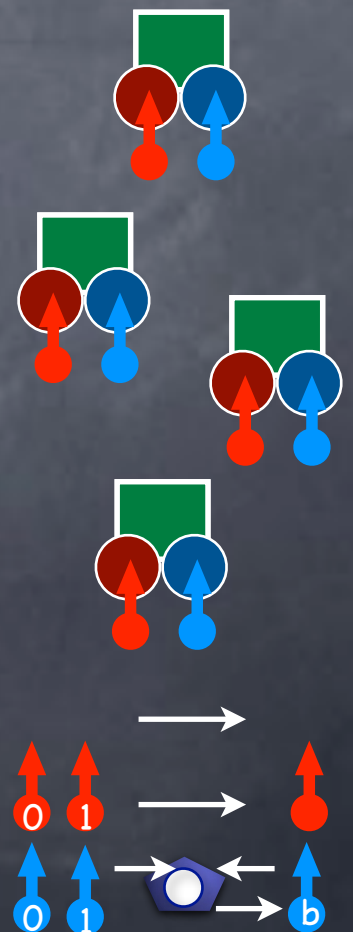
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A Physical Protocol

Secure?

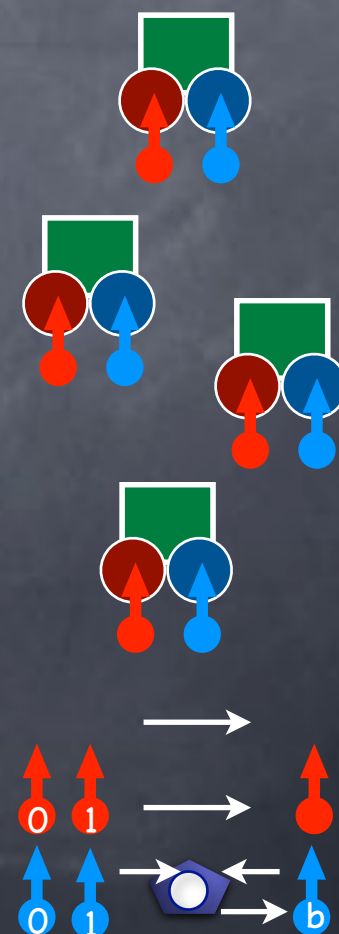
	0	1
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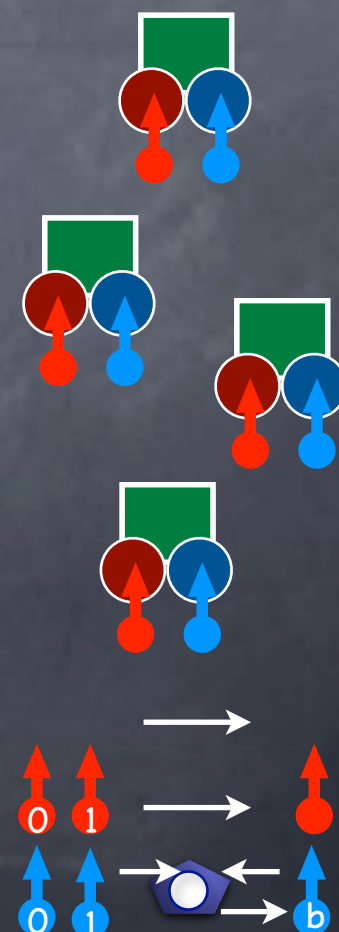
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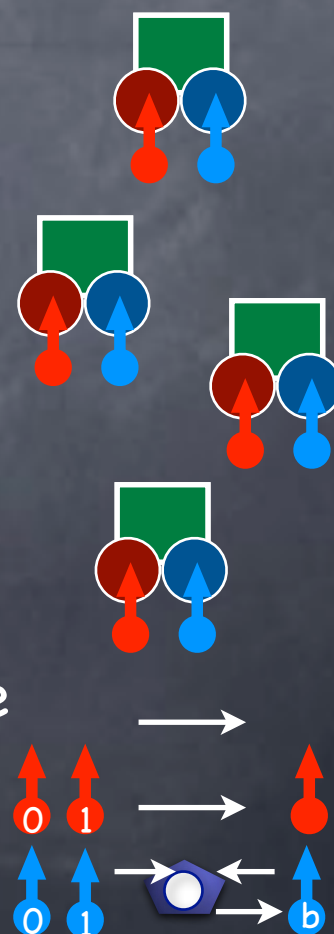
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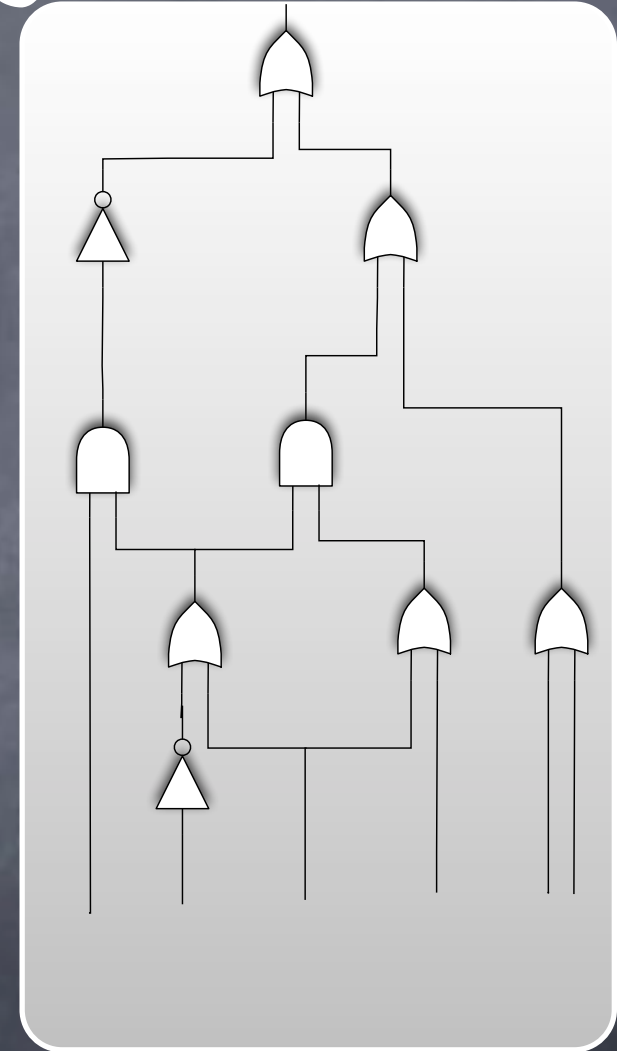
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 - Formally, easy to simulate (can stuff unopenable boxes randomly)

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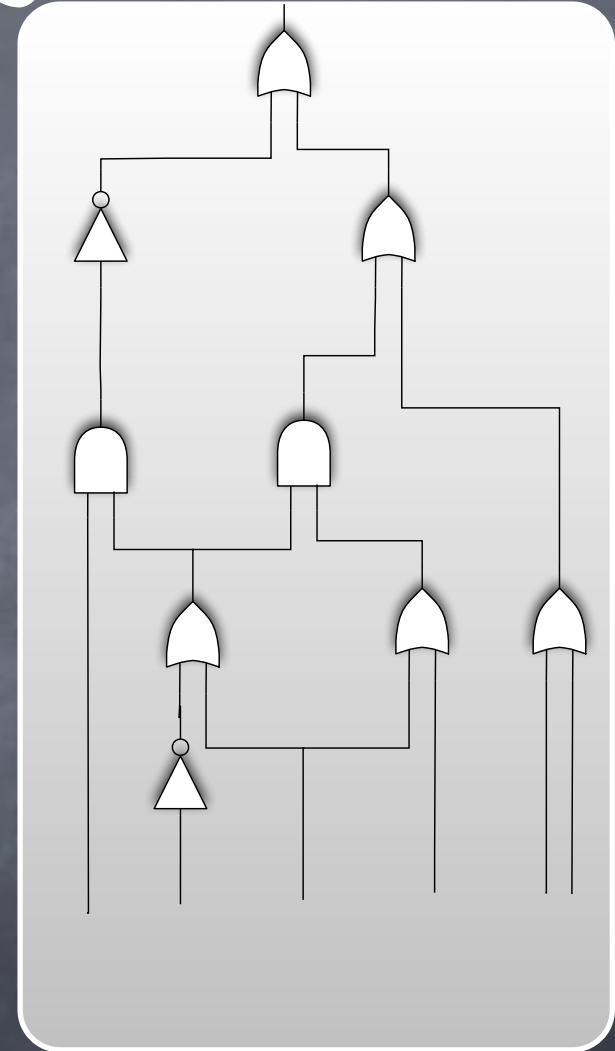
Larger Circuits

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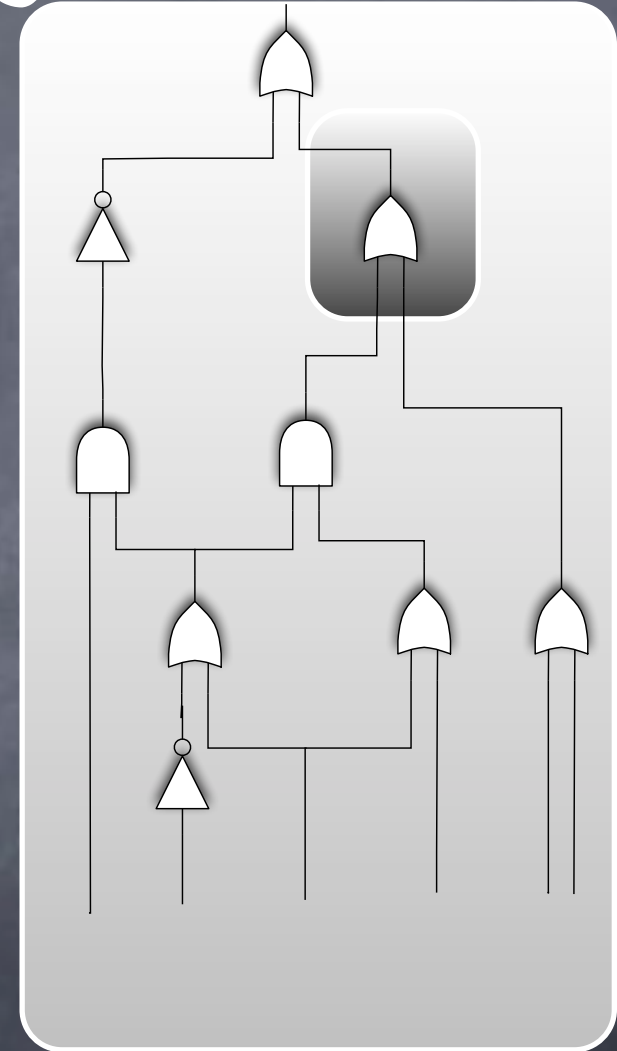
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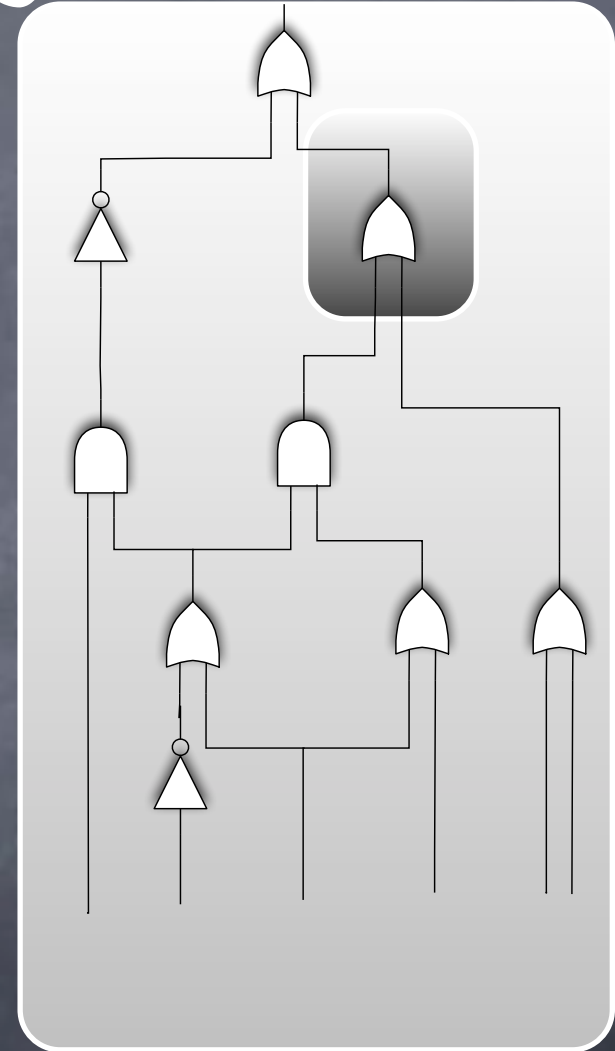
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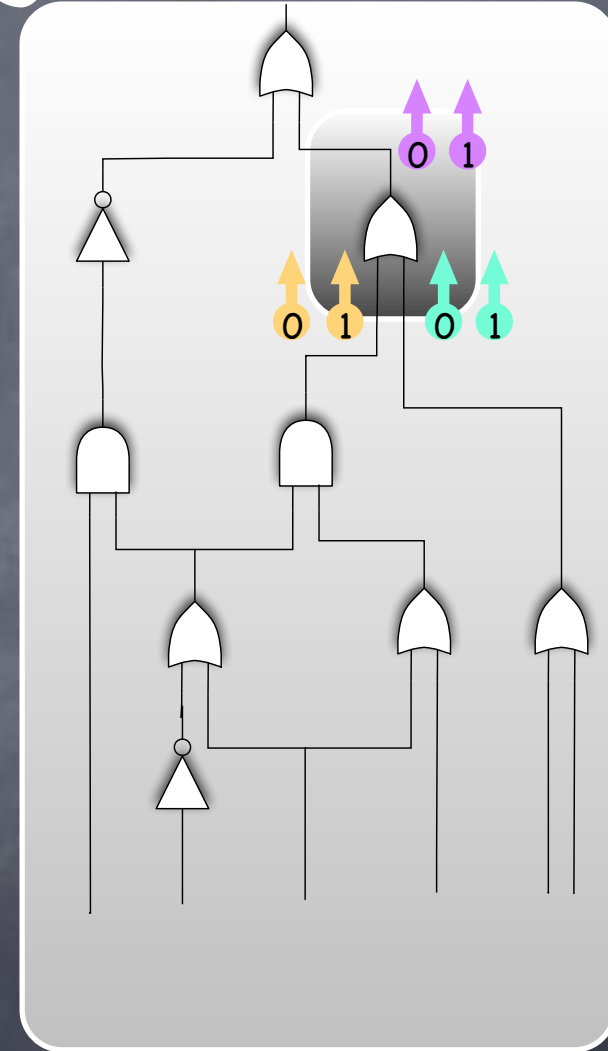
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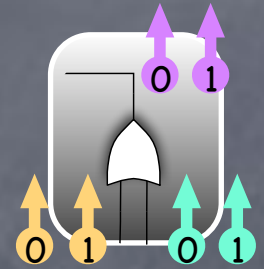
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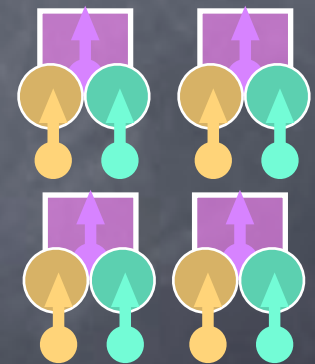
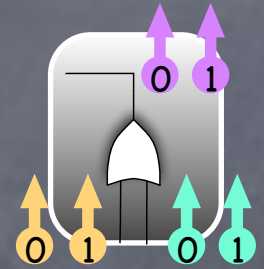
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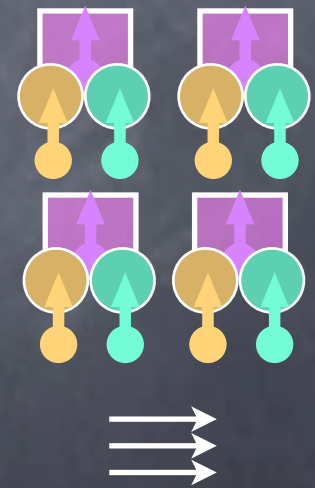
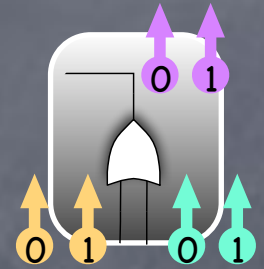
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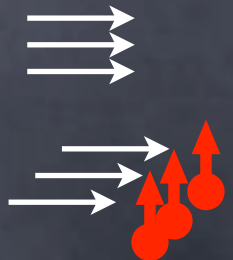
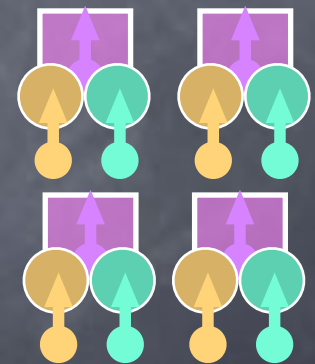
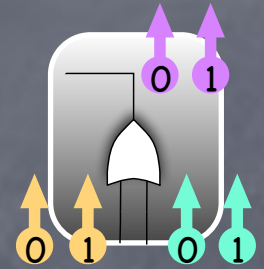
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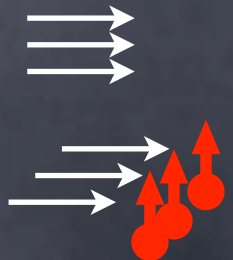
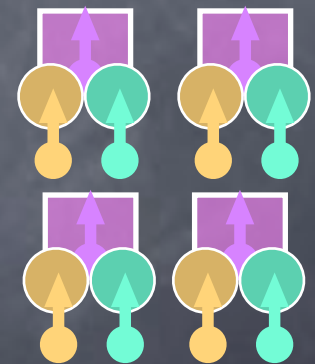
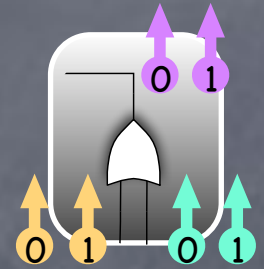
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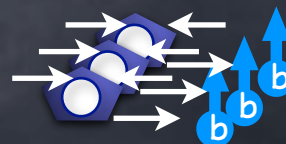
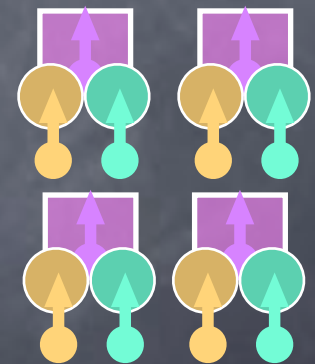
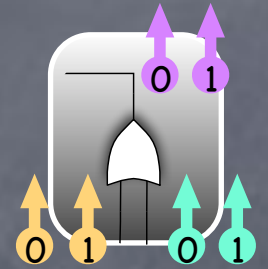
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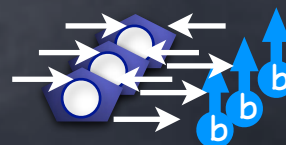
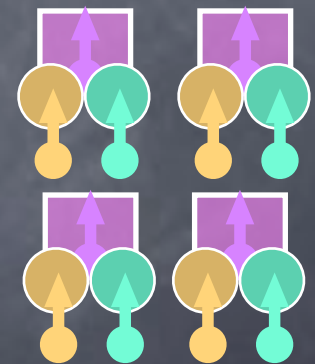
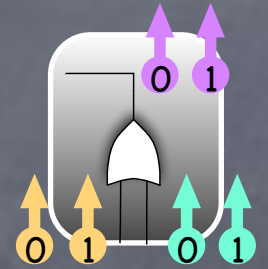
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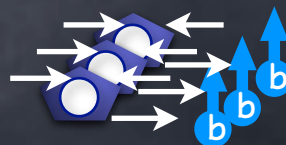
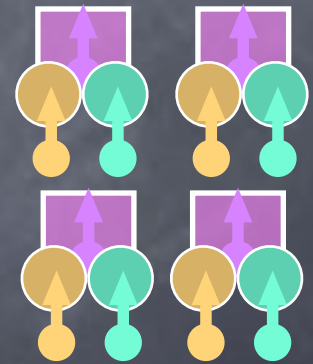
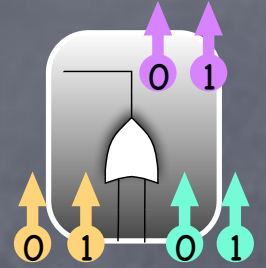


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- Boxes for output gates have values instead of keys

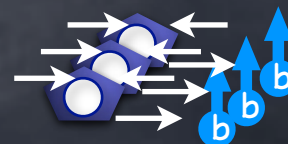
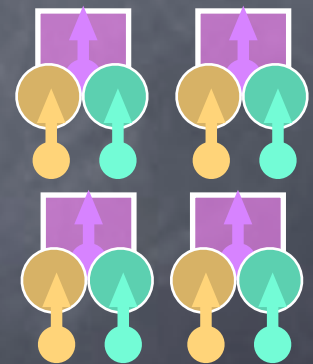
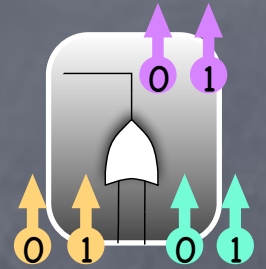


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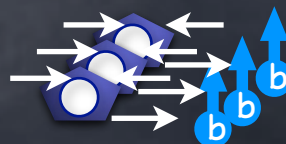
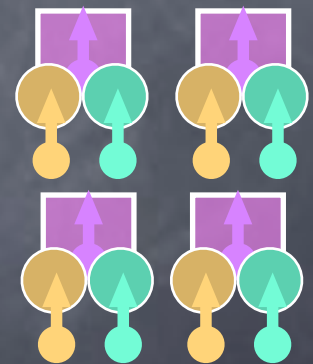
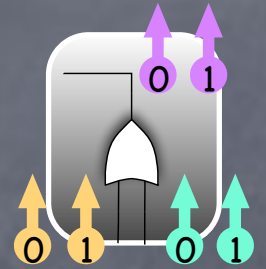
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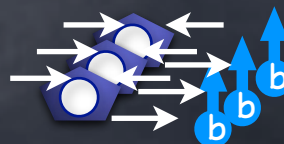
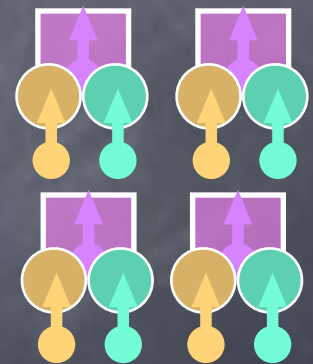
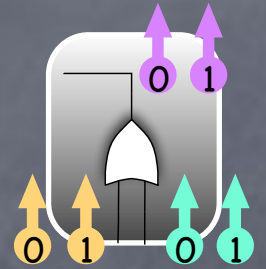
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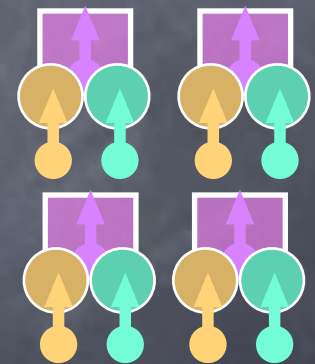
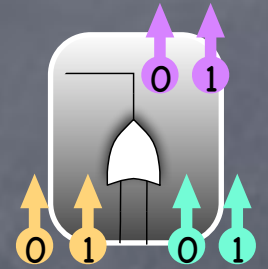
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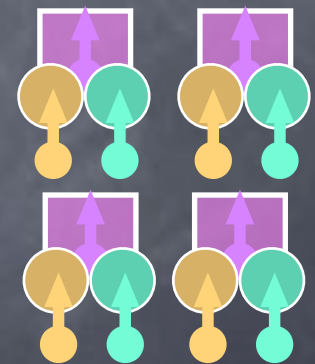
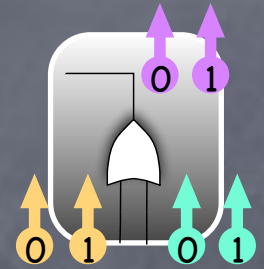
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- Everything is simulatable for curious Bob given final output: Bob could prepare boxes and keys (stuffing unopenable boxes arbitrarily); for an output gate, place the output bit in the box that opens



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 - Can we really compose? Yes, for passive security.

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 - Composition (implicitly)
- Coming up: Zero-Knowledge proofs and general multi-party computation, more protocols (for different settings).
Universal Composition