

Circuit Lower-bounds

Lecture 25

Weak circuits are indeed weak

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- **PARITY** $\notin AC^0$

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- Today:
 - **PARITY $\notin AC^0$**
 - Two different proofs! (Latter generalizes to ACC^0)
 - **CLIQUE cannot be decided by poly-sized monotone circuits**
- (Only sketches/partial proofs. See textbook or lecture-notes from linked courses)

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 - How shallow can a poly-sized circuit family for PARITY be?

A Switching Argument

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 - By “restricting” to n inputs
 - And showing how to rewrite with depth $d-1$, staying poly sized

AND-OR trees

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- Any circuit can be rewritten as an AND-OR tree (each leaf has a literal, possibly shared with other leaves)
 - If polynomial size and constant depth (AC^0), stays so

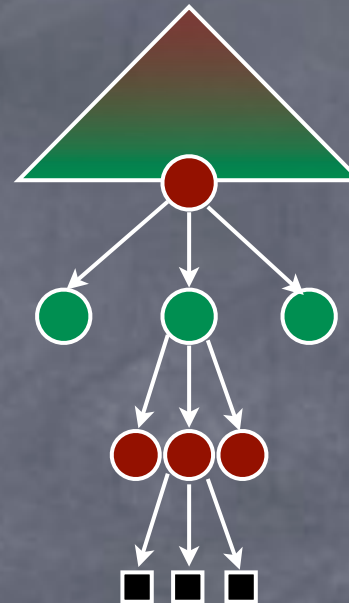
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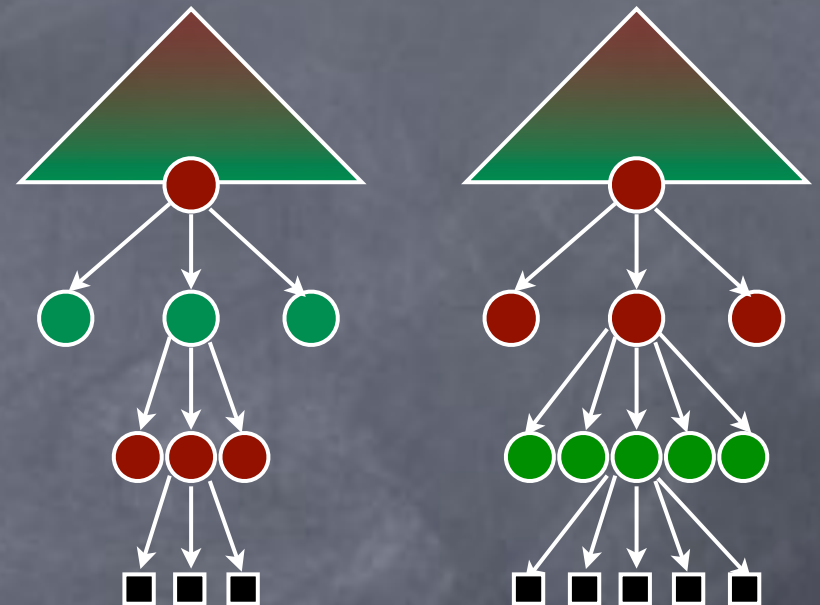
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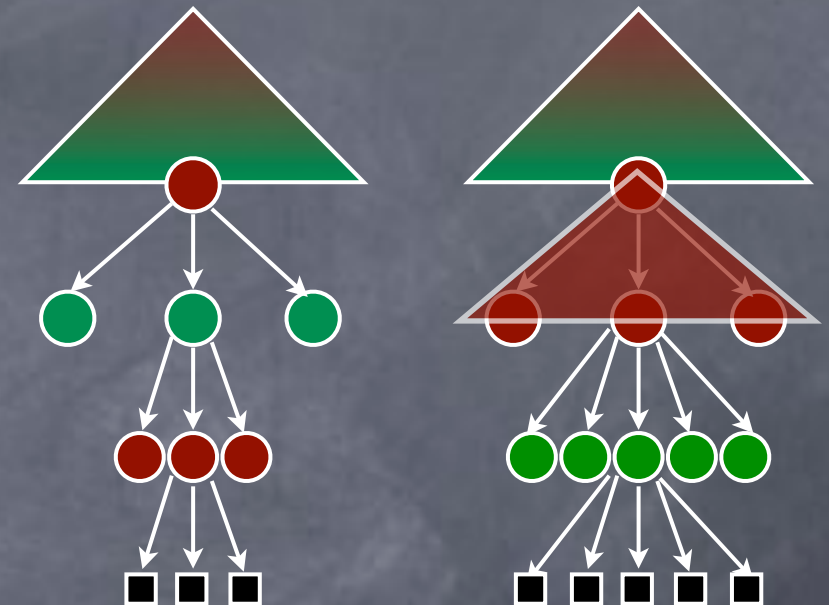
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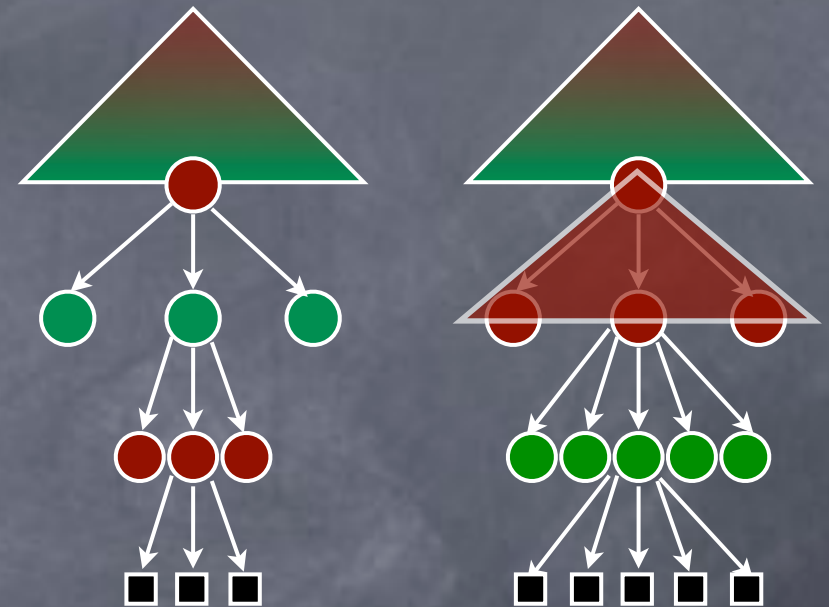
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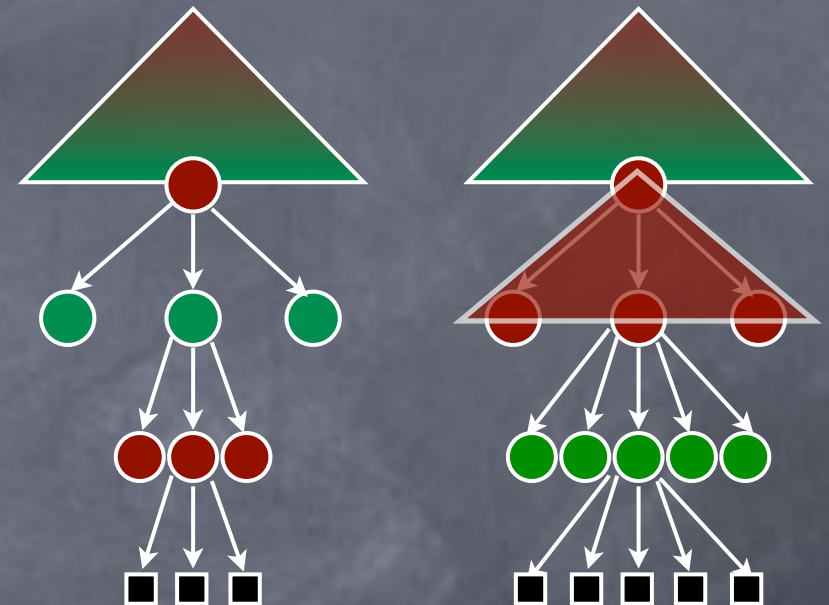
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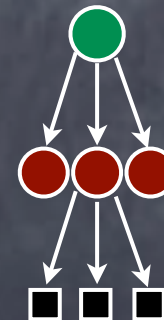
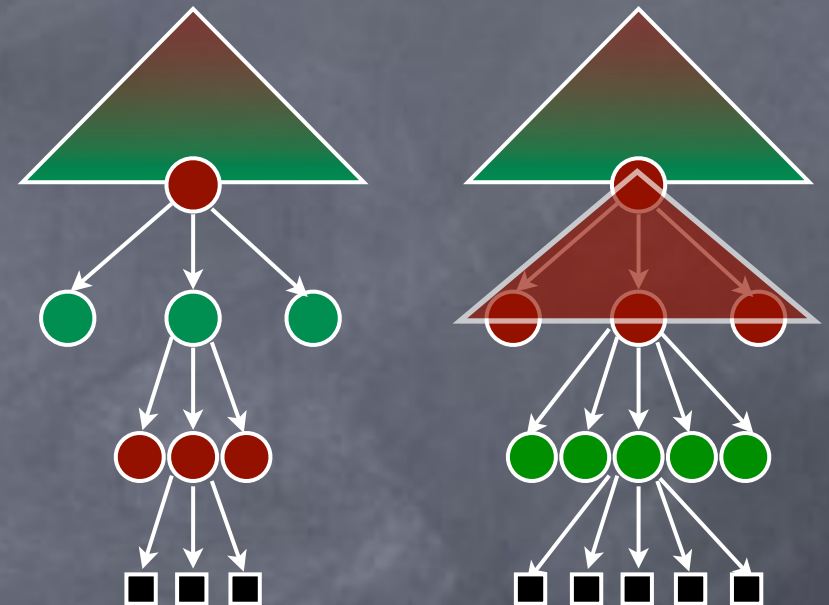
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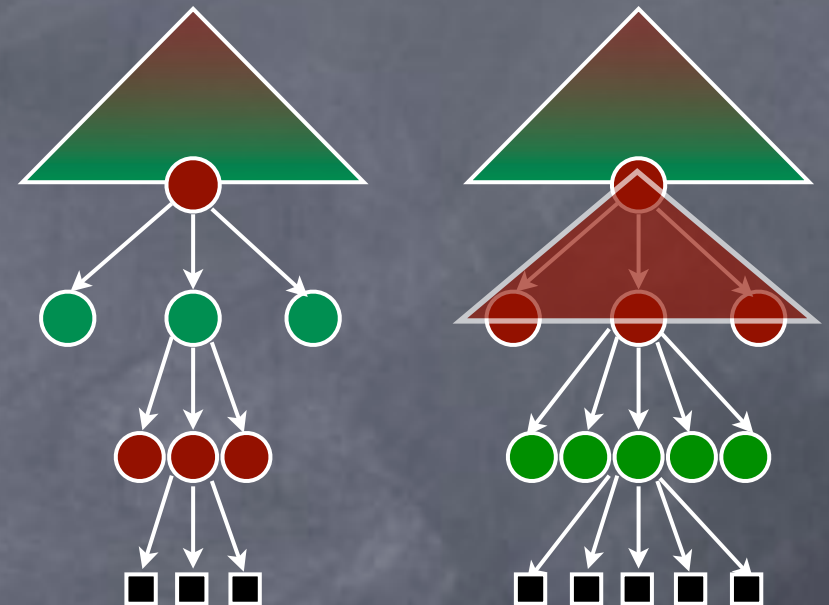
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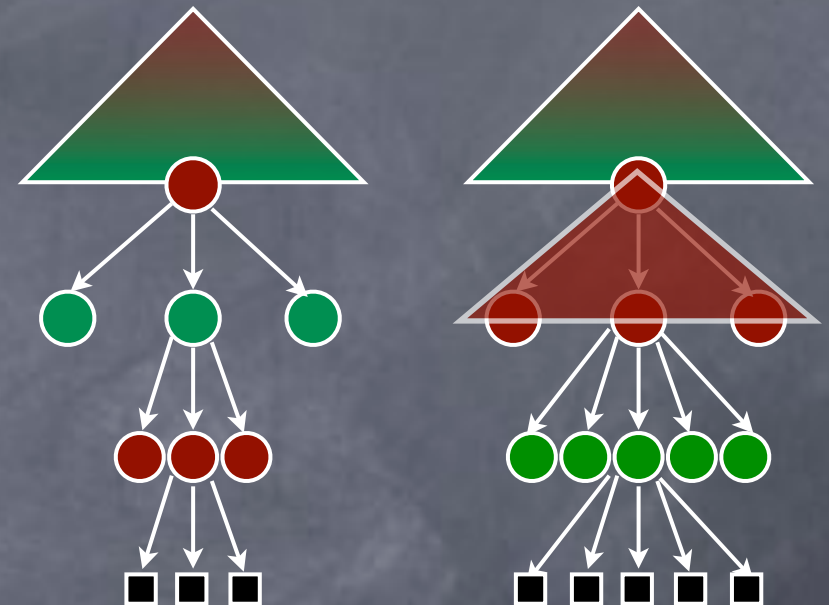
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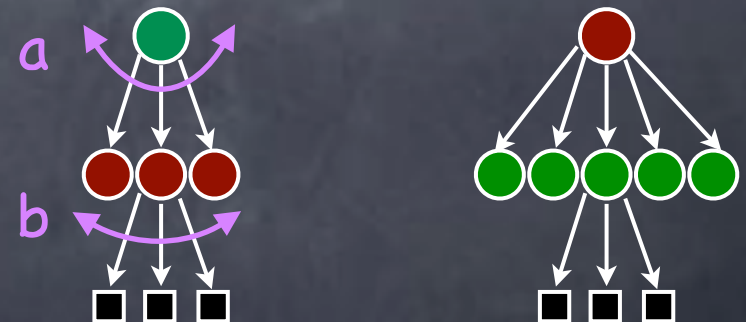
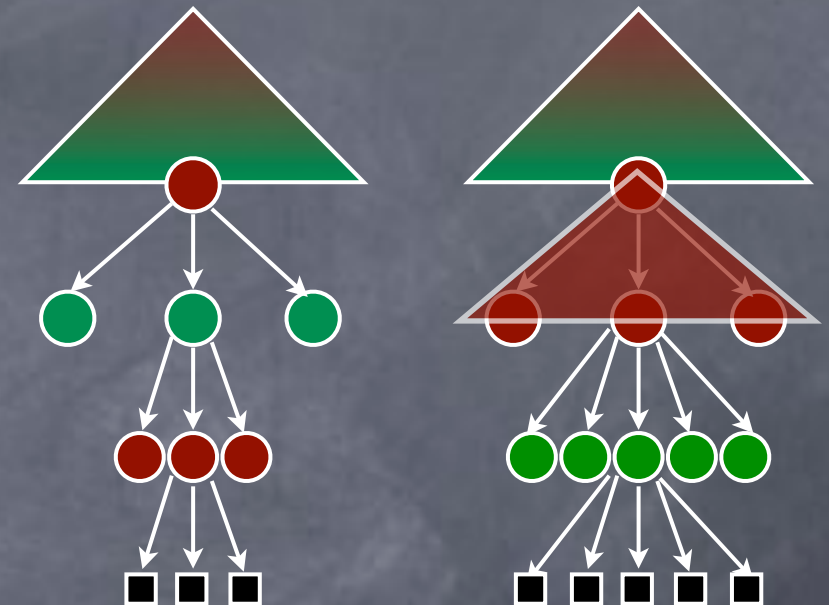
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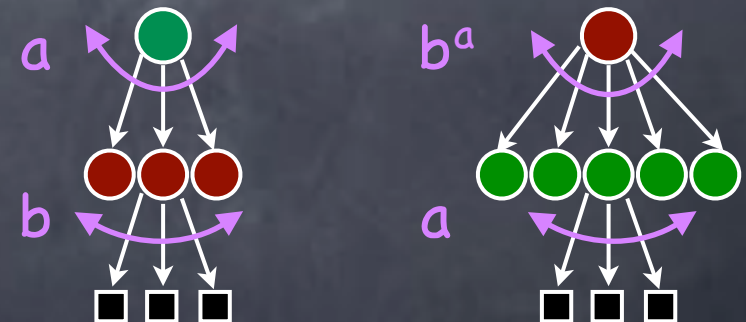
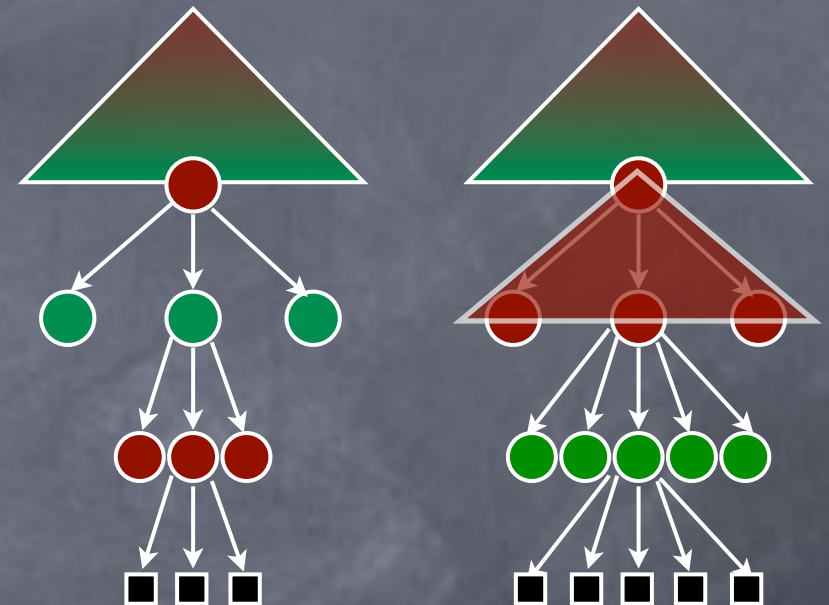
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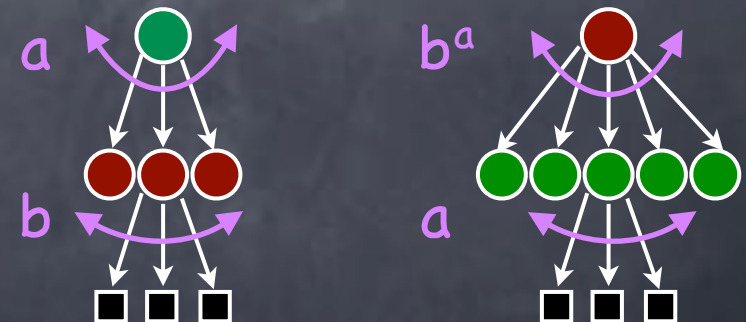
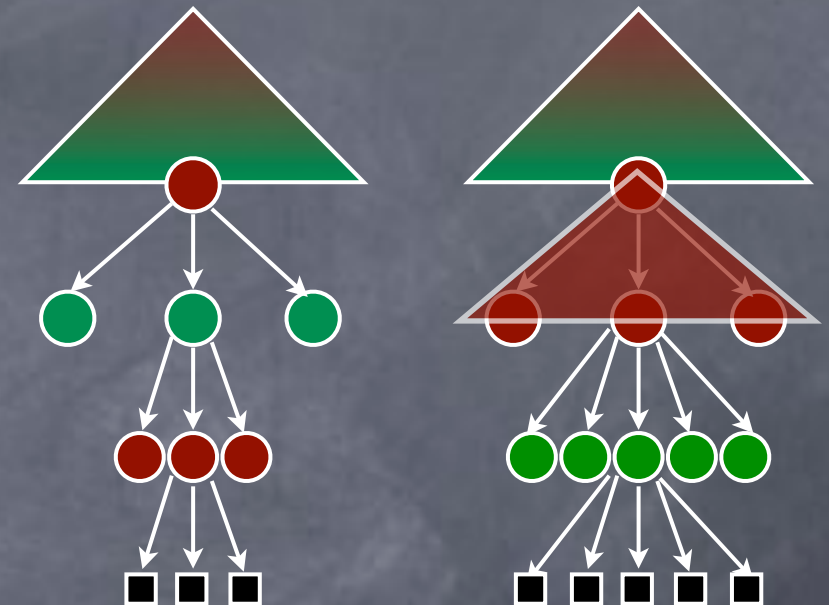
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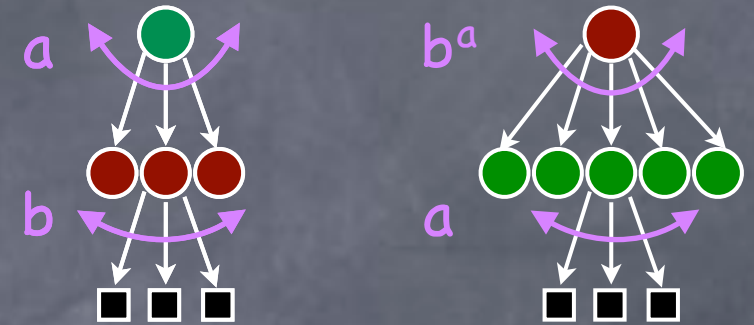


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 - But may increase size to exponential

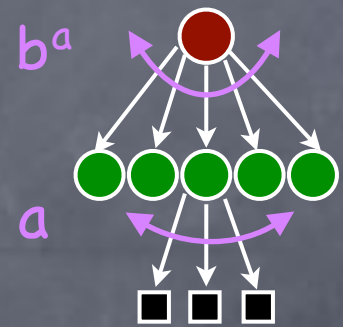
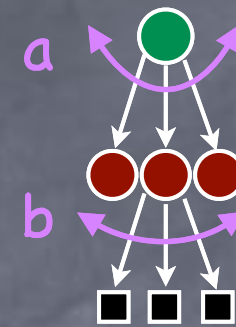


Switching Lemma



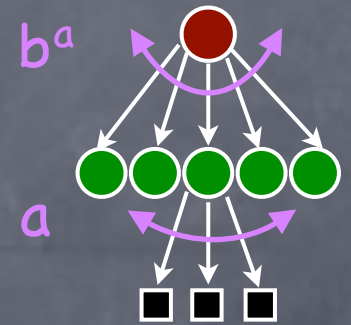
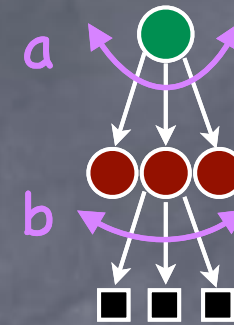
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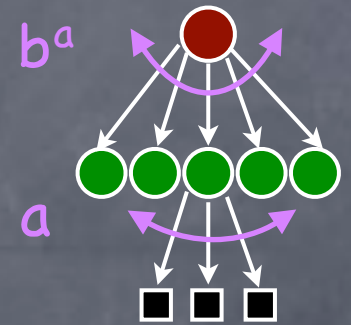
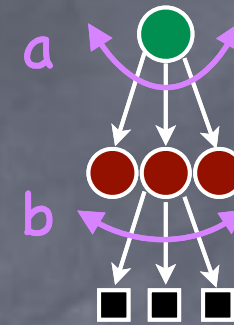
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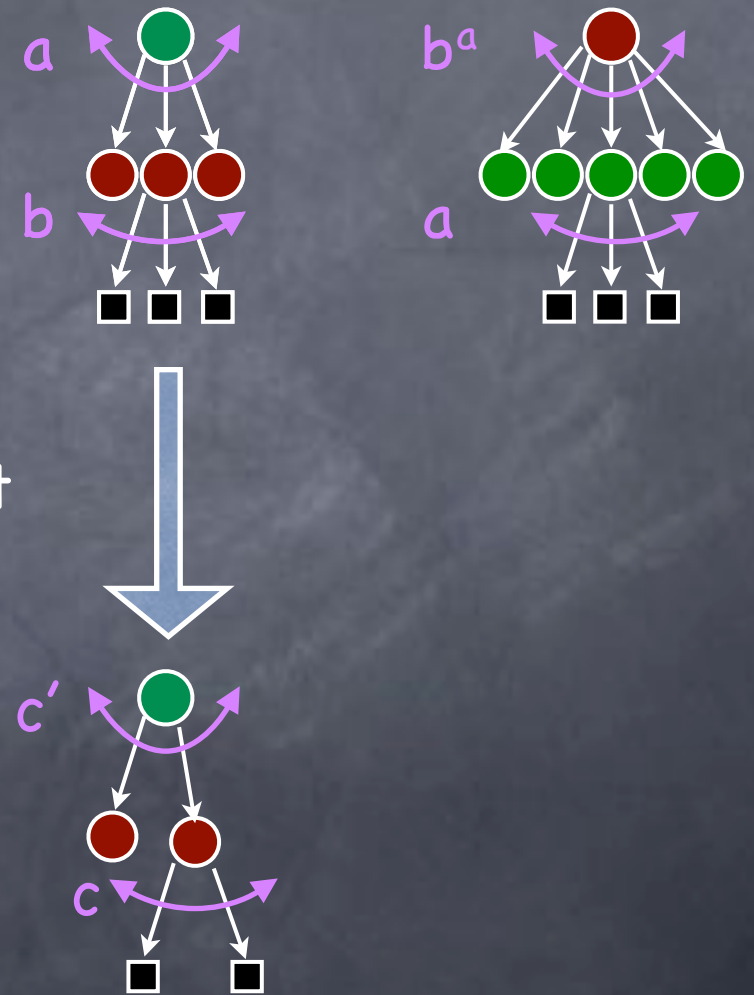
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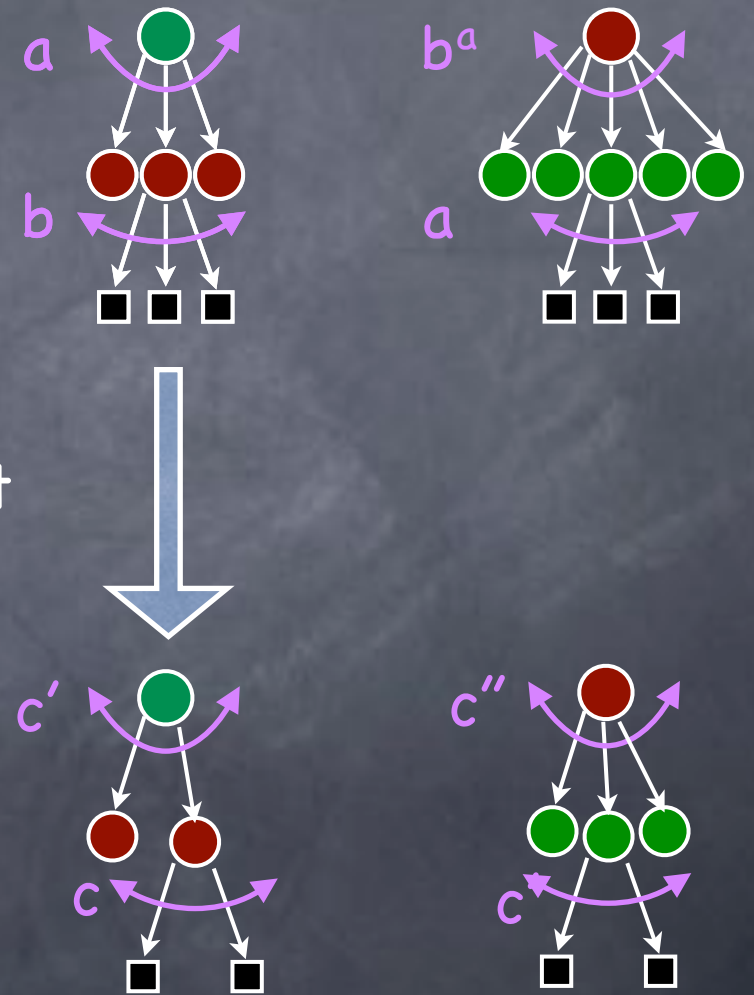
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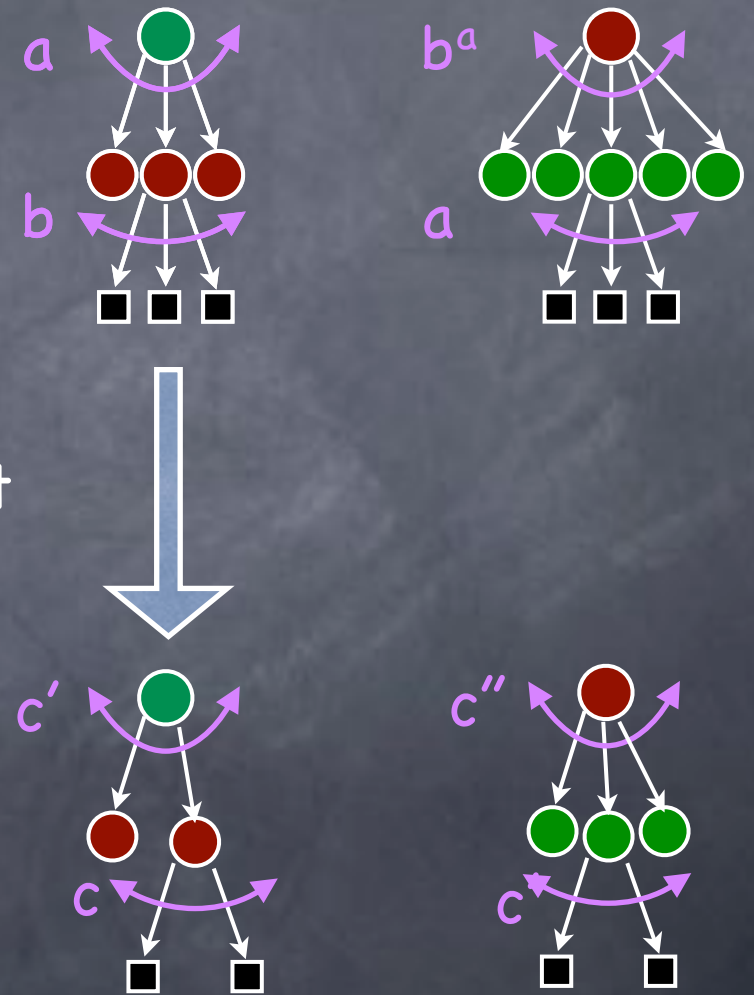
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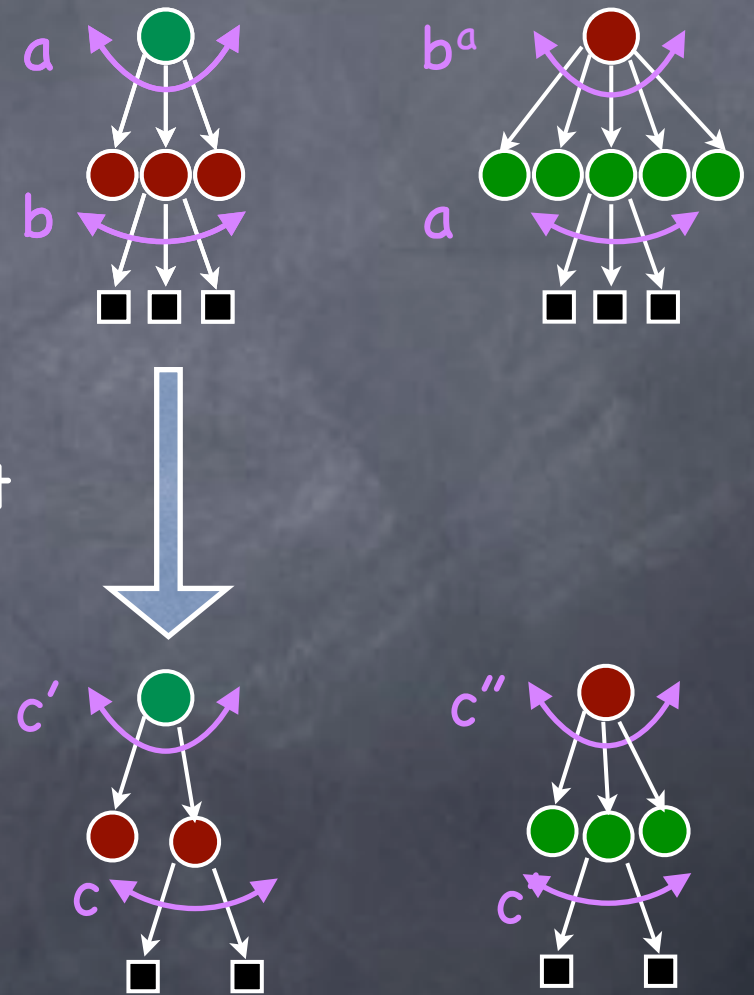
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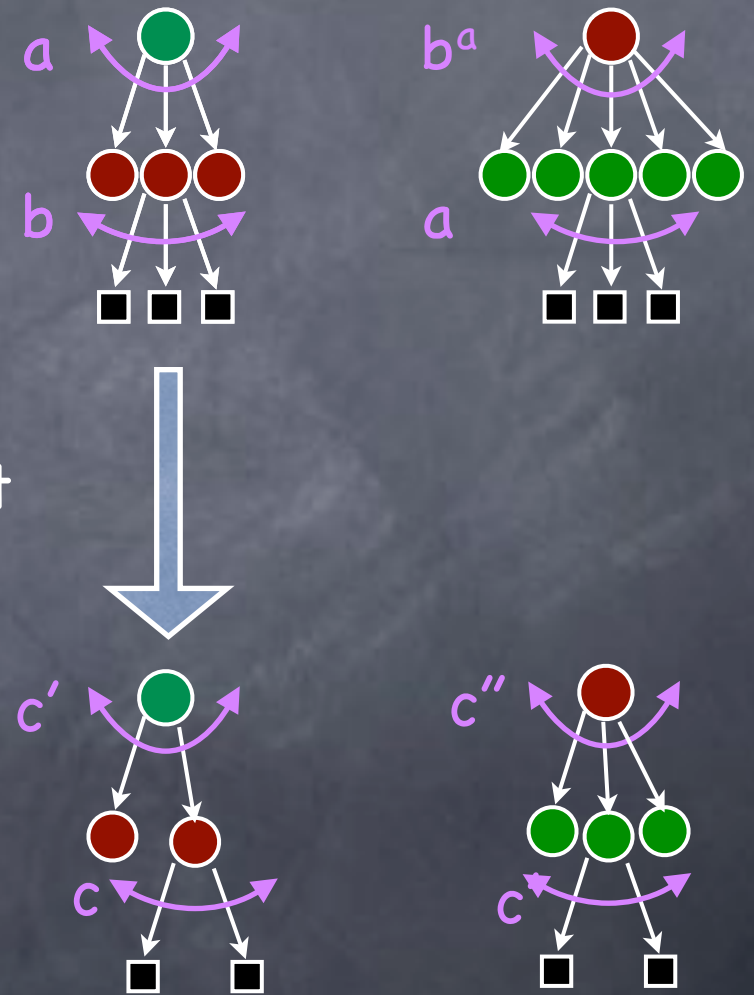
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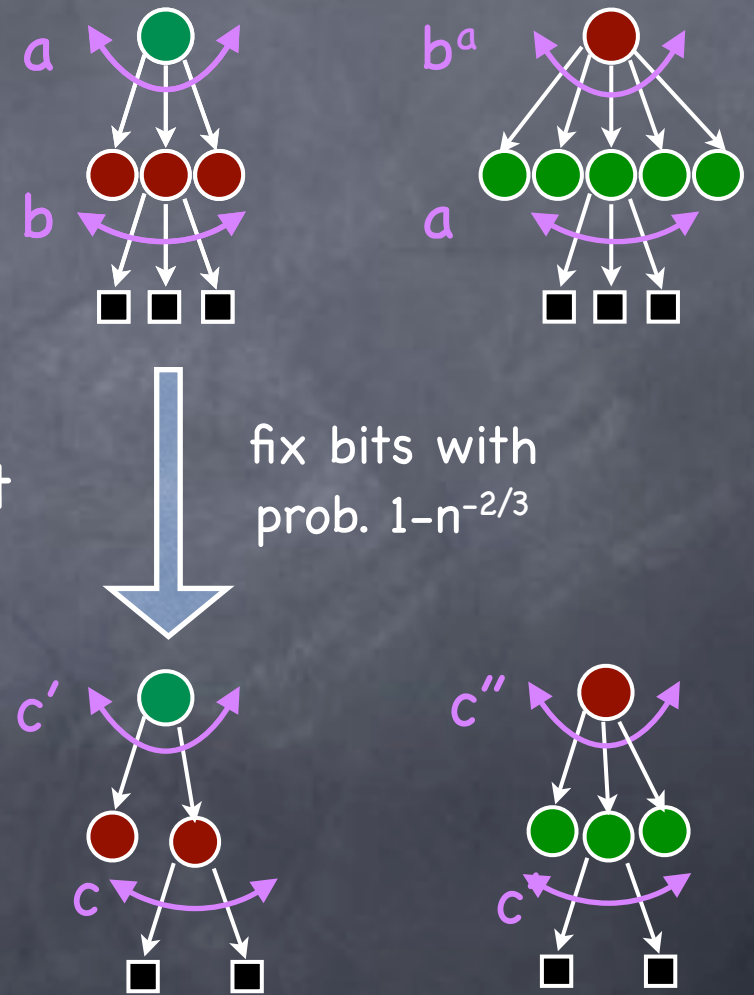
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 - Contradiction: started with minimal depth!

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 - Show that no low degree polynomial can agree with PARITY on that many inputs

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 - But high degree! Need OR to be simple!

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 - i.e. $\Pr_a[(a_1x_1 + \dots + a_nx_n)^{q-1} = 1] \geq 1 - 1/q$
- Can boost probability by doing (exact) OR t times: $\deg < qt$

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- Using $\epsilon=1/(4s)$, **degree $O(\log s)^d$ polynomial**, correct w.p. $> 3/4$

Approximate Polynomials

- OR: a random polynomial of **degree $O(\log 1/\epsilon)$** , such that it is correct with **prob. $> 1-\epsilon$**
- Composing gate-polynomials into circuit-polynomial
 - Substitute child polynomials as variables
 - Degree multiplies: depth d circuit gives deg **$O(\log 1/\epsilon)^d$**
 - Error adds (by union bound): size s circuit gives **error $< s\epsilon$**
- Using $\epsilon=1/(4s)$, **degree $O(\log s)^d$ polynomial**, correct w.p. $> 3/4$
 - One polynomial, **correct on $> 3/4$ fraction of inputs (why?)**

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 - Need $D = \Omega(\sqrt{n})$ to have enough degree $D+n/2$ polys.

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- PARITY needs degree $\Omega(\sqrt{n})$ polynomial for approximation
- $\log(s) = \Omega(\sqrt{n})^{1/d}$ or $s = 2^{\Omega(n)^{1/2d}}$: i.e., if depth is constant then size not poly (in fact exponential)

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 - Still possible that f may have a more efficient non-monotone circuit family (or even be in P)

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 - Pruning uses “sunflower lemma”: find a sunflower and replace petals by core

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- A circuit with only approximation gates errs on a large number of the examples
 - If output identically “No” then errs on entire Yes set. Else, output wire’s label has some subset X , $|X| \leq t = O(\sqrt{k})$, and then a constant fraction of No-examples get accepted