Lecture 10 Non-Uniform Computational Models: Circuits

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- Non-uniform: A different "program" for each input size
 - Then complexity of building the program and executing the program
 - Sometimes will focus on the latter alone
 - Not entirely realistic if the program family is uncomputable or very complex to compute

Program: TM M and advice strings {A_n}

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But $\{A_n\}$ can be uncomputable (even if just one bit long)

e.g. advice to decide undecidable unary languages

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P/log = $\bigcup_{c,k>0}$ DTIME(kn^c)/k log n

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Does P/log or P/poly contain NP?

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Use L₂ so that (x,z,pad)

in L₂ iff (x,z) in L₁. Can

query L₂ with same size

instances

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- Guess advice (poly many), and for each guessed advice, run the TM and see if it finds witness
- If no advice worked (one of them was correct), then input not in language

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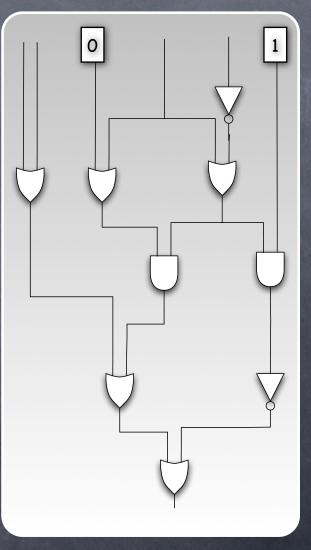
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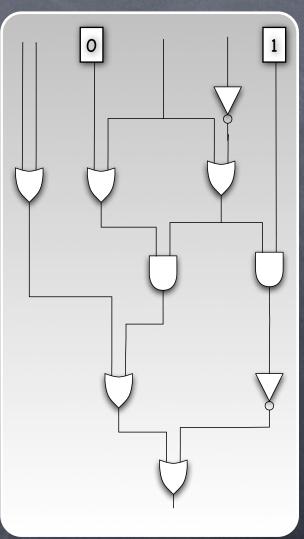
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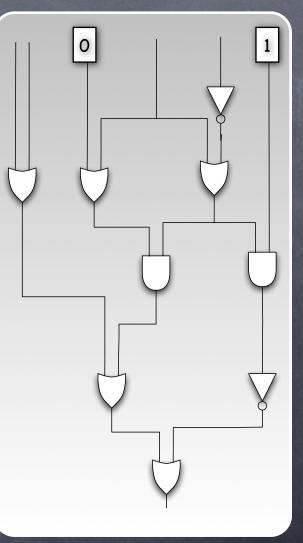


Directed acyclic graph



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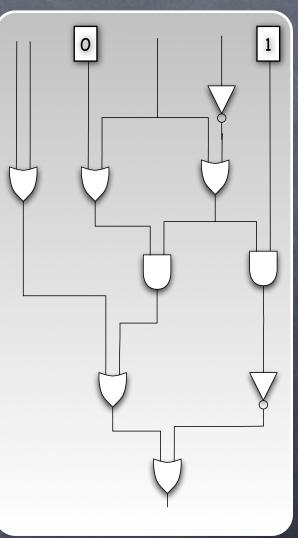
Nodes: AND, OR, NOT, CONST gates, inputs, output(s)



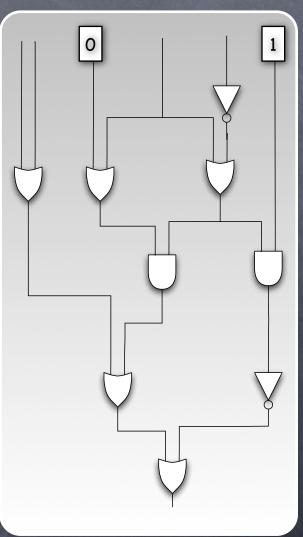
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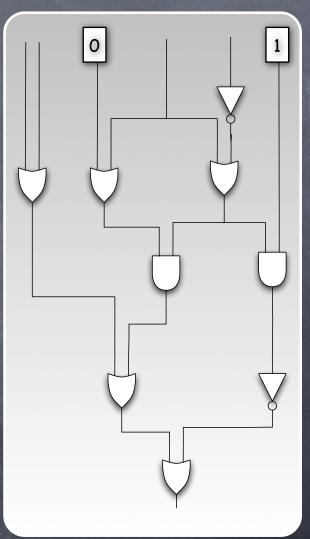
Sedges: Boolean valued wires



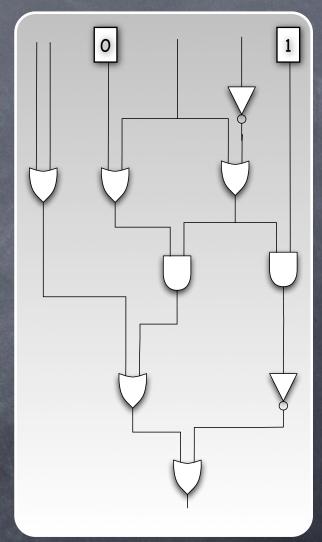
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 - Nodes: AND, OR, NOT, CONST gates, inputs, output(s)
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 - AND/OR fan-ins can be bounded (say two) or unbounded



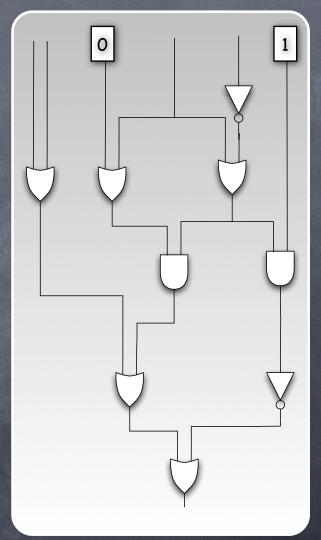
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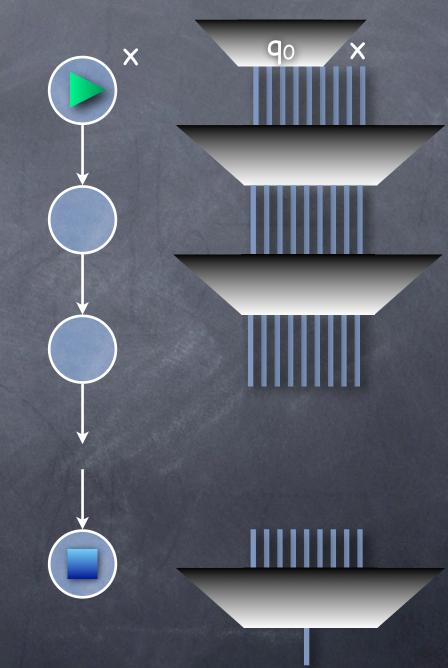


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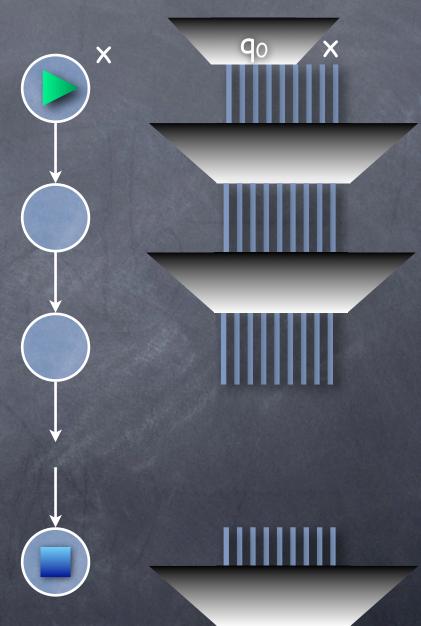


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 - Size of circuit: number of wires





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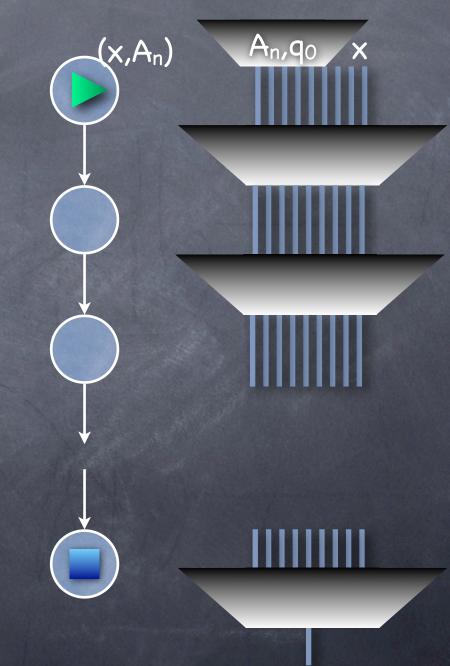
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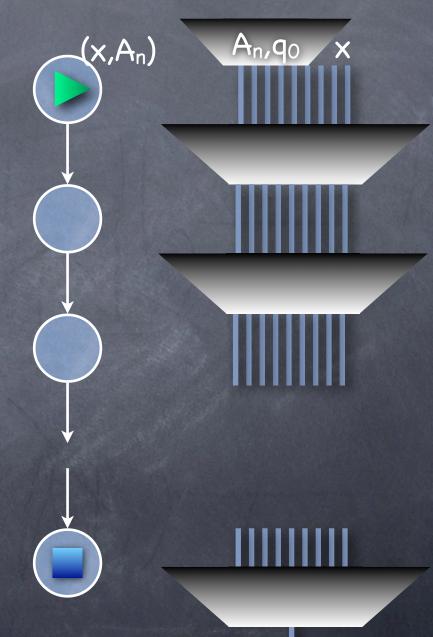
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 - If poly time TM, then poly sized circuit



 \mathbf{q}_0

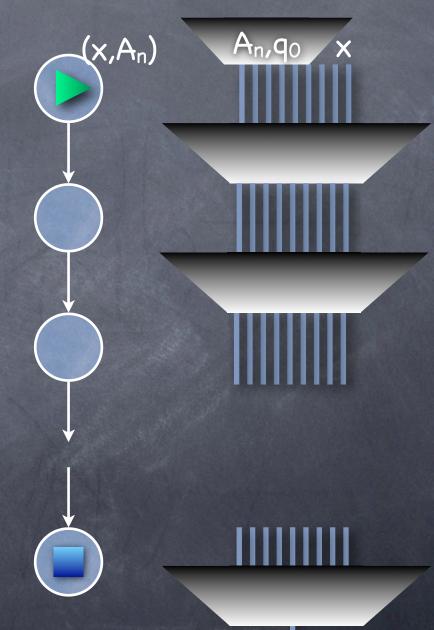
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Non-uniformity: circuit family {C_n}

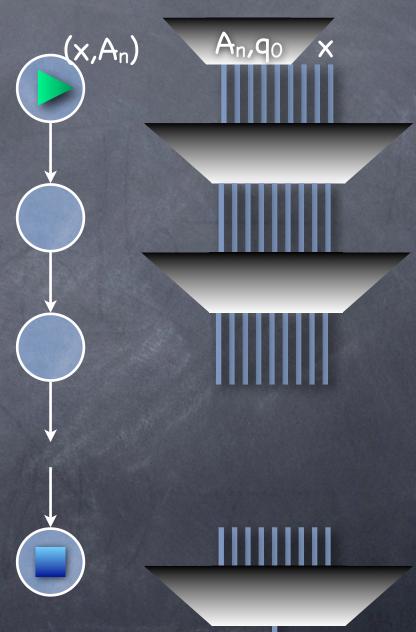
Given non-uniform computation
 (M,{A_n}) can define equivalent {C_n}



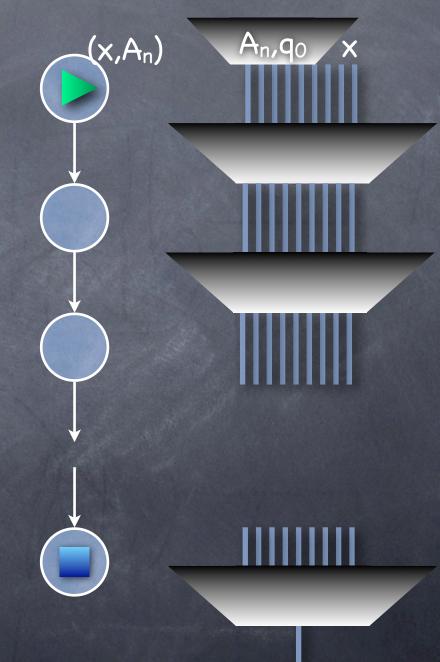
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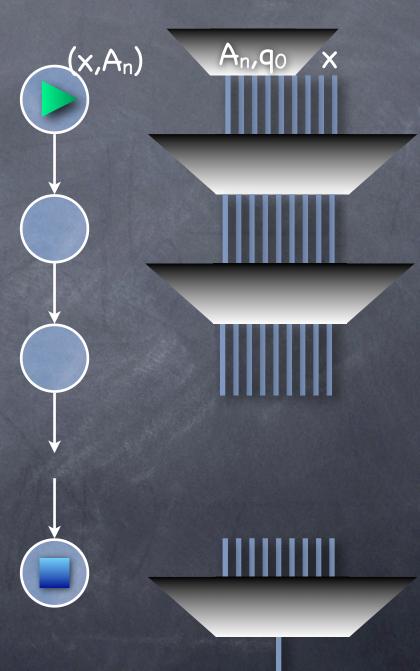
Advice An is hard-wired into circuit Cn



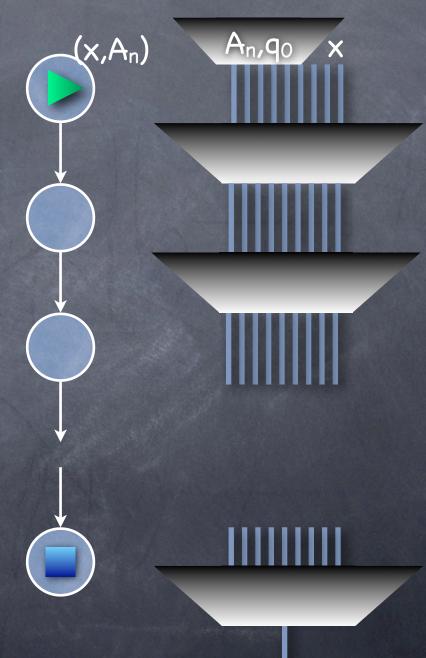
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 - P/poly ⊆ SIZE(poly): Transformation from Cook's theorem, with advice string hardwired into circuit

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 All of them in SIZE(T), most not in SIZE(T')

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Uniform circuit family: constructed by a TM

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Logspace-uniform:

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Logspace-uniform:

An O(log n) space TM can compute the circuit

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• Similarly AC^{i} and $AC = \bigcup_{i>0} AC^{i}$

Olearly NCⁱ ⊆ ACⁱ

⊘ Clearly $NC^i ⊆ AC^i$

 ACⁱ ⊆ NCⁱ⁺¹ because polynomial fan-in can be reduced to constant fan-in by using a log depth tree

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So NC = AC

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Build the circuit in logspace (so poly time) and evaluate it in time polynomial in the size of the circuit

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Open problem: Is NC = P?

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Total "work" is size of the circuit