Computational **Complexity**

Lecture 4 in which Diagonalization takes on itself, and we enter Space Complexity (But first Ladner's Theorem)

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Can show an NP language which is neither in P, nor NP complete (unless P = NP)

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To define H s.t. $H(n)$ bounded by const. iff SAT H in P \bigcirc

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- \bullet H(n) be least i < log log n s.t. M_i/T_i correct for all |z|<log r
- H is poly-time computable
- \circ SAT_H in P iff H(n) < i*
	- **Both equivalent to having** a row of all ☑

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Under-the-hood stuff
What if we had an oracle for language A

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Said to "relativize "

How does P vs. NP fare relative to different oracles?

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How does P vs. NP fare relative to different oracles? Does their relation (equality or not) relativize? No! Different in different worlds! \circ There exist languages A, B such that $P^A =$ NP^A, but $P^B \neq NP^B!$

A s.t. P^A = NP^A

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\overline{A} s.t. $P^A = NP^A$

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A simple EXP-complete language:

 \odot EXPTM = { (M,x,1ⁿ) | TM represented by M accepts x within time 2^n }

$B s.t. P^B \ne NP^B$

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- After Mi finished set B up to $x=1^n$ s.t. $L(1^n) \neq M_i^{B}(1^n)$

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e.g. of non-relativizing proof: that of Cook-Levin theorem

Natural complexity question

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How much memory is needed

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Natural complexity question How much memory is needed More pressing than time: **Examel generate memory on the fly**

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Natural complexity question How much memory is needed More pressing than time: Can't generate memory on the fly Or maybe less pressing: Turns out, often a little memory can go a long way (if we can spare the time)

Measure of working memory (work-tape) used by a TM/NTM: input kept in a read-only tape

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Constant factor (+O(log n)) simulation overhead

Non-deterministic M

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input: x

- Non-deterministic M
- input: x
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$L \in \mathsf{NSPACE}(S)$: Two Equivalent views

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Deterministic M' \circledcirc

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- makes non-det choices
- $\alpha \times \epsilon$ L iff some thread of M accepts
- in at most S(|x|) space
- Deterministic M'
- input: x and read-once w

- Non-deterministic M
- input: x
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Deterministic M' input: x and read-once w reads bits from w (certificate)

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- in at most S(|x|) space
- Deterministic M' input: x and read-once w reads bits from w (certificate) \circ $x \in L$ iff for some cert. w, M' accepts
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L = DSPACE(O(log n))

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L = DSPACE(O(log n)) $O L = U_{a,b > 0}$ DSPACE(a.log n+b) NL = NSPACE(O(log n))

L = DSPACE(O(log n)) \odot $\mathsf{L} = \bigcup_{a,b \geq 0} \mathsf{DSPACE}(a.log~n+b)$ NL = NSPACE(O(log n)) \odot NL = $U_{a,b}$, o NSPACE(a.log n+b)

L = DSPACE(O(log n)) \odot L = $U_{a,b}$, 0 DSPACE(a.log n+b) NL = NSPACE(O(log n)) $N_L = U_{a,b>0}$ NSPACE(a.log n+b) "L and NL are to space what P and NP are to time "

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Again, tighter than for NTIME (where in fact, we needed $T(n+1) = o(T'(n))$

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	- Again, tighter than for NTIME (where in fact, we needed $T(n+1) = o(T'(n))$

No "delayed flip, " because, as we will see $later, NSPACE(O(S)) = co-NSPACE(O(S))!$

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 \bullet In fact, NTIME(T) \subseteq DSPACE(O(T)) (simulate with all T-long certificates)

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 \odot With space S(n), only 2^{O(S(n))} configurations (for S(n) = $\Omega(\log n)$). So can take at most $2^{O(S(n))}$ time (else gets into an infinite loop)

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 \odot With space S(n), only 2^{O(S(n))} configurations (for S(n) = $\Omega(\log n)$). So can take at most 2^{0(S(n))} time (else gets into an infinite loop) $\mathsf{DSPACE}(\mathsf{S}) \subseteq \mathsf{DTIME}(2^{O(\mathsf{S})})$

In time T(n), can use at most T(n) space

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 $\mathsf{DSPACE}(\mathsf{S}) \subseteq \mathsf{DTIME}(2^{O(\mathsf{S})})$

In fact, $\text{NSPACE}(S) \subseteq \text{DTIME}(2^{O(S)})$

 $h=2^{\left(O(S)\right)}$

Configuration graph as a DAG is of size 2O(S)

Configuration graph as a DAG is of size 20(S) \bigcirc

Write down all configurations and edges

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Write down all configurations and edges

Can do it less explicitly if space were a concern (but it's not, here) $\Big|_{h=2^{\left(O(S)\right)}}$

- Configuration graph as a DAG is of size 20(S) \circledcirc
	- Write down all configurations and edges
		- Can do it less explicitly if space were a concern (but it's not, here)
	- Run (in poly time) any reachability algorithm (say, breadth-first search) to see if there is a (directed) path from start config. to an accept config.

 $h=2^{(O(S))}$

- Configuration graph as a DAG is of size 2O(S)
	- Write down all configurations and edges
		- Can do it less explicitly if space were a concern (but it's not, here)
	- Run (in poly time) any reachability algorithm (say, breadth-first search) to see if there is a (directed) path from start config. to an accept config.
		- poly(2^{0(S)}) = 2^{0(S)}

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