# Operating Systems Design (CS 423)



Elsa L Gunter 2112 SC, UIUC

http://www.cs.illinois.edu/class/cs423/

Based on slides by Roy Campbell, Sam King, and Andrew S Tanenbaum

3/7/11



# **Definitions**

- Tasks unit of work
- Periodic and aperiodic tasks
- Arrival time time when task can begin execution
- Deadline time by which task must be done
  - Can be relative or absolute
- Execution time amount of time task needs to finish work

3/7/11 2



#### EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11



## EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11



#### EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75



3/7/11



# EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75



3/7/11 6

# 4

## **EDF** example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11 7



- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11 8



## EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11



## EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75

3/7/11

10

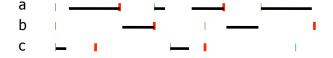
12



#### EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75



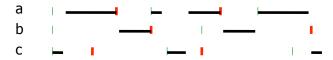
3/7/11



# EDF example

- Job a: e(a) = 15, D(a) = 20 (period 30)
- Job b: e(b) = 10, D(b) = 30 (period 45)
- Job c: e(c) = 5, D(c) = 10 (period 35)

Time 0 5 10 15 20 25 30 35 40 45 50 55 60 65 70 75



3/7/11



#### **Address Spaces and Memory**

- Process = one or more threads in an address space
- Thread: stream of execution
  - Unit of concurrency
- Address space: memory space that threads
  - Unit of data

3/7/11



## Address Space Abstrac2on

- Address space: all the memory data
  - Program code, stack, data segment
- Hardware interface (physical reality)
  - One memory, small, shared
- Application interface (illusion)
  - Each process has own memory, large

3/7/11 14



#### Illusions Provided by Address Apace

- Address independence
  - Same address in different processes not conflicting with each other
  - Eg Same address for stack
- Protection
  - One process cannot access the data of another
  - Secret data, protected code
- Virtual memory
  - 64 bit address space, memory many 4 G

3/7/11 15



13

17

#### **Uni-programming**

- One process runs at a time
- Always load process into the same spot
- How do you switch processes?
- What abstractions
- does this provide?
- Problems?

Operating System ROM

User Memory

16

3/7/11



#### Multi-programming

- Multi-programming: more than one process in memory at a time
- Need address translation
  - Need protection
- Address translation
  - Avoid conflicting addresses
  - Static: before you execute
  - Dynamic: during execution, could change

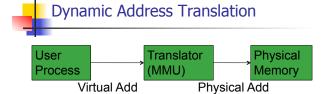
3/7/11



#### Dynamic translation

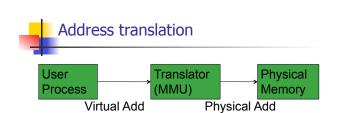
- Translate every memory reference from virtual address to physical address
- Virtual address: an address used by the user process to reference a location in memory
- Physical address: an address used by the physical memory

3/7/11 18



- Translation enforces protection
  - One process can't even refer to another process's address space
- Translation enables virtual memory
  - A virtual address only needs to be in physical memory when it is being accessed
- Change translations on the fly as different virtual addresses occupy physical memory
- Do you need hardware support?

3/7/11 19



- Lots of ways to implement, remember the big picture
- Tradeoffs:
  - Flexibility (e.g., sharing, growth, virtual memory)
  - Size of translation data
  - Speed of translation

3/7/11 20