Intro. Algorithms & Models of Computation CS/ECE 374A, Fall 2024

# Circuit satisfiability and Cook-Levin Theorem

Lecture 24 Thursday, December 5, 2024

<sup>L</sup>ATEXed: August 25, 2024 14:23

Intro. Algorithms & Models of Computation CS/ECE 374A, Fall 2024

24.1 Recap

NP: languages that have non-deterministic polynomial time algorithms

A language  $L$  is **NP-Complete** if and only if

- $\blacktriangleright$  *L* is in NP
- ▶ for every  $L'$  in NP,  $L' \leq_P L$

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Theorem 24.1 (Cook-Levin).

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# Pictorial View



# P and NP

#### Possible scenarios:

- 1.  $P = NP$ .
- 2.  $P \neq NP$

Question: Suppose  $P \neq NP$ . Is every problem in NP \ P also NP-Complete?

#### Theorem 24.2 (Ladner).

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- 1. Independent Set  $\leq_P$  Clique, Clique  $\leq_P$  Independent Set.  $\implies$  Clique  $\approx_P$  Independent Set.
- 2. Vertex Cover  $\leq_P$  Independent Set, Independent Set  $\leq_P$  Vertex Cover.  $\implies$  Independent Set  $\approx_{P}$  Vertex Cover.
- 3. **3SAT**  $\leq_{\rho}$  **SAT**, **SAT**  $\leq_{\rho}$  **3SAT**  $\Rightarrow$  **3SAT**  $\approx_{\rho}$  **SAT**.
- 4. 3SAT  $\leq_P$  Independent Set. Exercise (or Cook-Levin theorem): **Independent Set**  $\leq_{\mathbf{P}}$  **SAT**  $\implies$  3SAT  $\approx_P$  Independent Set.
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All these problems are in NP.

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# 24.2 Circuit SAT

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# 24.2.1 The circuit satisfiability (CSAT) problem

### **Circuits**

#### Definition 24.1.

A circuit is a directed acyclic graph with



- 1. Input vertices (without incoming edges) labelled with  $0, 1$  or a distinct variable.
- 2. Every other vertex is labelled ∨, ∧ or ¬.
- 3. Single node output vertex with no outgoing edges.

Can safely assume every node has at most two incoming edges.

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# CSAT: Circuit Satisfaction

#### Definition 24.2 (Circuit Satisfaction (CSAT).).

Given a circuit as input, is there an assignment to the input variables that causes the output to get value 1?

Claim 24.3. CSAT is in NP.

- 1. Certificate: Assignment to input variables.
- **2. Certifier:** Evaluate the value of each gate in a topological sort of DAG and check the output gate value.

# CSAT: Circuit Satisfaction

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# Circuit SAT vs SAT

CNF formulas are a rather restricted form of Boolean formulas.

Circuits are a much more powerful (and hence easier) way to express Boolean formulas

However they are equivalent in terms of polynomial-time solvability.

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#### Converting a CNF formula into a Circuit  $3SAT <_{P}$  CSAT

Given  $3\text{CNF}$  formula  $\varphi$  with *n* variables and *m* clauses, create a Circuit *C*.

- Inputs to C are the *n* boolean variables  $x_1, x_2, \ldots, x_n$
- ▶ Use NOT gate to generate literal  $\neg x_i$  for each variable  $x_i$
- ▶ For each clause  $(\ell_1 \vee \ell_2 \vee \ell_3)$  use two OR gates to mimic formula
- ▶ Combine the outputs for the clauses using AND gates to obtain the final output

$$
\varphi = \Big(x_1 \vee \vee x_3 \vee x_4 \Big) \wedge \Big( x_1 \vee \neg x_2 \vee \neg x_3 \Big) \wedge \Big( \neg x_2 \vee \neg x_3 \vee x_4 \Big)
$$

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Example  $3SAT \leq_P CSAT$ 

$$
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# 3SAT  $\leq_P$  CSAT

Lemma 24.4. SAT  $\leq_P 3$ SAT  $\leq_P$  CSAT.

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# 24.2.2 Towards reducing CSAT to 3SAT



















$$
\left(\begin{matrix} z = x \wedge y \end{matrix}\right)
$$

$$
\equiv
$$

 $(z \vee \overline{x} \vee \overline{y}) \wedge (\overline{z} \vee x \vee y) \wedge (\overline{z} \vee x \vee \overline{y}) \wedge (\overline{z} \vee \overline{x} \vee y)$ 











$$
(z = x \land y)
$$
  
\n
$$
\equiv
$$
  
\n
$$
(z \lor \overline{x} \lor \overline{y}) \land (\overline{z} \lor x \lor y) \land (\overline{z} \lor x \lor \overline{y}) \land (\overline{z} \lor \overline{x} \lor y)
$$

Simplify further if you want to

1. Using that  $(x \vee y) \wedge (x \vee \overline{y}) = x$ , we have that:

1.1  $(\overline{z} \vee x \vee u) \wedge (\overline{z} \vee x \vee \overline{y}) = (\overline{z} \vee x)$ 1.2  $(\overline{z} \vee x \vee y) \wedge (\overline{z} \vee \overline{x} \vee y) = (\overline{z} \vee y)$ 

2. Using the above two observation, we have that our formula  $\psi \equiv (z \vee \overline{x} \vee \overline{y}) \wedge (\overline{z} \vee x \vee y) \wedge (\overline{z} \vee x \vee \overline{y}) \wedge (\overline{z} \vee \overline{x} \vee y)$ is equivalent to  $\psi \equiv \big(z \vee \overline{\mathbf{x}} \vee \overline{\mathbf{y}}\big) \wedge \big(\overline{\mathbf{z}} \vee \mathbf{x}\big) \wedge \big(\overline{\mathbf{z}} \vee \mathbf{y}\big)$ 

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\n
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#### Lemma 24.6.

Simplify further if you want to

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(z = x \vee y) \equiv (z \vee x \vee \overline{y}) \wedge (z \vee \overline{x} \vee y) \wedge (z \vee \overline{x} \vee \overline{y}) \wedge (\overline{z} \vee x \vee y)
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#### Lemma 24.6.

#### Converting  $z = \overline{x}$  to CNF

Lemma 24.7.  $z = \overline{x}$   $\equiv$   $(z \vee x) \wedge (\overline{z} \vee \overline{x})$ .

## Summary of formulas we derived

#### Lemma 24.8.

The following identities hold:

1. 
$$
z = \overline{x}
$$
  $\equiv$   $(z \lor x) \land (\overline{z} \lor \overline{x})$ .  
\n2.  $(z = x \lor y)$   $\equiv$   $(z \lor \overline{y}) \land (z \lor \overline{x}) \land (\overline{z} \lor x \lor y)$   
\n3.  $(z = x \land y)$   $\equiv$   $(z \lor \overline{x} \lor \overline{y}) \land (\overline{z} \lor x) \land (\overline{z} \lor y)$ 

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# 24.2.3 Reduction from CSAT to SAT

# Converting a circuit into a CNF formula

Label the nodes



## Converting a circuit into a CNF formula

Introduce a variable for each node


Write a sub-formula for each variable that is true if the var is computed correctly.



$$
x_k \quad \text{(Demand a sat' assignment!)}
$$
\n
$$
x_k = x_i \land x_j
$$
\n
$$
x_j = x_g \land x_h
$$
\n
$$
x_i = \neg x_f
$$
\n
$$
x_h = x_d \lor x_e
$$
\n
$$
x_g = x_b \lor x_c
$$
\n
$$
x_f = x_a \land x_b
$$
\n
$$
x_d = 0
$$
\n
$$
x_a = 1
$$

(C) Introduce var for each node.

(D) Write a sub-formula for each variable that is true if the var is computed correctly.

Convert each sub-formula to an equivalent CNF formula



From [Lemma 24.8](#page-68-0) :

1.  $z = \overline{x}$   $\equiv$   $(z \vee x) \wedge (\overline{z} \vee \overline{x})$ 

2.  $(z = x \vee y) \equiv (z \vee \overline{y}) \wedge (z \vee \overline{x}) \wedge (\overline{z} \vee x \vee y)$ 

3.  $(z = x \wedge y) \equiv (z \vee \overline{x} \vee \overline{y}) \wedge (\overline{z} \vee x) \wedge (\overline{z} \vee y)$ 

Convert each sub-formula to an equivalent CNF formula



From [Lemma 24.8](#page-68-0) :

1. 
$$
z = \overline{x}
$$
  $\equiv$   $(z \lor x) \land (\overline{z} \lor \overline{x})$   
\n2.  $(z = x \lor y)$   $\equiv$   $(z \lor \overline{y}) \land (z \lor \overline{x}) \land (\overline{z} \lor x \lor y)$   
\n3.  $(z = x \land y)$   $\equiv$   $(z \lor \overline{x} \lor \overline{y}) \land (\overline{z} \lor x) \land (\overline{z} \lor y)$ 

Take the conjunction of all the CNF sub-formulas



$$
x_k \wedge (\neg x_k \vee x_i) \wedge (\neg x_k \vee x_j) \wedge (x_k \vee \neg x_i \vee \neg x_j) \wedge (\neg x_j \vee x_g) \wedge (\neg x_j \vee x_h) \wedge (x_j \vee \neg x_g \vee \neg x_h) \wedge (x_i \vee x_f) \wedge (\neg x_i \vee \neg x_f) \wedge (x_h \vee \neg x_d) \wedge (x_h \vee \neg x_e) \wedge (\neg x_h \vee x_d \vee x_e) \wedge (x_g \vee \neg x_b) \wedge (x_g \vee \neg x_c) \wedge (\neg x_g \vee x_b \vee x_c) \wedge (\neg x_f \vee x_a) \wedge (\neg x_f \vee x_b) \wedge (x_f \vee \neg x_a \vee \neg x_b) \wedge (\neg x_d) \wedge x_a
$$

We got a CNF formula that is satisfiable if and only if the original circuit is satisfiable.

### Correctness of Reduction

Need to show circuit C is satisfiable if and only if  $\varphi_c$  is satisfiable

 $\Rightarrow$  Consider a satisfying assignment a for C

- 1. Find values of all gates in  $C$  under  $a$
- 2. Give value of gate  $v$  to variable  $x_v$ ; call this assignment  $a'$
- 3. **a'** satisfies  $\varphi_{\mathcal{C}}$  (exercise)

 $\Leftarrow$  Consider a satisfying assignment a for  $\varphi_C$ 

- 1. Let  $a'$  be the restriction of  $a$  to only the input variables
- 2. Value of gate  $\bm{v}$  under  $\bm{a'}$  is the same as value of  $\bm{x_v}$  in  $\bm{a}$
- 3. Thus, a' satisfies C

### The result

Lemma 24.9. CSAT  $\leq_P$  SAT  $\leq_P$  3SAT.

Theorem 24.10. CSAT is NP-Complete.

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# 24.3 NP-Completeness of Graph Coloring

Intro. Algorithms & Models of Computation CS/ECE 374A, Fall 2024

# 24.3.1 The coloring problem

#### Problem: Graph Coloring

**Instance:**  $G = (V, E)$ : Undirected graph, integer k. **Question:** Can the vertices of the graph be colored using  $k$  colors so that vertices connected by an edge do not get the same color?

#### Problem: 3 Coloring

**Instance:**  $G = (V, E)$ : Undirected graph.

Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?



#### Problem: 3 Coloring

**Instance:**  $G = (V, E)$ : Undirected graph. Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?



- 1. Observation: If G is colored with  $k$  colors then each color class (nodes of same color) form an independent set in G.
- 2. G can be partitioned into k independent sets  $\iff$  G is k-colorable.
- 3. Graph 2-Coloring can be decided in polynomial time.
- 4. G is 2-colorable  $\iff$  G is bipartite.
- 5. There is a linear time algorithm to check if G is bipartite using BFS (we saw this earlier).

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# 24.3.2 Problems related to graph coloring

# Register allocation during compilation

- 1. When a compiler generates the assembly/VM code it needs to allocation registers to values being handled.
- 2. Need to make sure registers are not in conflict.
- 3. Build a conflict graph.
- 4. Color the conflict graph.
- 5. Every color is a register.
- 6. If not enough registers, then use memory/stack to store values. 7. CISC v.s. RISC.

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# Graph Coloring and Register Allocation

#### Register Allocation

Assign variables to (at most)  $k$  registers such that variables needed at the same time are not assigned to the same register

#### Interference Graph

Vertices are variables, and there is an edge between two vertices, if the two variables are "live" at the same time.

#### **Observations**

- ▶ [Chaitin] Register allocation problem is equivalent to coloring the interference graph with  $k$  colors
- Moreover, 3-COLOR  $\leq_P$  k-Register Allocation, for any  $k \geq 3$

- 1. Given  $n$  classes and their meeting times, are  $k$  rooms sufficient?
- 2. Reduce to Graph  $k$ -Coloring problem
- 3. Create graph G
	- $\blacktriangleright$  a node  $\upsilon_i$  for each class i
	- **•** an edge between  $v_i$  and  $v_j$  if classes *i* and *j* conflict
- 4. Exercise: G is k-colorable  $\iff$  k rooms are sufficient.

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# Frequency Assignments in Cellular Networks

- 1. Cellular telephone systems that use Frequency Division Multiple Access (FDMA) (example: GSM in Europe and Asia and AT&T in USA)
	- $\triangleright$  Breakup a frequency range  $[a, b]$  into disjoint bands of frequencies  $[a_0, b_0], [a_1, b_1], \ldots, [a_k, b_k]$
	- $\blacktriangleright$  Each cell phone tower (simplifying) gets one band
	- ▶ Constraint: nearby towers cannot be assigned same band, otherwise signals will interference
- 2. Problem: given k bands and some region with n towers, is there a way to assign the bands to avoid interference?
- 3. Can reduce to k-coloring by creating interference/conflict graph on towers.

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# 24.3.3 Showing NP-Completeness of 3 COLORING

Intro. Algorithms & Models of Computation CS/ECE 374A, Fall 2024

# 24.3.3.1 The variable assignment gadget

# 3-Coloring is NP-Complete

#### ▶ 3-Coloring is in NP.

- $\triangleright$  Certificate: for each node a color from  $\{1, 2, 3\}$ .
- ▶ Certifier: Check if for each edge  $(u, v)$ , the color of u is different from that of v.
- ▶ Hardness: We will show  $3-SAT < p$  3-Coloring.

#### 1.  $\varphi$ : Given **3SAT** formula (i.e., **3**CNF formula).

2.  $\varphi$ : variables  $x_1, \ldots, x_n$  and clauses  $C_1, \ldots, C_m$ .

3. Create graph  $G_{\varphi}$  s.t.  $G_{\varphi}$  3-colorable  $\iff \varphi$  satisfiable.

- 
- 
- 
- 
- 

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- 3. Create graph  $G_{\varphi}$  s.t.  $G_{\varphi}$  3-colorable  $\iff \varphi$  satisfiable.
	- **Exercise assignment**  $x_1, \ldots, x_n$  **in colors assigned nodes of**  $G_\varphi$ **.**
	- ▶ create triangle with node True, False, Base
	- **•** for each variable  $x_i$  two nodes  $v_i$  and  $\bar{v}_i$  connected in a triangle with common Base
	- If graph is 3-colored, either  $v_i$  or  $\bar{v}_i$  gets the same color as True. Interpret this as a truth assignment to  $v_i$
	- ▶ Need to add constraints to ensure clauses are satisfied (next phase)

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## Assignment encoding using 3-coloring



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# 24.3.3.2 The clause gadget

## 3 color this gadget.

Clicker question

You are given three colors: red, green and blue. Can the following graph be three colored in a valid way (assuming the two nodes are already colored as indicated).



(A) Yes. (B) No.

## 3 color this gadget II

Clicker question

You are given three colors: red, green and blue. Can the following graph be three colored in a valid way (assuming the two nodes are already colored as indicated).



(A) Yes. (B) No.

## Clause Satisfiability Gadget

- 1. For each clause  $C_i = (a \vee b \vee c)$ , create a small gadget graph
	- $\triangleright$  gadget graph connects to nodes corresponding to  $a, b, c$
	- ▶ needs to implement OR

2. OR-gadget-graph:

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	- ▶ needs to implement OR
- 2. OR-gadget-graph:



## OR-Gadget Graph

Property: if  $a, b, c$  are colored False in a 3-coloring then output node of OR-gadget has to be colored False.

Property: if one of  $a, b, c$  is colored True then OR-gadget can be 3-colored such that output node of OR-gadget is colored True.

#### Reduction

- ▶ create triangle with nodes True, False, Base
- ▶ for each variable  $x_i$  two nodes  $v_i$  and  $\bar{v}_i$  connected in a triangle with common Base
- ▶ for each clause  $C_i = (a \vee b \vee c)$ , add OR-gadget graph with input nodes a, b, c and connect output node of gadget to both False and Base



#### Reduction



#### Claim 24.1.

No legal 3-coloring of above graph (with coloring of nodes  $T, F, B$  fixed) in which  $a, b, c$  are colored False. If any of  $a, b, c$  are colored True then there is a legal 3-coloring of above graph.

3 coloring of the clause gadget



#### Reduction Outline

#### Example 24.2.  $\varphi = (u \vee \neg v \vee w) \wedge (v \vee x \vee \neg y)$



- $\varphi$  is satisfiable implies  $G_{\varphi}$  is 3-colorable
	- if  $x_i$  is assigned True, color  $v_i$  True and  $\bar{v}_i$  False
	- ▶ for each clause  $C_i = (a \vee b \vee c)$  at least one of  $a, b, c$  is colored True. OR-gadget for  $C_i$  can be 3-colored such that output is True.

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	- ▶ for each clause  $C_i = (a \lor b \lor c)$  at least one of  $a, b, c$  is colored True. OR-gadget for  $C_i$  can be 3-colored such that output is True.
- $G_{\varphi}$  is 3-colorable implies  $\varphi$  is satisfiable
	- if  $v_i$  is colored True then set  $x_i$  to be True, this is a legal truth assignment
	- ▶ consider any clause  $C_i = (a \vee b \vee c)$ . it cannot be that all a, b, c are False. If so, output of OR-gadget for  $C_i$  has to be colored False but output is connected to Base and False!

- $\varphi$  is satisfiable implies  $G_{\varphi}$  is 3-colorable
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... from 3SAT to 3COLOR



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# 24.4 Proof of Cook-Levin Theorem

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# 24.4.1 Statement and sketch of idea for the proof

Cook-Levin Theorem

#### Theorem 24.1 (Cook-Levin). SAT is NP-Complete.

We have already seen that **SAT** is in **NP**.

#### Need to prove that every language  $L \in NP$ ,  $L \leq_P SAT$

**Difficulty:** Infinite number of languages in **NP**. Must simultaneously show a generic reduction strategy.

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## The plot against SAT

High-level plan to proving the Cook-Levin theorem

What does it mean that  $L \in \mathsf{NP}$ ?

 $L \in NP$  implies that there is a non-deterministic TM M and polynomial  $p()$  such that

 $L = \{x \in \Sigma^* \mid M \text{ accepts } x \text{ in at most } p(|x|) \text{ steps}\}$ 

**Input:**  $M, x, p$ . **Question:** Does M stops on input x after  $p(|x|)$  steps?

Describe a reduction R that computes from  $M, x, p$  a **SAT** formula  $\varphi$ .

- $\triangleright$  R takes as input a string x and outputs a SAT formula  $\varphi$
- $\triangleright$  R runs in time polynomial in  $|x|, |M|$
- $\triangleright$   $x \in L$  if and only if  $\varphi$  is satisfiable

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 $\varphi$  is satisfiable if and only if  $x \in L$ 

 $\varphi$  is satisfiable if and only if nondeterministic M accepts x in  $p(|x|)$  steps

#### BIG IDEA

- $\triangleright$   $\varphi$  will express "M on input x accepts in  $p(|x|)$  steps"
- $\triangleright$   $\varphi$  will encode a computation history of M on x



 $\varphi$  is satisfiable if and only if  $x \in L$ 

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 $\varphi$  is satisfiable if and only if  $x \in L$ 

 $\varphi$  is satisfiable if and only if nondeterministic M accepts x in  $p(|x|)$  steps

#### BIG IDEA

- $\triangleright$   $\varphi$  will express "M on input x accepts in  $p(|x|)$  steps"
- $\triangleright \varphi$  will encode a computation history of M on x



 $\varphi$  is satisfiable if and only if  $x \in L$ 

 $\varphi$  is satisfiable if and only if nondeterministic M accepts x in  $p(|x|)$  steps

#### BIG IDEA

- $\triangleright$   $\varphi$  will express "M on input x accepts in  $p(|x|)$  steps"
- $\triangleright \varphi$  will encode a computation history of M on x

#### The Matrix Executions

Tableau of Computation

M runs in time  $p(|x|)$  on x. Entire computation of M on x can be represented by a "tableau"



Row  $\boldsymbol{i}$  gives contents of all cells at time  $\boldsymbol{i}$ At time  $\theta$  tape has input x followed by blanks Each row long enough to hold all cells M might ever have scanned.
# Variables of  $\varphi$

Four types of variables to describe computation of  $M$  on  $x$ 

- $\triangleright$   $\tau(b, h, i)$ : tape cell at position h holds symbol h at time i. For  $h = 1, ..., p(|x|)$ ,  $b \in \Gamma$ ,  $i = 0, ..., p(|x|)$ .
- $\blacktriangleright$   $H(h, i)$ : read/write head is at position h at time i. Fir  $h = 1, ..., p(|x|)$ , and  $i = 0, ..., p(|x|)$
- $\triangleright$   $S(q, i)$  state of M is q at time i. For all  $q \in Q$  and  $i = 0, \ldots, p(|x|)$ .

 $\triangleright$   $I(i, i)$  instruction number *i* is executed at time *i* M is non-deterministic, need to specify transitions in some way. Number transitions as  $1, 2, \ldots, \ell$  where  $j$ th transition is  $<\bm{q_j}, \bm{b_j}, \bm{q'_j}$  $'_{j}, b'_{j}$  $j'_j, d_j >$  indication  $(q_i)$  $'_{j}, b'_{j}$  $\left(j,d_j\right)\in \delta(q_j,b_j)$ , direction  $d_j\in\{-1,0,1\}.$ Number of variables is  $O(p(|x|)^2|M|^2)$ 

Some abbreviations for ease of notation  $\bigwedge_{k=1}^{m} x_k$  means  $x_1 \wedge x_2 \wedge \ldots \wedge x_m$ 

 $\bigvee_{k=1}^{m} x_k$  means  $x_1 \vee x_2 \vee \ldots \vee x_m$ 

 $\bigoplus (x_1, x_2, \ldots, x_k)$  is a formula that means exactly one of  $x_1, x_2, \ldots, x_m$  is true. Can be converted to CNF form

CNF formula showing making sure that at most one variable is assigned value 1:

 $\bigwedge$   $(\overline{x_i} \vee \overline{x_j})$  $1 \le i \le i \le k$ 

$$
\bigoplus (x_1, x_2, \ldots, x_k) = \bigwedge_{1 \leq i < j \leq k} (\overline{x_i} \vee \overline{x_j}) \bigwedge (x_1 \vee x_2 \vee \cdots \vee x_k).
$$

Some abbreviations for ease of notation  $\bigwedge_{k=1}^{m} x_k$  means  $x_1 \wedge x_2 \wedge \ldots \wedge x_m$ 

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$$
\bigoplus (x_1, x_2, \ldots, x_k) = \bigwedge_{1 \leq i < j \leq k} (\overline{x_i} \vee \overline{x_j}) \bigwedge (x_1 \vee x_2 \vee \cdots \vee x_k).
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$$
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$$

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$$
\bigoplus (x_1, x_2, \ldots, x_k) = \bigwedge_{1 \leq i < j \leq k} (\overline{x_i} \vee \overline{x_j}) \bigwedge (x_1 \vee x_2 \vee \cdots \vee x_k).
$$

# Clauses of  $\varphi$

 $\varphi$  is the conjunction of **8** clause groups:

$$
\varphi=\bigwedge_{i=1}^{12}\varphi_i
$$

where each  $\varphi_i$  is a  $\text{CNF}$  formula. Described in subsequent slides.

**Property:**  $\varphi$  is satisfied  $\iff$  there is an execution of M on x that accepts the language in  $p(|x|)$  time.

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# 24.4.2 The consistency of execution

# The variables of  $\varphi$

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_j, b'_j$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $\vec{I}(i, i)$ : Instruction  $i$  was issued at time  $i$ .  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

# $\varphi_1$ : The input is encoded correctly

 $\varphi_1$  asserts (is true iff) the variables are set T/F indicating that M starts in state  $q_0$  at time 0 with tape contents containing x followed by blanks. Let  $x = x_1x_2...x_n$ 

$$
\varphi_1 = S(q_0, 0)
$$
\n// state at time 0 is  $q_0$   
\n
$$
\bigwedge_{h=1}^{n} T(x_h, h, 0)
$$
\n// at time 0 cells 1 to *n* have value  $x_1$  to  $x_n$   
\n
$$
\bigwedge_{h=n+1}^{p(n)} T(\square, h, 0)
$$
\n// all remaining cells are blank  
\n
$$
\bigwedge_{h=n+1}^{h=n+1} T(1, 0)
$$
\n// The head is at time 0 at start of tape

## $\varphi_2$ : M is in exactly one state at any point in time

 $\varphi_2$  asserts M in exactly one state at any time *i*:

$$
\varphi_2=\bigwedge_{i=0}^{p(|x|)}\left(\bigoplus\left(S(q_0,i),S(q_1,i),\ldots,S(q_{|Q|},i)\right)\right)
$$

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

## $\varphi_3$ : Each tape cell holds a unique symbol at any time

 $\varphi_3$  asserts that each tape cell holds a unique symbol at any given time.

$$
\varphi_3 = \bigwedge_{i=0}^{p(|x|)} \bigwedge_{h=1}^{p(|x|)} \oplus (\mathcal{T}(b_1, h, i), \mathcal{T}(b_2, h, i), \ldots, \mathcal{T}(b_{|\Gamma|}, h, i))
$$

For each time i and for each cell position h exactly one symbol  $b \in \Gamma$  at cell position h at time i

#### Variables:

 $\big\langle \, q_j, b_j, q'_i \,$  $'_{j}, b'_{j}$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

## $\varphi_4$ : tape head of M is in exactly one position at any time *i*

 $\varphi_4$  asserts that the read/write head of M is in exactly one position at any time i

$$
\varphi_4=\bigwedge_{i=0}^{p(|x|)}(\oplus(H(1,i),H(2,i),\ldots,H(p(|x|),i)))
$$

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \bm{\mathit{j}},\bm{\mathit{d}}_{\bm{j}}\rangle$ :  $\bm{j}$ th instruction of  $\bm{\mathit{M}}$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

# $\varphi_5$ : M accepts the input

 $\varphi_5$  asserts that M accepts

- $\blacktriangleright$  Let  $q_a$  be unique accept state of M
- ightharpoonup without loss of generality assume M runs all  $p(|x|)$  steps

 $\varphi_5 = S(q_a, p(|x|))$ 

State at time  $p(|x|)$  is  $q_a$  the accept state.

If we don't want to make assumption of running for all steps

$$
\varphi_5 = \bigvee_{i=1}^{p(|x|)} S(q_a, i)
$$

which means  $M$  enters accepts state at some time.

### $\varphi_6$ : M executes a unique instruction at each time

 $\varphi_6$  asserts that M executes a unique instruction at each time

$$
\varphi_6=\bigwedge_{i=0}^{p(|x|)}\oplus (l(1,i),l(2,i),\ldots,l(m,i))
$$

where  $m$  is max instruction number.

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $I(j, i)$ : Instruction j was issued at time i.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

# $\varphi$ <sub>7</sub>: Tape changes only because of the head writing something

 $\varphi_7$  ensures that variables don't allow tape to change from one moment to next if the read/write head was not there.

"If head is not at position h at time i then at time  $i + 1$  the symbol at cell h must be unchanged"

$$
\varphi_7 = \bigwedge_{i} \bigwedge_{h} \bigwedge_{b \neq c} \left( \overline{H(h,i)} \Rightarrow \overline{T(b,h,i) \bigwedge T(c,h,i+1)} \right)
$$

since  $A \Rightarrow B$  is same as  $\neg A \lor B$ , rewrite above in CNF form

$$
\varphi_7 = \bigwedge_i \bigwedge_{h} \bigwedge_{b \neq c} (H(h, i) \vee \neg T(b, h, i) \vee \neg T(c, h, i + 1))
$$

 $\varphi_8$ : Transitions are done from correct states  $j$ th instruction of  $M$ :  $< q_j, b_j, q'_j$  $'_{j}, b'_{j}$  $'_j, d_j >$ 

$$
\varphi_8 = \bigwedge_{i} \bigwedge_{j} (I(j,i) \Rightarrow S(q_j,i))
$$

If instruction  $j$  is executed at time  $i$  then state at time  $i$  must be  $\boldsymbol{q_j}.$ 

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \bm{\mathit{j}},\bm{\mathit{d}}_{\bm{j}}\rangle$ :  $\bm{j}$ th instruction of  $\bm{\mathit{M}}$  $\hat{I}(i, i)$ : Instruction  $i$  was issued at time  $i$ .  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

#### $\varphi_9$ : Transitions are done into correct state  $j$ th instruction of  $M$ :  $< q_j, b_j, q'_j$  $'_{j}, b'_{j}$  $'_j, d_j >$

$$
\varphi_9 = \bigwedge_i \bigwedge_j (I(j,i) \Rightarrow S(q'_j,i+1))
$$

If instruction  $j$  was performed at time  $i$ , then state at time  $i+1$  must be  $\boldsymbol{q}'_i$ j .

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \bm{\mathit{j}},\bm{\mathit{d}}_{\bm{j}}\rangle$ :  $\bm{j}$ th instruction of  $\bm{\mathit{M}}$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.  $\varphi_{10}$ : The character written on tape that triggered an instruction, is the correct one

$$
\varphi_{10} = \bigwedge_{i} \bigwedge_{h} \bigwedge_{j} [(I(j, i) \bigwedge H(h, i)) \Rightarrow T(b_j, h, i)]
$$

If instruction  $j$  was executed at time  $i$  and head was at position  $h$ , then cell  $h$  has the symbol needed to issue instruction  $\boldsymbol{j}$  is written under the head location on the tape.

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.  $\varphi_{11}$ : The correct symbol was written to the tape at time *i* 

$$
\varphi_{11} = \bigwedge_i \bigwedge_j \bigwedge_k [(I(j, i) \wedge H(h, i)) \Rightarrow \top(b'_j, h, i + 1)]
$$

If instruction  $j$  was executed time  $i$  with head at  $h$ , then at next time step symbol  $b_i^\prime$ j was written in position h

#### Variables:

 $\big\langle \, q_j, b_j, q'_j \,$  $'_{j}, b'_{j}$  $\langle \bm{\mathit{j}},\bm{\mathit{d}}_{\bm{j}}\rangle$ :  $\bm{j}$ th instruction of  $\bm{\mathit{M}}$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.  $\varphi_{12}$ : Head was moved in the right direction at time *i* 

$$
\varphi_{12} = \bigwedge_i \bigwedge_j \bigwedge_k [(l(j, i) \wedge H(h, i)) \Rightarrow H(h + d_j, i + 1)]
$$

The head is moved properly according to instr  $\boldsymbol{j}$ .

#### Variables:

 $\big\langle \, q_j, b_j, q'_i \,$  $'_j, b'_j$  $\langle \vec{g},\vec{d_j}\rangle$ :  $j$ th instruction of  $M$  $I(i, i)$ : Instruction *i* was issued at time *i*.  $H(h, i)$ : The head is at location h at time i.  $T(c, h, i)$ : The tape at location h at time i stored the character c.

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# 24.4.3 Proof of correctness

# Proof of Correctness

(Sketch)

- $\triangleright$  Given M, x, poly-time algorithm to construct  $\varphi$
- $\triangleright$  if  $\varphi$  is satisfiable then the truth assignment completely specifies an accepting computation of  $M$  on  $x$
- $\triangleright$  if M accepts x then the accepting computation leads to an "obvious" truth assignment to  $\varphi$ . Simply assign the variables according to the state of M and cells at each time i.

Thus M accepts x if and only if  $\varphi$  is satisfiable

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# 24.5 NP-Complete problems to know and remember

# List of NP-Complete Problems to Remember

#### Problems

- 1. SAT
- 2. 3SAT
- 3. CircuitSAT
- 4. Independent Set
- 5. Clique
- 6. Vertex Cover
- 7. Hamilton Cycle and Hamilton Path in both directed and undirected graphs
- 8. 3Color and Color