NP hardness reductions

Lecture 22

CS 374

Recap



- P = YES/NO questions that can be answered in polynomial time in input size (algorithm)
- NP= YES/No problems where YES instance can be verified in polynomial time
- X is NP-hard: X in P implies P=NP
- Cook-Levin: CircuitSAT NP hard

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How to prove NP hardness



To prove X is NP-hard:

- Step 1: Pick a known NP-hard problem Y
- **Step 2:** Assume for the sake of argument, a polynomial time algorithm for X.
- **Step 3**: Derive a polynomial time algorithm for Y, using algorithm for X as subroutine.
- Step 4: Contradiction

Reduce Y to X

Reduce FROM the problem
I know about
TO the problem
I am curious about

NP hardness



Library of NP-hard problems

CircuitSAT

SAT

Let's assume the problem is easy and see what ridiculous consequences follow

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3SAT



Look at boolean formulas in CNF

$$\overbrace{(a \lor b \lor c \lor d)}^{\text{clause}} \land (b \lor \bar{c} \lor \bar{d}) \land (\bar{a} \lor c \lor d) \land (a \lor \bar{b})$$

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3SAT



Look at boolean formulas in CNF

$$\overbrace{(a \lor b \lor c \lor d)}^{\text{clause}} \land (b \lor \bar{c} \lor \bar{d}) \land (\bar{a} \lor c \lor d) \land (a \lor \bar{b})$$

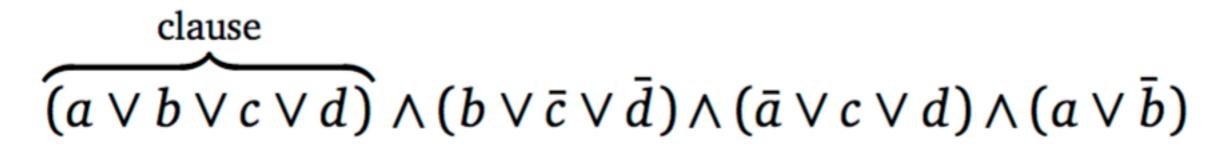
3SAT: exactly three literals per clause! every literal is a variable or the negation of a variable

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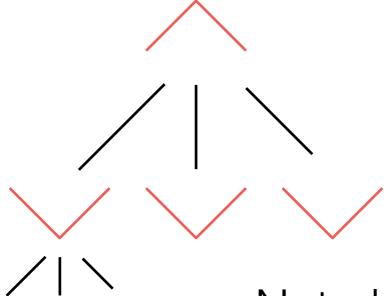
3SAT



Look at boolean formulas in CNF



Parse tree:



3SAT: exactly three literals per clause! every literal is a variable or the negation of a variable

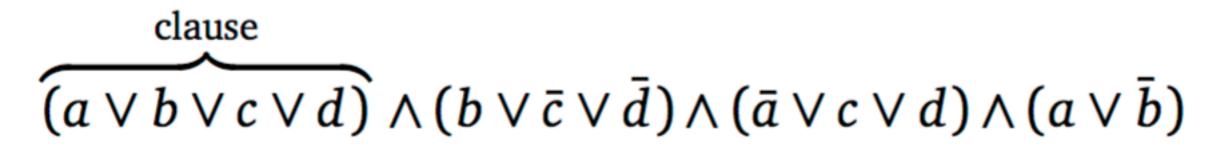
Not all boolean functions can be in the form aVbVcVd

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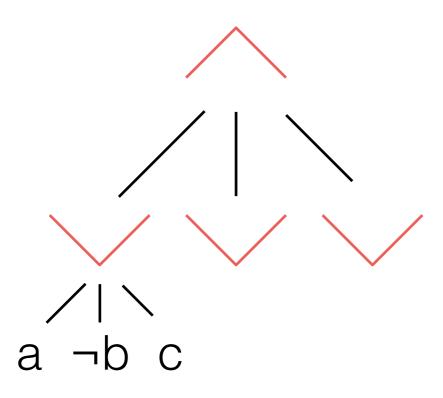
3SAT



Look at boolean formulas in CNF



Parse tree:



3SAT special case of SAT.

unlike when we are thinking about special cases in algorithms

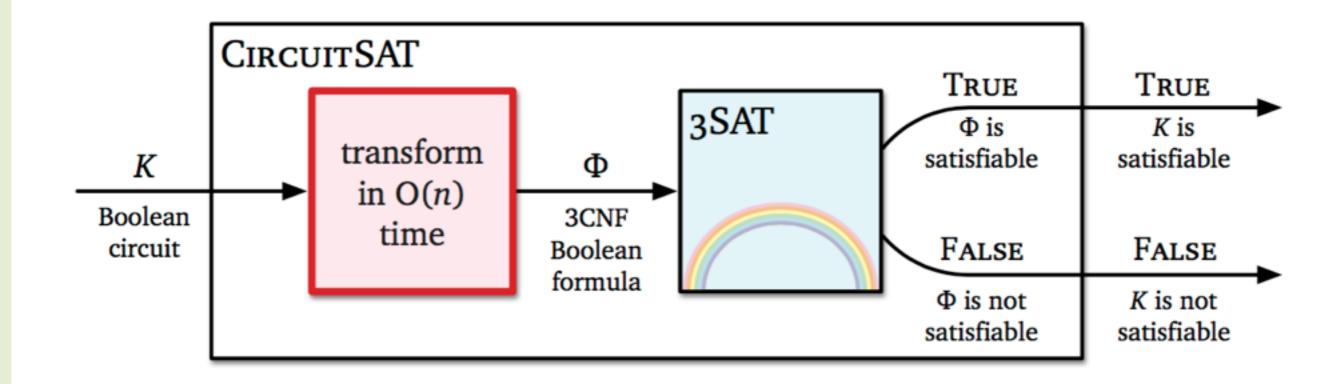
2SAT there is algorithm!

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NP hardness



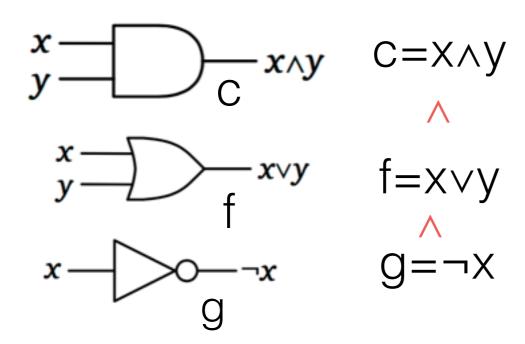
- Poly time reduction from CircuitSAT.
- If there is a poly time algorithm to solve 3SAT, then there is poly time algorithm to solve CircuitSAT



Reduction CircuitSAT to 3SAT



- **Step 1**: Make gates binary (blows up size by at most 2x wires, if there were x wires). Poly time.
- Step 2: Transcribe



Reduction CircuitSAT to 3SAT



Step 3: Make clauses in 3CNF

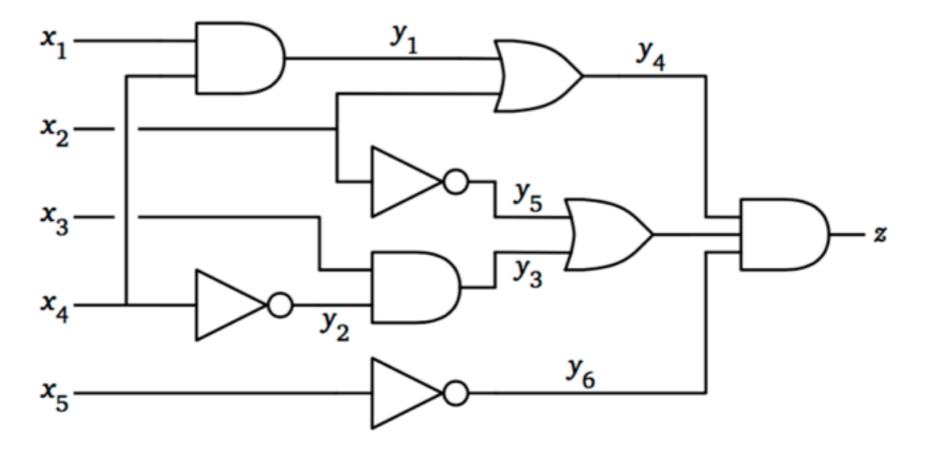
$$a = b \wedge c \longmapsto (a \vee \bar{b} \vee \bar{c}) \wedge (\bar{a} \vee b) \wedge (\bar{a} \vee c)$$

$$a = b \vee c \longmapsto (\bar{a} \vee b \vee c) \wedge (a \vee \bar{b}) \wedge (a \vee \bar{c})$$

$$a = \bar{b} \longmapsto (a \vee b) \wedge (\bar{a} \vee \bar{b})$$

$$a \lor b \longmapsto (a \lor b \lor x) \land (a \lor b \lor \bar{x})$$

 $a \longmapsto (a \lor x \lor y) \land (a \lor \bar{x} \lor y) \land (a \lor x \lor \bar{y}) \land (a \lor \bar{x} \lor \bar{y})$



$$(y_{1} \lor \overline{x_{1}} \lor \overline{x_{4}}) \land (\overline{y_{1}} \lor x_{1} \lor z_{1}) \land (\overline{y_{1}} \lor x_{1} \lor \overline{z_{1}}) \land (\overline{y_{1}} \lor x_{4} \lor z_{2}) \land (\overline{y_{1}} \lor x_{4} \lor \overline{z_{2}})$$

$$\land (y_{2} \lor x_{4} \lor z_{3}) \land (y_{2} \lor x_{4} \lor \overline{z_{3}}) \land (\overline{y_{2}} \lor \overline{x_{4}} \lor z_{4}) \land (\overline{y_{2}} \lor \overline{x_{4}} \lor \overline{z_{4}})$$

$$\land (y_{3} \lor \overline{x_{3}} \lor \overline{y_{2}}) \land (\overline{y_{3}} \lor x_{3} \lor z_{5}) \land (\overline{y_{3}} \lor x_{3} \lor \overline{z_{5}}) \land (\overline{y_{3}} \lor y_{2} \lor z_{6}) \land (\overline{y_{3}} \lor y_{2} \lor \overline{z_{6}})$$

$$\land (\overline{y_{4}} \lor y_{1} \lor x_{2}) \land (y_{4} \lor \overline{x_{2}} \lor z_{7}) \land (y_{4} \lor \overline{x_{2}} \lor \overline{z_{7}}) \land (y_{4} \lor \overline{y_{1}} \lor z_{8}) \land (y_{4} \lor \overline{y_{1}} \lor \overline{z_{8}})$$

$$\land (y_{5} \lor x_{2} \lor z_{9}) \land (y_{5} \lor x_{2} \lor \overline{z_{9}}) \land (\overline{y_{5}} \lor \overline{x_{2}} \lor z_{10}) \land (\overline{y_{5}} \lor \overline{x_{2}} \lor \overline{z_{10}})$$

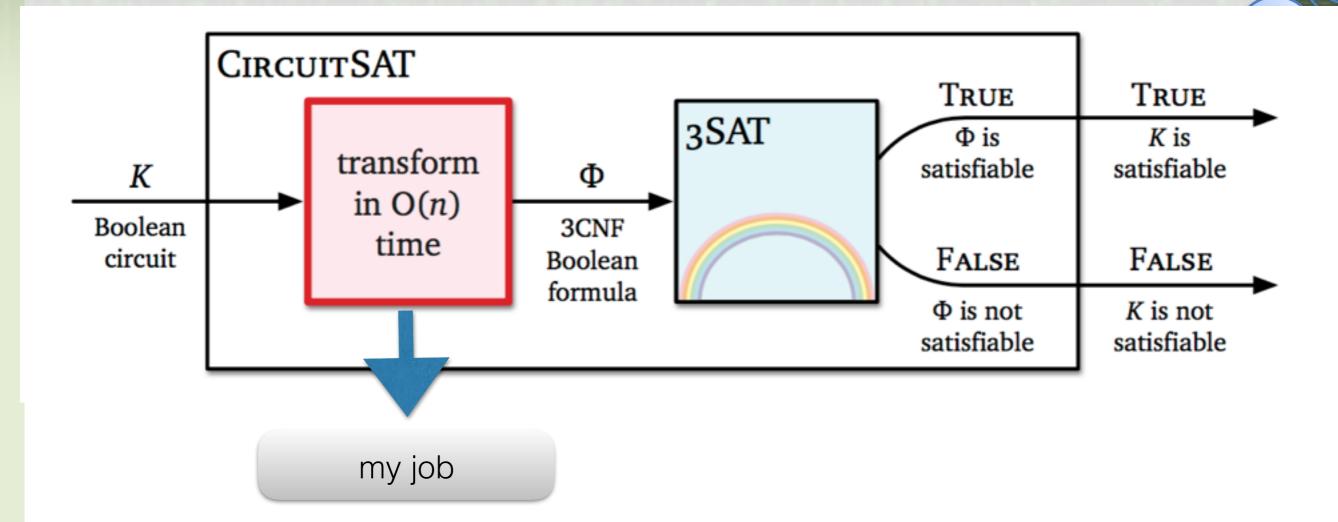
$$\land (y_{6} \lor x_{5} \lor z_{11}) \land (y_{6} \lor x_{5} \lor \overline{z_{11}}) \land (\overline{y_{6}} \lor \overline{x_{5}} \lor \overline{z_{12}}) \land (\overline{y_{6}} \lor \overline{x_{5}} \lor \overline{z_{12}})$$

$$\land (\overline{y_{7}} \lor y_{3} \lor y_{5}) \land (y_{7} \lor \overline{y_{3}} \lor z_{13}) \land (y_{7} \lor \overline{y_{3}} \lor \overline{z_{13}}) \land (y_{7} \lor \overline{y_{5}} \lor z_{14}) \land (y_{7} \lor \overline{y_{5}} \lor \overline{z_{14}})$$

$$\land (y_{8} \lor \overline{y_{4}} \lor \overline{y_{7}}) \land (\overline{y_{8}} \lor y_{4} \lor z_{15}) \land (\overline{y_{8}} \lor y_{4} \lor \overline{z_{15}}) \land (\overline{y_{9}} \lor y_{6} \lor z_{18}) \land (\overline{y_{9}} \lor y_{6} \lor \overline{z_{18}})$$

$$\land (y_{9} \lor \overline{y_{8}} \lor \overline{y_{6}}) \land (y_{9} \lor \overline{z_{19}} \lor z_{20}) \land (y_{9} \lor \overline{z_{19}} \lor \overline{z_{20}}) \land (y_{9} \lor \overline{z_{19}} \lor \overline{z_{20}}) \land (y_{9} \lor \overline{z_{19}} \lor \overline{z_{20}}) \land (y_{9} \lor \overline{z_{19}} \lor \overline{z_{20}})$$

Although this formula may look a lot more ugly and complicated than the original circuit at first glance, it's actually only a constant factor larger—every binary gate in the original



- Algo runs in poly time Proof:
 - Circuit satisfiable implies formula satisfiable
 - formula satisfiable implies circuit satisfiable

even though the reduction goes one direction, the proof needs to go both directions

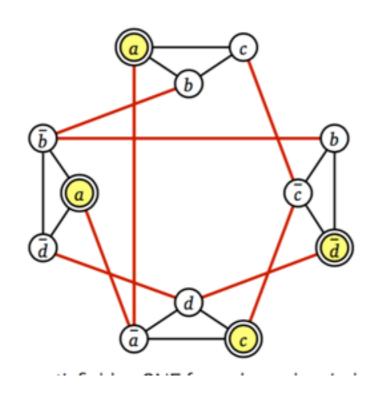
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MAX Independent Set



Input: a graph G(V,E)

 Output: Largest subset of vertices with no edges between them. (enough to find size)



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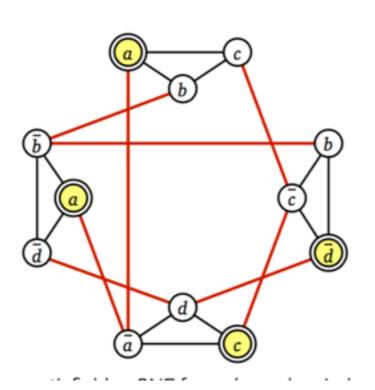
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MAX Independent Set

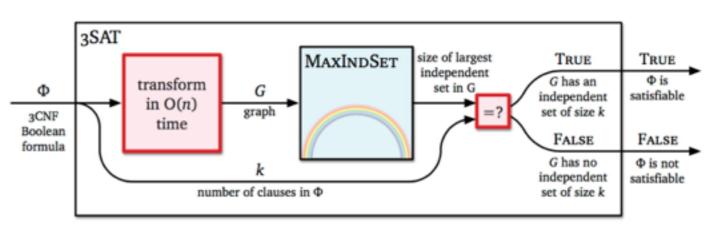


Input: a graph G(V,E)

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Prove this is NP hard by reduction from 3SAT



Polynomial-time reduction from 3SAT to MAXINDSET

MAX Independent Set

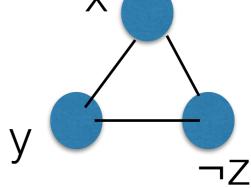


Given an arbitrary 3CNF formula

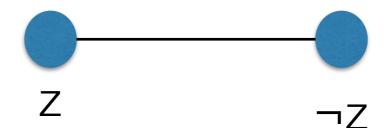
Build a graph G as follows

1) For every clause 3 vertices connected in a triangle

$$X \vee Y \vee \neg Z$$



2) add edges between a literal and its negation

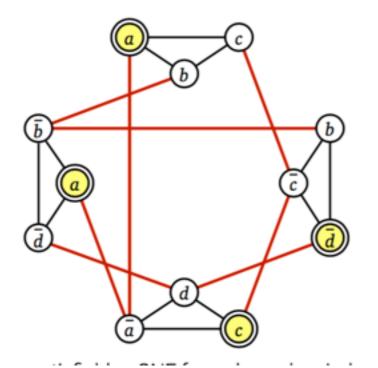


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MAX Independent Set



- Input: a graph G(V,E)
- Output: Largest subset of vertices with no edges between them. (enough to find size)



 $(a \lor b \lor c) \land (b \lor \bar{c} \lor \bar{d}) \land (\bar{a} \lor c \lor d) \land (a \lor \bar{b} \lor \bar{d})$

k clauses -> 3k vertices graph has IS of size k if and only if the formula is satisfiable

MAX Independent Set



Claim:

Graph has IS of size k if and only if the formula is satisfiable

2 steps to proof:

Step 1) Assume formula satisfiable

-Choose satisfying assignment (a=1, b=1, c=1, d=0)



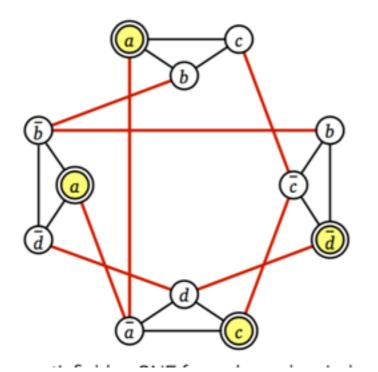
No long edges because every selected literal is true and no edges between each triangle G Has IS of size k!

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MAX Independent Set



- Input: a graph G(V,E)
- Output: Largest subset of vertices with no edges between them. (enough to find size)



 $(a \lor b \lor c) \land (b \lor \bar{c} \lor \bar{d}) \land (\bar{a} \lor c \lor d) \land (a \lor \bar{b} \lor \bar{d})$

k clauses -> 3k vertices graph has IS of size k if and only if the formula is satisfiable

MAX Independent Set



Claim:

Graph has IS of size k if and only if the formula is satisfiable

2 steps to proof:

Step 2) Suppose G has IS of size k. Then this IS contains at most one node per triangle so it has exactly one node per triangle. These nodes provide a satisfying assignment to the formula



When you write reductions

Step 1: Describe the algorithm (and it runs in poly time)

Step 2: Prove one way

Step 3: Prove the other way

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NP hardness



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MAX IS

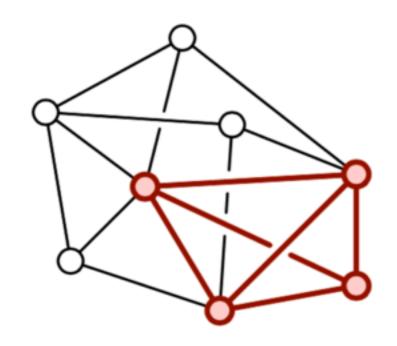
Let's assume the problem is easy and see what ridiculous consequences follow

MAX Clique

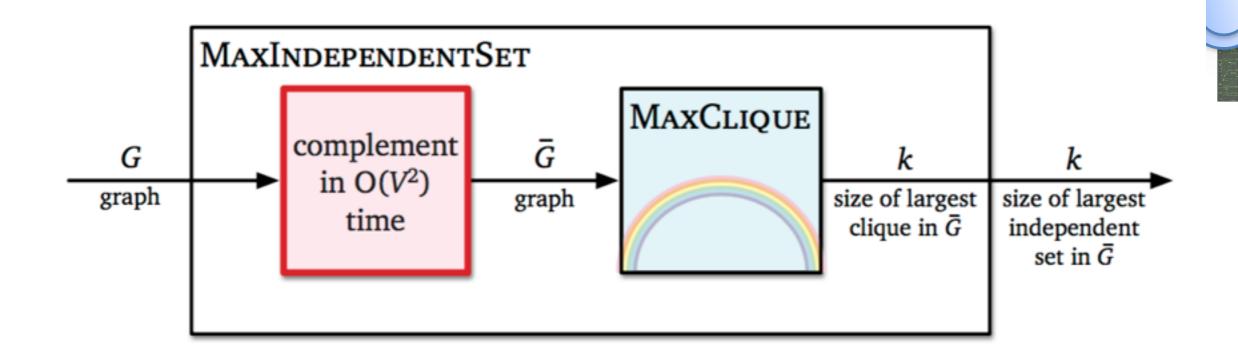


Input: a graph G(V,E)

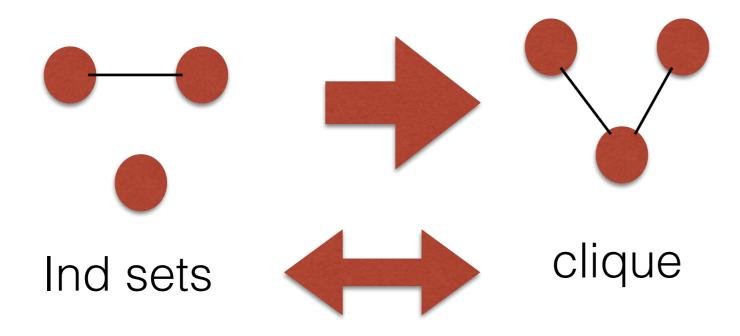
Output: Largest subset of vertices that are all pairwise connected



- Reduction from MAX-IS
- Assume poly time algorithm for MAX Clique
- Derive poly time algorithm for MAX IS



what is G'? Not the same graph as G



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MAX IS

MAX Clique

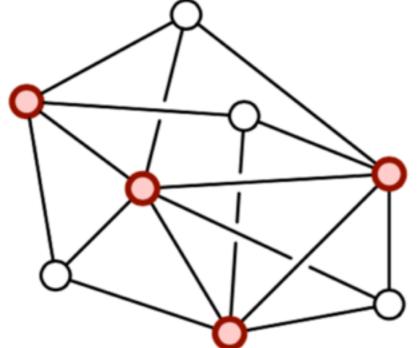
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MIN Vertex Cover

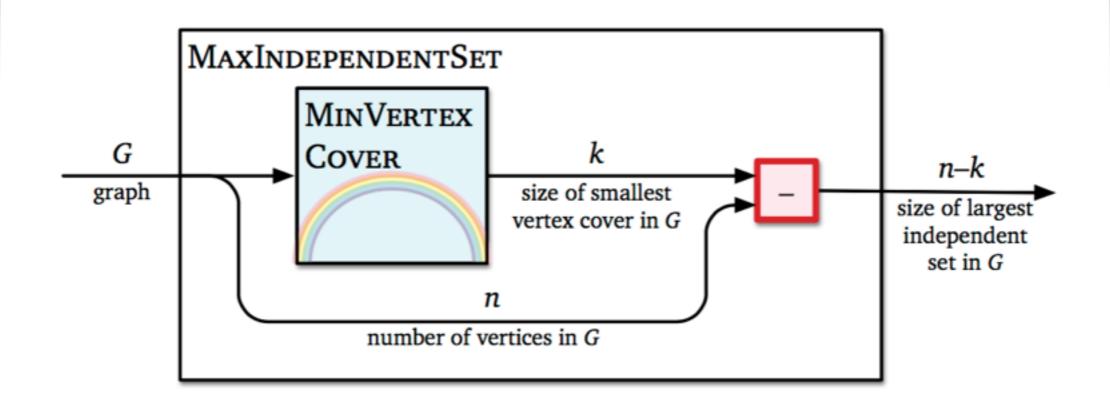


Input: a graph G(V,E)

Output: Smallest set of vertices that touch every edge



- Reduction from MAX-IS
- Assume poly time algorithm for MIN Vertex Cover



what is G'? same graph as G Output is different