

More decidable problems,  
encodings, simulating "real" computers,  
Recognizability vs decidability

Arithmetic .

$$L = \{ a_n \dots a_1 + b_m \dots b_0 = c_r \dots c_0 \}$$

$$a_i, b_j, c_k \in \{0, \dots, 9\},$$

$$\llbracket c_r \dots c_0 \rrbracket = \llbracket a_n \dots a_0 \rrbracket + \llbracket b_m \dots b_0 \rrbracket$$

$$\Sigma = \{0, \dots, 9, +, =\}$$

$$\llbracket a_n \dots a_0 \rrbracket = \sum_{i=0}^n a_i \cdot 10^i .$$

$$\begin{array}{|l}
 \hline
 a_n \dots a_0 + b_m \dots b_0 = c_r \dots c_0 \dots \\
 \hline
 \downarrow \downarrow \\
 a_0 | a_1 | \dots | a_n \quad \square \\
 \hline
 \downarrow \downarrow \\
 b_0 | b_1 | \dots | b_m \\
 \hline
 \downarrow \\
 d_0 | d_1 | \dots | d_m | d_{m+1} | \\
 \hline
 \end{array}$$

All arithmetic ops can be effected by a TM.

$$\langle G = (V, E), s, t \rangle$$

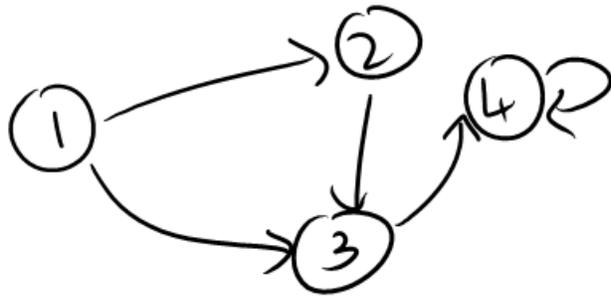
$$E \subseteq V \times V, s, t \in V$$



$$\rightarrow n, m, (n_1, n'_1), (n_2, n'_2), \dots, (n_m, n'_m)$$

$$L = \left\{ \langle n \rangle, \langle m \rangle, \langle n_1, n'_1 \rangle, \dots, \langle n_m, n'_m \rangle, n_s, n_t \mid \right.$$

Graph rep. by this string has  
t reachable from s



"4, 5, (1, 2), (2, 3), (1, 3), (3, 4), (4, 4),  
1, 4"

Decision problem  $\xrightarrow{\text{Encoding}}$  Language

"As far as the encoding is 'natural'  
the language being TM-decidable  
or not will not change"

Encodings don't matter for TM-dec/  
TM-ecog.

But they do matter

- DFA

- Resource-bounded TM.

Problem. Given  $\langle x \rangle$ , find a prime factor of  $x$

$\langle x \rangle$  - binary

$\langle x \rangle$  - unary

$(1^{|x|})$

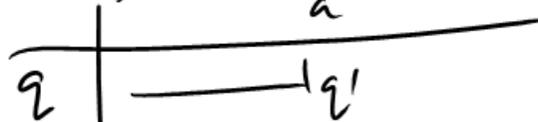
- no one knows if a P-TIME  
m/c.  
- ~~but~~ we can build a P-TIME  
TM.

$$\Gamma_{\text{DFA}} = \{ \langle M \rangle \mid M \text{ is a DFA} \\ \text{and } L(M) = \emptyset \}$$

Encode DFAs as strings.

$$A = (Q, \Sigma, \delta, q_0, F)$$

Encode  $\langle Q \rangle, \langle \Sigma \rangle, \langle q_0 \rangle, \langle F \rangle$



TM can decide  $\Gamma_{\text{DFA}}$  (it does not matter how you encode DFAs)

## S-t Connectivity

$(n, m, \langle n_1, n'_1 \rangle \dots \langle n_m, n'_m \rangle, s, t)$

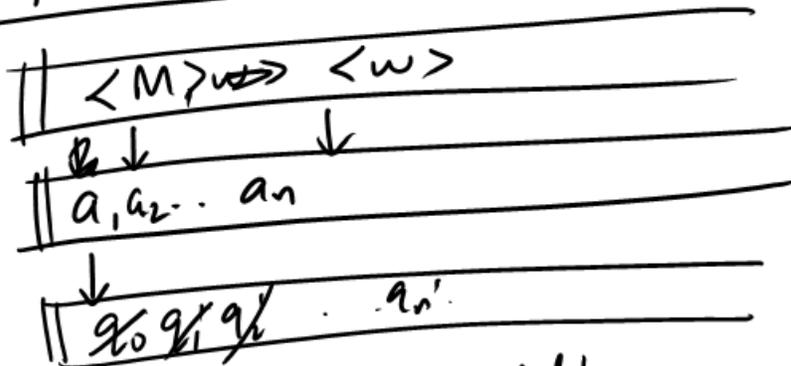
Edge-list  $(\langle n_1, n'_1 \rangle \dots \langle n_m, n'_m \rangle)$

Done list  $(s, u_1, u_2, u_3)$

To-do list  $(\cancel{s}, \cancel{u_1}, \cancel{u_2}, \cancel{u_3}, v_1, v_2, v_3, \dots, v_5, v_6, \dots, w_4, \dots)$   
↑

$$A_{\text{DFA}} = \{ \langle M, w \rangle \mid M \text{ is a DFA, } w \text{ is an input for } M, M \text{ acc } w \}$$

TM for  $A_{\text{DFA}}$



$A_{\text{DFA}}$  is TM-decidable.

$$EQ_{DFA} = \{ \langle M_1, M_2 \rangle \mid L(M_1) = L(M_2) \}$$

$$- L(M_1) \subseteq L(M_2)$$

$$L(M_1) \cap \overline{L(M_2)} = \emptyset$$

$$- L(M_2) \subseteq L(M_1)$$

$EQ_{DFA}$  is TM-decidable.

$$A_{\text{NFA}} = \left\{ \langle M, w \rangle \mid \begin{array}{l} M \text{ accepts } w \\ M \text{ is an NFA} \end{array} \right\}.$$

I. Convert  $M$  to a DFA  $M'$ .  
~~Check if~~ Simulate  $M'$  on  $w$   
And accept iff  $M'$  acc  $w$ .

II. TM could simulate  $M$  on  $w$ ,  
but at each prefix of  $w$ ,  
keep track of the set of states  
the NFA could be in.

$$A_{\text{regex}} = \{ \langle R, w \rangle \mid R \text{ is a regexp} \\ \text{and } w \in L(R) \}$$

TM deciding  $A_{\text{regex}}$  -

- Convert  $R$  to a DFA  $M$
- Check if  $M$  accepts  $w$ .

TMs can simulate "real" computers.

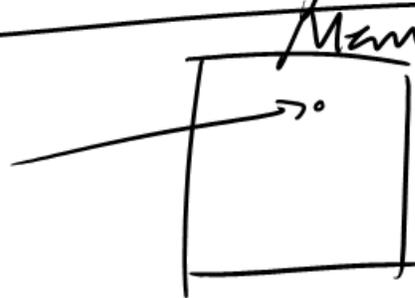
RAM  $r_1, \dots, r_k$

LOAD  $r_1, r_2$

STORE  $r_1, r_2$

JMP ~~to~~  $r_1$

⋮



TM simulating a RAM-machine

$(a_1, d_1), (a_2, d_2), \dots, (a_n, d_n)$

$(5, 123), (6, 124), (9, 12), \dots$

$r_1 = 9$   
LOAD  $q, r_1, r_2$

$r_2 := 12$

Real comp: Registers  
+ Instructions  
+ RAM-memory  
+ control.  
+ arithmetic (ALU)

## Theorem

$L$  is TM-decidable

iff  $L$  and  $\bar{L}$  are TM-recognizable.

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( $\Rightarrow$ )  $L$  is decidable,  $L$  is recognizable.

Since  $L$  is decidable,  $\bar{L}$  is decidable.

So  $\bar{L}$  is also recognizable.

( $\Leftarrow$ )

$M_1$	recog.	$L$
$M_2$	recog	$\bar{L}$

Decider for  $M$ :

Simulate  $M_1$  &  $M_2$  on  $w$   
in "parallel"

Accept' if  $M_1$  halts & accepts

Reject if  $M_2$  halts & accepts.