CS 340 #2: Character Encodings and Binary Math

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Representing Letters: ASCII

Representing numbers is great -- but what about words? Can we make sentences with binary data?

- **Key Idea:** Every letter is _____ binary bits.^{*: in ASCII} (*This means that every letter is _____ hex digits.*)
- Global standard called the American Standard Code for Information Interchange (ASCII) is a ______ for translating numbers to characters.

ASCII (ASCII Character Encoding Examples:						
		Binary	Hex	Char.	Binary	Hex	Char.
Øb	0100	0001	0x41	Α	0b 0110 0001	0x61	а
0b	0100	0010	0x42	В	0b 0110 0010	0x62	b
				С			С
				D			d
Øb	0010	0100	0x24	\$	0b0111 1011	0x7b	{

...and now we can form sentences!

Q: Are there going to be any issues with ASCII?

Representing Letters: Other Character Encodings

Since ASCII uses only 8 bits, we are limited to only 256 unique characters. There's far more than 256 characters -- and what about EMOJIs??

- Many other character encodings exist other than ASCII.
- The most widely used character encoding is known as **Unicode Transformation Format (8-bit)** or _____
- Standard is **ISO/IEC 10646** (Updated annually!).

Technical Details of UTF-8 Encoding

UTF-8 uses a ______-bit design where each character by be any of the following:

Length	Byte #1	Byte #2	Byte #3	Byte #4
1-byte	0			
2-bytes:	110	10		
3-bytes:	1110	10	10	
4-bytes:	1111 0	10	10	10

Unicode characters are represented by **U**+## (where ## is the hex value of the character encoding data) and all 1-byte characters match the ASCII character encoding:

• 'a' is ASCII _____, or _____.

Example: ε (epsilon) is defined as **U+03b5**. How do we encode this?

Example: I received the following binary message encoded in UTF-8:0100 1000 0110 1001 1111 0000 1001 1111 1000 1110 1000 10011. What is the hexadecimal representation of this message?

2. What is the byte length of this message?

- 3. What is the character length of this message?
- 4. What does the message say?

02/utf8-binary.c

Bit Manipulation: Binary Addition For the past two lectures we have focused on the first foundation: **DATA**. Today, we are going to begin the transition away from data and into how data applies to the **CPU**. Binary addition work just like decimal addition, but with only **0**s and **1**s:

	0b	010011	e)b	0011
+	0b	<u>001001</u>	+ 6)b	<u>0111</u>

Negative Numbers:	
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	0b	010011	0b	0011	
-	0b	<u>001001</u>	- 0b	<u>0111</u>	

42	18
- <u>18</u>	- <u>42</u>
-42	31
- <u>32</u>	+ <u>42</u>

Overflow Detection in Two's Complement:

Two's Complement

The Two's Complement is a way to represent signed (ex: positive vs.	
negative) numbers in a way	_!

Towards Multiplication

With Two's Complement, we can add and subtract numbers! What about more complex operations?

For simplicity, let's imagine running on an **7-bit machine***:*

-17 =

-4 =

-1 =

 $10 \times 2 =$ $10 \times 4 =$ $10 \times 9 =$ **Bit Shift Operations:** 1. [Left Shift]: 2. [Right Shift]: