

Processor

r0

r1

r2

r3

r4

r5

r6

r7

PC

Condition codes

N

Z

P

address	
<i>count</i>	a0
<i>highest_score</i>	a4
<i>num_quizzes</i>	a8
<i>quizzes</i>	a12
	a16
	a20

a100	zero_reg r1
a102	store r1 -> [r1+0]
a104	store r1 -> [r1+4]
a106	load r2 <- [r1+0]
a108	load r3 <- [r1+8]
a110	sub r3 - r2 -> r4
a112	br.nz 12
a114	load r2 <- [r1+0]
a116	add r2 + r2 -> r2
a118	add r2 + r2 -> r2
a120	load r3 <- [r2+12]
a122	load r4 <- [r1+4]
a124	sub r3 - r4 -> r5
a126	br.nz 1
a128	store r3 -> [r1+4]
a130	load r2 <- [r1+0]
a132	add r2 + 1 -> r2
a134	store r2 -> [r1+0]
a136	br.pnz -16
a138	(display ...)