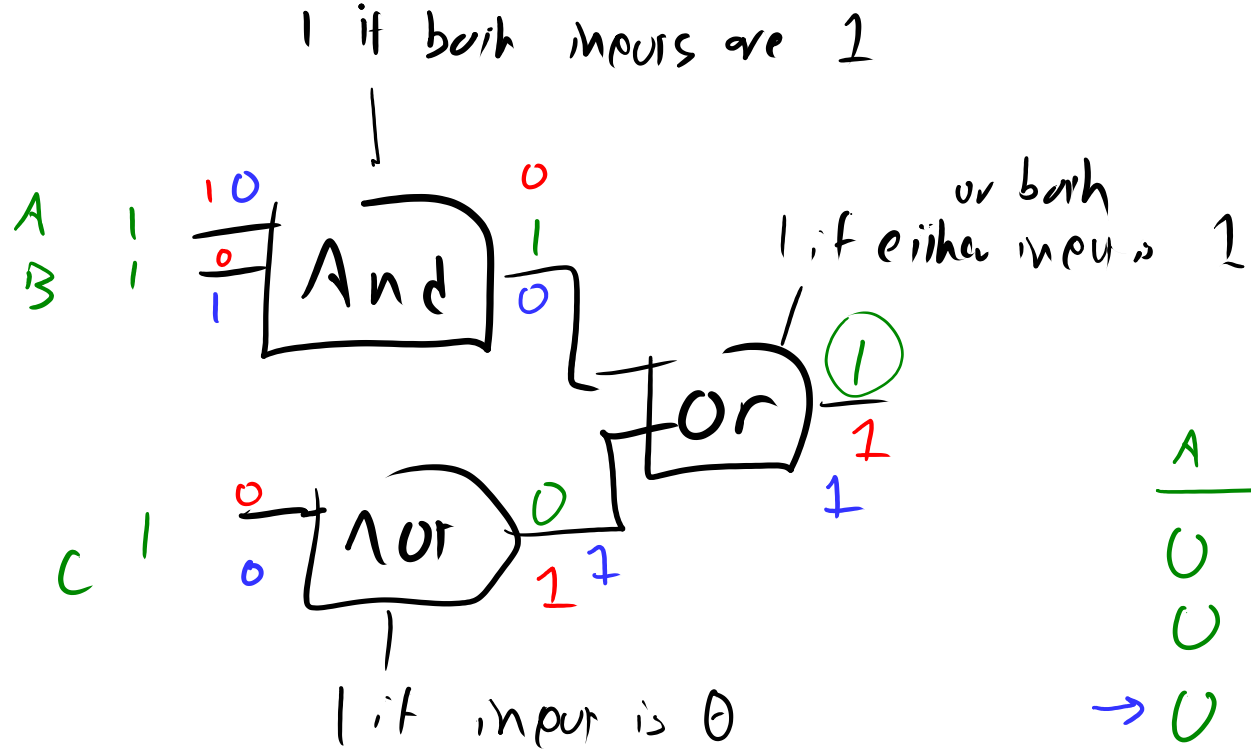
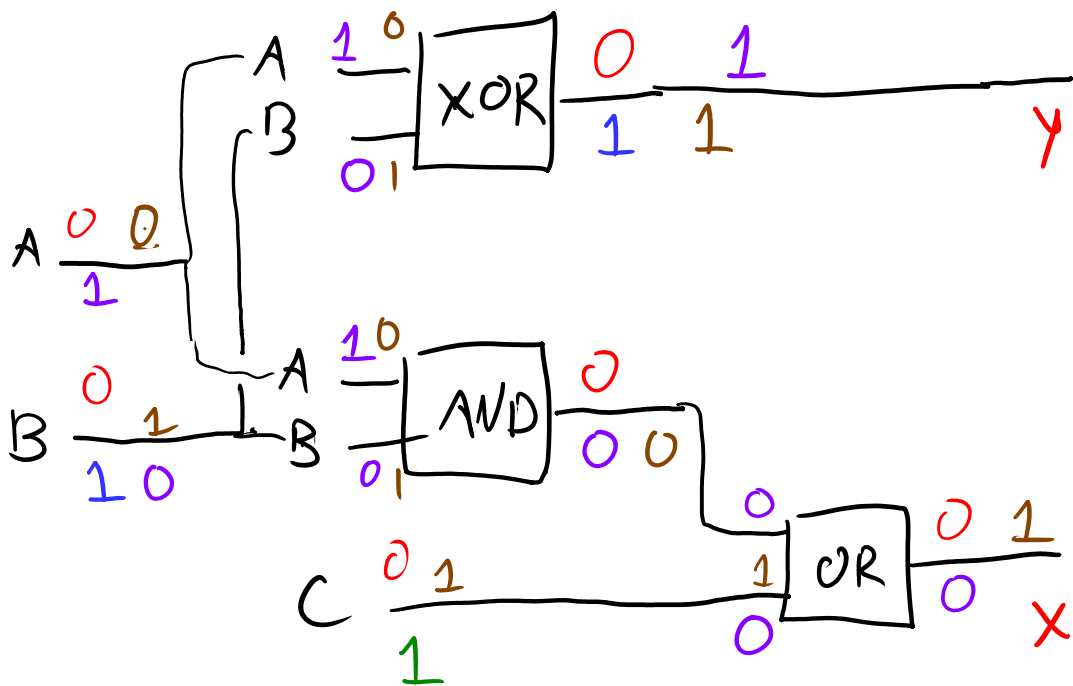






A	B	C	result
0	0	0	
0	0	1	
→ 0	1	0	1
0	1	1	
→ 1	0	0	1
1	0	1	0
1	1	0	
1	1	1	1

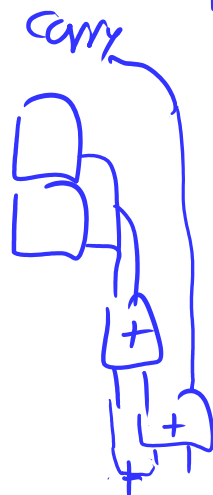
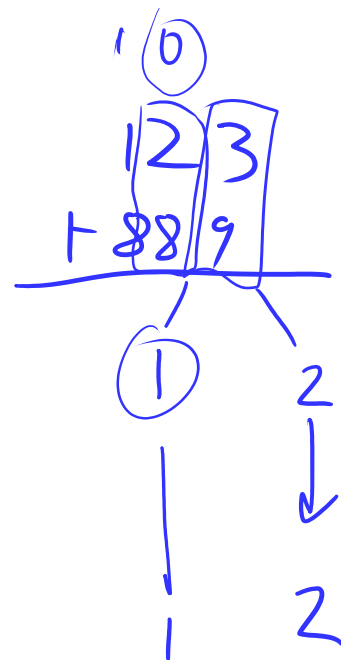
What does And do (match?)
new circuit example



A	B	C	X	Y
0	0	0	0	0
0	0	1	1	0
0	1	0	0	1
1	0	0	0	1
0	1	1	1	1

A	B	A+B
0	0	00
0	1	01
1	0	01
1	1	10

AND
XOR



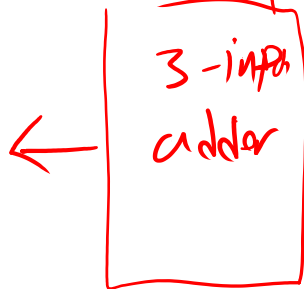
+

0

1

1

1



Carry

OR

+

+

1

0

3-input
adder

Carry

+

and xor

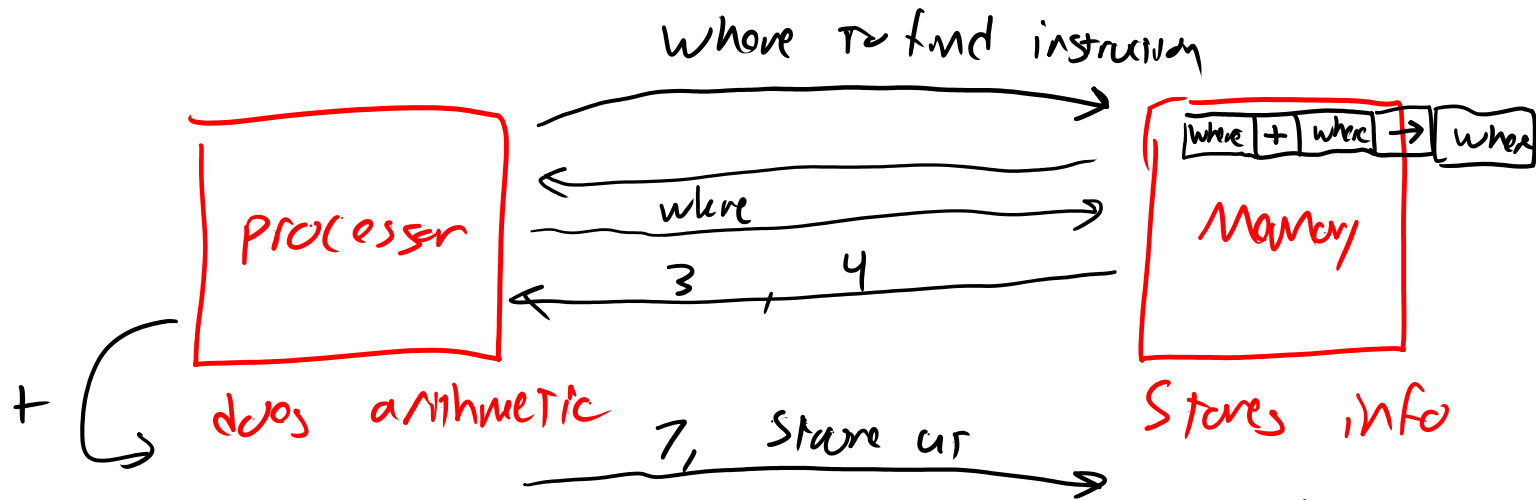
0

0









Speed

thing / second Hz

2.4 GHz

Size

B (bytes)

16 GB	RAM
1.5 TB	disk

Fast
\$
needs power