Superconducting Quantum Memory with a Suspended Coaxial Resonator Coaxial Resonator

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Why is Quantum Memory Needed?

- Quantum Communication: Enables long-distance secure data transfer.
- Quantum Computing: Stores intermediate results for quantum algorithms.
- Quantum Networks: Links quantum devices for sharing entanglement.

Meet the classical modes of a resonator

$$
\omega=\pi c\sqrt{\frac{n_x^2}{a_x^2}+\frac{n_y^2}{a_y^2}+\frac{n_z^2}{a_z^2}}
$$

Eg. 3D cavity Eg. 2D resonator

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Quantizing the LC oscillator

We cannot uniquely address the harmonic oscillator eigenstates with a classical drive.

A. Blais et al., Rev. Mod. Phys. (2021)

An anharmonic LC oscillator: the transmon qubit

A. Blais et al., Rev. Mod. Phys. (2021)

$$
\hat{H}=\frac{E_C}{4}\hat{Q}^2-E_J{\rm cos}(\hat{\varphi})
$$

expanding the cosine up to fourth order:

$$
\hat{H} \approx \hbar \omega_q \hat{a}^\dagger \hat{a} - \frac{E_C}{2} \hat{a}^\dagger \hat{a}^\dagger \hat{a} \hat{a}
$$

Unequally spaced levels allow us to isolate two levels as our qubit subspace.

Transmon-resonator interaction in the dispersive regime

dispersive coupling

Dispersive readout of transmon/resonator

$$
\hat{H} \approx \hbar \omega_r \hat{a}^\dagger \hat{a} + \frac{\hbar \omega_q}{2} \hat{\sigma}_z + \hbar \chi \hat{a}^\dagger \hat{a} \hat{\sigma}_z
$$
\n
$$
\hat{H} \approx \hbar \hat{a}^\dagger \hat{a} (\omega_r + \chi \hat{\sigma}_z) + \frac{\hbar \omega_q}{2} \hat{\sigma}_z
$$
\nresonator frequency
\ndepends on the qubit
\nstate\n
$$
\hat{H} \approx \hbar \omega_r \hat{a}^\dagger \hat{a} + \frac{\hbar \hat{\sigma}_z}{2} (\omega_q + 2\chi \hat{a}^\dagger \hat{a})
$$
\nqubit frequency
\non the resonator

A. Blais et al., Rev. Mod. Phys. (2021)

measure with a probe tone at this frequency

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Motivation for the paper

Quantum Information Storage Challenges:

- Motivation for the paper

example of the paper

wantum Information Storage Challenges:

 Require robust, high-coherence quantum

memories for storing quantum states.

 Trade-offs between coherence time, scalability, memories for storing quantum states.
- **Motivation for the paper

Quantum Information Storage Challenges:**

 Require robust, high-coherence quantum

memories for storing quantum states.

 Trade-offs between coherence time, scalability,

and ease of fabricati

- low surface participation, reducing losses but are hard to integrate with qubits.
- suffer from high losses due to surface defects.

LEV KRAYZMAN

Summary of the paper

Proposed Solution:

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 Introduces a hybrid resonator design:

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 Thin-film conductor supported by a dielectric s

 Encased within a 3D package for enhanced co **Mary of the paper**

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○ Thin-film conductor supported by a dielectric scaffold.

○ Encased within a 3D package for enhanced coherence.

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Key Results:

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 Thin-film conductor supported by a dielectric scaffold.

 Encased within a 3D package for enhanced coherence.

 Achieved single-photon lifetimes exceeding one **Example 19 Solution:**

■ Introduces a hybrid resonator design:

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Significance:

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- **•** Introduces a hybrid resonator design:

 Thin-film conductor supported by a dielectric scaffold.

 Thin-film conductor supported by a dielectric scaffold.

 Results:

 Achieved single-photon lifetimes exceeding one Modularity allows independent replacement of qubit and resonator components, essential for scalable quantum systems.

2.5D architecture of the device

3D render of the main piece of the device (cross-section view)

Read out

top and side view diagrams of the device

Center conductor fabrication

1.Laser cut Silicon/Sapphire into cross shape.

2. Rotational evaporation coat one stick in the cross with thin-film .

https://doi.org/10.1016/C2018-0-04648-4

Previous work: on-chip resonators

-
- **IS WOrk: On-chip resonators**

 High material quality and well-suited for integration with qubits.

 Limitation: High surface participation can lead to significant energy
 $\frac{T_1}{T_2}$
 $\frac{1}{T_1}$ losses.

S. Ganjam et al., Surpassing millisecond coherence times in on-chip superconducting quantum memories by optimizing materials, processes, and circuit design, arXiv:2308.15539 [quant-ph] (2023).

Relaxation time

Coherence time

Previous work: 3D cavity resonators

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- **WORE: 3D cavity resonators**

 Achieved long coherence times due to minimization of surface losses.

 Limitation: Complex fabrication and lack of scalability for quantum processors.

 $\frac{1}{2}$ (a) 0.8

M. Reagor et al., Quantum memory with millisecond coherence in circuit QED, Phys. Rev. B 94, 014506 (2016).

O. Milul, B. Guttel, U. Goldblatt, S. Hazanov, L. M. Joshi, D. Chausovsky, N. Kahn, E. Çiftyürek, F. Lafont, and S. Rosenblum, Superconducting Cavity Qubit with Tens of Milliseconds Single-Photon Coherence Time, PRX Quantum 4, 030336 (2023). 13

This work: Methods

- \bullet Use the $\lambda/2$ mode for storage, since it has a node in the center of the resonator
	- Minimizes dielectric loss
- Use 3λ/2 mode for readout
- Preparation of Fock states to measure decoherence lifetimes
	- \circ Two separate methods for measuring T₁

Limiting quality factors for different loss modes are given in the table to the right.

1 / $Q_i \equiv \sum_i \frac{p_i}{q_i}$

This work: Results

Citation analysis

The paper was published this year, and there's no citation…

The most cited references include following works done by Schoelkopf's group at Yale:

- **ttion analysis**

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● Chou et a
- **Physical review letters. [cited 209 times]**
- computing. npj Quantum Information. **[cited 180 times]**

Development of the field

Conclusion

- Hybrid Design: Utilizes a dielectric scaffold to support a thin-film conductor within a 3D package, merging low-loss characteristics of bulk resonators with the material quality control of thin-film circuits.
- Enhanced Coherence: Achieves single-photon lifetimes over one millisecond, indicating high coherence and reliability for quantum information storage.
- Scalability and Modularity: Allows for separate fabrication and replacement of qubit and resonator components, facilitating easier scaling and maintenance of quantum systems.

