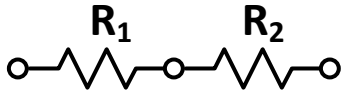


(a) Series:

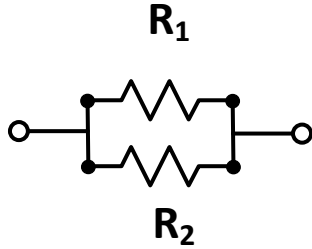
$$R_{eq} = \sum_{k=1}^N R_k$$



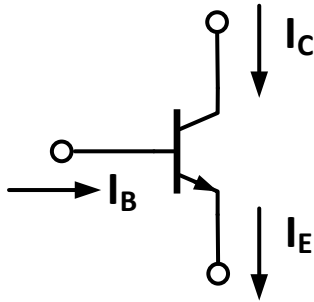
$$R_{eq} = R_1 + R_2$$

(b) Parallel:

$$\frac{1}{R_{eq}} = \sum_{k=1}^N \frac{1}{R_k}$$



$$R_{eq} = \frac{R_1 R_2}{R_1 + R_2}$$

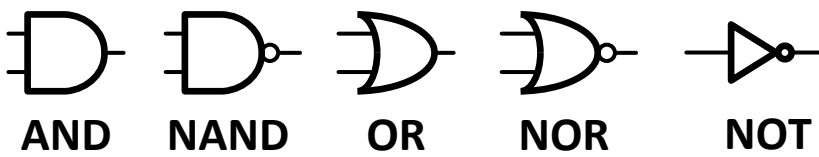


OFF: $V_{BE} < V_{BE} (on)$, $I_B = I_C = I_E = 0$

FA: $V_{BE} = V_{BE} (on)$, $I_C = \beta I_B$

SAT: $V_{BE} = V_{BE} (on)$, $V_{CE} = V_{CE} (sat)$

Basic Gates:



Selected rules of boolean algebra:

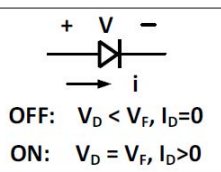
$$(a.b).c = a.(b.c); (a + b) + c = a + (b + c)$$

$$a.b = b.a; a + b = b + a$$

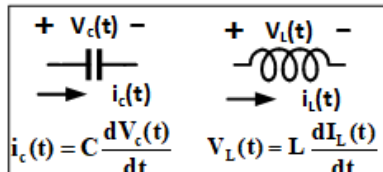
$$a.(b + c) = a.b + a.c$$

$$NOT(NOT(a)) = a$$

$$a + \bar{a}.b = a + b$$

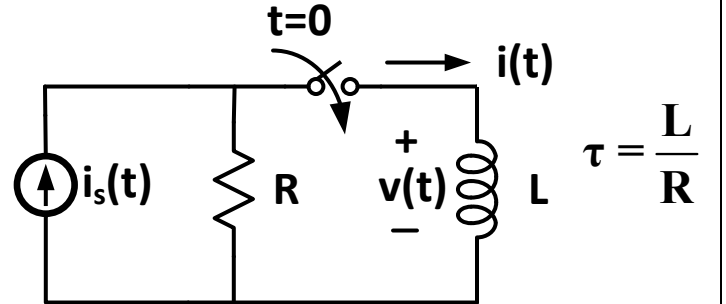
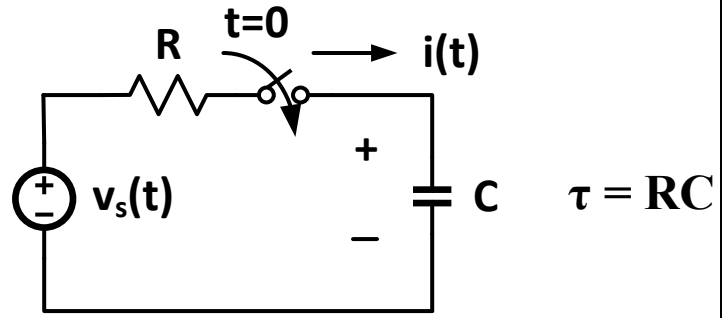


OFF: $V_D < V_F$, $I_D = 0$
ON: $V_D = V_F$, $I_D > 0$



$$i_c(t) = C \frac{dV_c(t)}{dt} \quad v_L(t) = L \frac{dI_L(t)}{dt}$$

RC and RL Circuits



Voltage/Current Divider

