

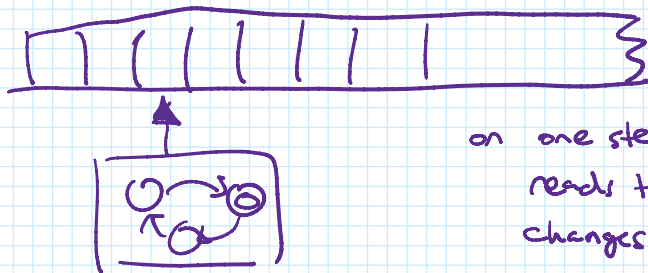
Lecture 9

Tuesday, 23 February, 2021 10:47

Today: A closer look at TMs

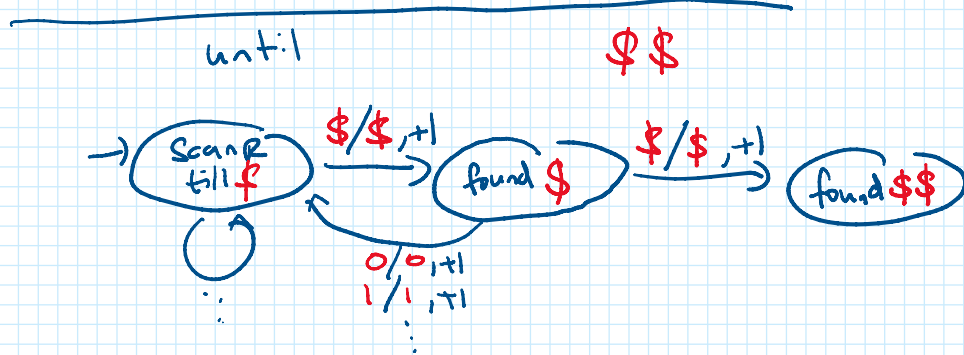
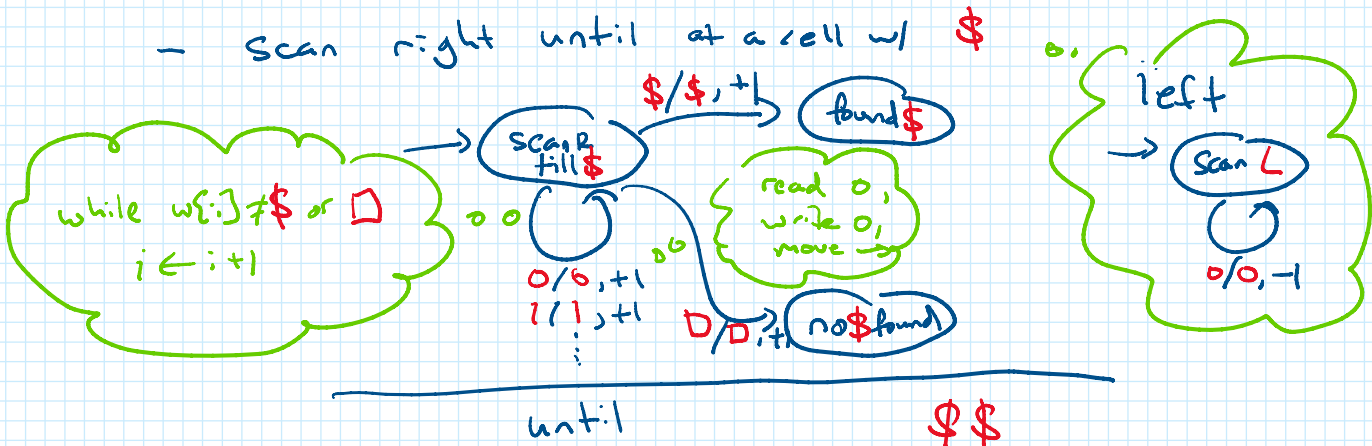
- Some basic subroutines
- the precise model doesn't matter b/c simulation
- TMs are code
- TMs can run code (universal TMs)

Recall:

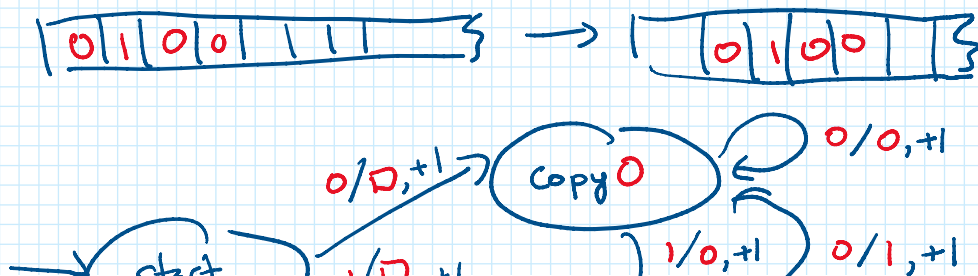


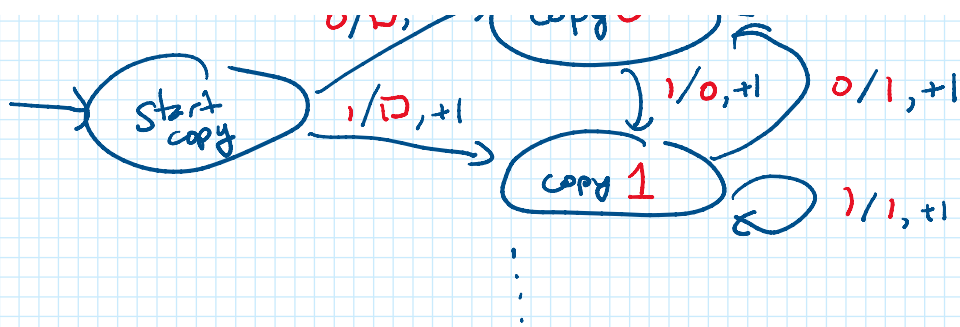
$\Gamma = \{0, 1, \square, x, \$\}$
 blank \square
 Σ (under 0, 1)
 Extra TM symbol $\{x, \$\}$
 on one step:
 reads tape, writes to tape
 changes state, moves \leftarrow/\rightarrow .

- scan right until at a cell w/ \$



- shift everything right.





- keep a binary counter

idea: - find the least significant bit

- flip it \oplus

if 1, move to "carry" state.

- or if carrying, move on.

→ "assume" that I can say things like

the TM will find the first 0

↳ failsafe: on \square

move to no 0 found state.

————— X —————

Many variations on TMs:

- in this class: defined TM to have 1 tape,
infinite to the right.

Q: what if you move \leftarrow from ?

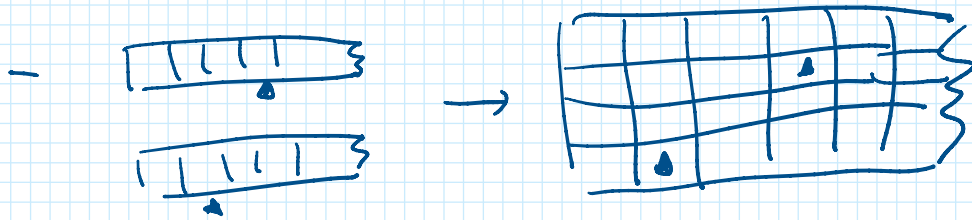
crash!

But... don't worry: always first shift everything \rightarrow .
so doesn't really matter.

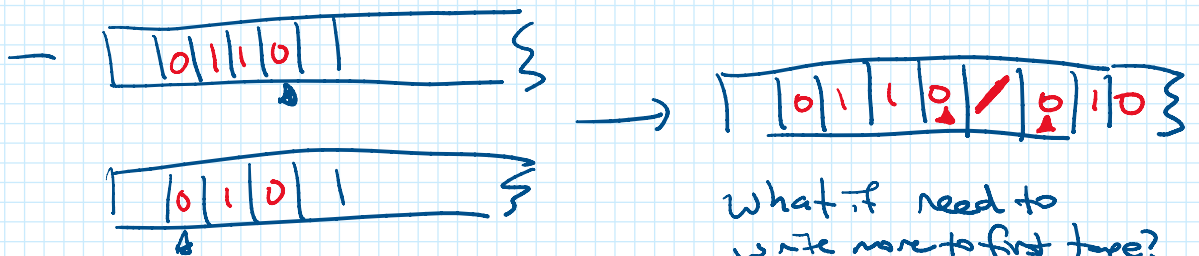
- some people define TMs to have multiple tapes.

one workaround: expand Γ to have "columns"

one workaround: expand Γ to have "columns" of symbols



to simulate multiple tapes:
for each tape, scan for \blacktriangle for that tape & execute 1 step.



what if need to write more to first tape?
shift everything else \rightarrow

a lot of this is bookkeeping on how your data is being stored



Aside: $L = \{w \mid w \text{ is a regex}\}$ is not regular.
but it is context-free. \rightarrow exercise

Q: is $L = \{w \mid w \text{ is a TM}\}$ decidable by a TM?

— understand what "w is a TM" means

— if a TM can tell if "w is a TM"

or `exec()`
in python
etc.

- if a TM can tell if "w is a TM" ^{or {exec, in python, etc.}}
 can we execute the TM described by w?

~~Universal Turing Machines~~

once again the precise model doesn't matter, because simulation
 a lot of bookkeeping

Suppose given

$$M = (Q, \Gamma, \text{start}, \text{acc}, \text{rej}, \delta)$$

$$= \{0, 1, \square, x, \$\}$$

create encoding over some other alphabet

$$\{0, 1, [,], \cdot, \emptyset, \frac{1}{2}, \square\}$$

make some arbitrary encoding decisions

$$\langle \square \rangle = 000$$

$$\langle 0 \rangle = 001$$

$$\langle 1 \rangle = 010$$

$$\langle x \rangle = 011$$

$$\langle \$ \rangle = 100$$

$$\langle \text{start} \rangle = 111$$

log |Q| bits

$$\langle \text{acc} \rangle = 001$$

$$\langle \text{rej} \rangle = 000$$

$$\langle \textcircled{p} \xrightarrow{a/b, \Delta} \textcircled{q} \rangle$$

$$= [[\langle p \rangle \cdot \langle a \rangle \cdot \langle b \rangle \cdot \langle q \rangle \cdot \langle \Delta \rangle]]$$

$\langle \delta \rangle$ = concat of all transitions.

$$\langle 001 \rangle = [001 \cdot 001 \cdot 010]$$

$$\langle M \rangle = [\langle \text{rej} \rangle \cdot \langle \square \rangle] [\langle \delta \rangle]$$

this allows us to sketch a TM that takes in
 $\langle M \rangle \cdot \langle w \rangle$ and then run M on input w.

UTM w/ 3 tapes

- input tape (won't modify it)
- state tape remembers current state of M
- work tape all computations here.

• initialization:

copy $\langle w \rangle$ to work tape.

mark first symbol of $\langle w \rangle$

$\oplus \rightarrow \ominus, 1 \rightarrow \underline{1}$

copy $\langle \text{start} \rangle$ to state tape

scan on input until start of $\langle w \rangle$.
Copy: read on input, write same thing in work, move \rightarrow

• loop:

- find marked char on work tape.

\rightarrow gives us $\langle a \rangle$

- Scan input tape for $[[\langle p \rangle \cdot \langle a \rangle \cdot \langle b \rangle \cdot \langle q \rangle \cdot \langle \Delta \rangle]]$

where $\langle p \rangle$ is on state tape.

- change $\langle a \rangle$ to $\langle b \rangle$ on work tape

- change $\langle p \rangle$ to $\langle q \rangle$ on state tape

- mark the beginning of the encoding of next symbol

- If state tape is $\langle \text{acc} \rangle$ or $\langle \text{rej} \rangle$
accept / reject accordingly.

TM for simulating assembly + RAM?

given: encoding of set of assembly instructions.

- keep track of what line we're at via a binary counter tape.
- scan until the right line by counting on another tape
- "remember" instruction read on instruction tape
- run whatever instruction on RAM tape

We are all Universal Turing Machines

Bonus Halting problem & Friends X

o o o o

Strictly optional for today
for the course
will covered again in
April/May

Define: L is decidable if \exists TM M s.t.

$w \in L, \quad M \text{ accepts}$
 $w \notin L, \quad M \text{ rejects}$

} never infinite loops

$SELFREJECT = \{ \langle M \rangle \mid M \text{ rejects } \langle M \rangle \}$.

Suppose \exists TM M_{SR} that decides $SELFREJECT$.

$\circ \circ$
 accepts $\langle M \rangle \in SELFREJECT$
 rejects $\langle M \rangle \notin SELFREJECT$
 never loops

$M_{SR} \text{ accepts } \langle M \rangle \iff M \text{ rejects } \langle M \rangle$
 plug in $\langle M_{SR} \rangle$ for $\langle M \rangle$

↓

$M_{SR} \text{ accepts } \langle M_{SR} \rangle \iff M_{SR} \text{ rejects } \langle M_{SR} \rangle.$

contradiction!

→ no TM deciding $SELFREJECT$! It's undecidable.

$SELFHALT = \{ \langle M \rangle \mid M \text{ halts on } \langle M \rangle \}$

} ends in acc.

$SELFHALT = \{ \langle M \rangle \mid M \text{ halts on } \langle M \rangle \}$

Suppose \exists TM M_{SH} deciding $SELFHALT$

ends in acc or rej state

$M_{SH} \rightarrow \overline{M_{SH}}$ by changing all transitions to acc to a new loop state

all transitions to rej to acc.

$\overline{M_{SH}}$ accepts $\langle M \rangle \Leftrightarrow M_{SH}$ rejects $\langle M \rangle \Leftrightarrow M$ does not halt on $\langle M \rangle$
plug in $\overline{M_{SH}}$

$\overline{M_{SH}}$ accepts $\langle \overline{M_{SH}} \rangle \Leftrightarrow \overline{M_{SH}}$ does not halt on $\langle \overline{M_{SH}} \rangle$.
halt
contradiction
no TM deciding $SELFHALT!$