

Circuit satisfiability and Cook-Levin Theorem

Lecture 24

Thursday, December 1, 2022

24.1

Recap

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NP: languages that have non-deterministic polynomial time algorithms

A language **L** is **NP-Complete** if and only if

- ▶ **L** is in **NP**
- ▶ for every **L'** in **NP**, $L' \leq_P L$

L is **NP-Hard** if for every **L'** in **NP**, $L' \leq_P L$.

Theorem 24.1 (Cook-Levin).

SAT is **NP-Complete**.

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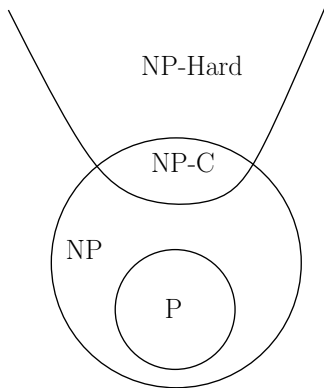
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Pictorial View



P and NP

Possible scenarios:

1. $P = NP$.
2. $P \neq NP$

Question: Suppose $P \neq NP$. Is every problem in $NP \setminus P$ also **NP-Complete**?

Theorem 24.2 (Ladner).

If $P \neq NP$ then there is a problem/language $X \in NP \setminus P$ such that X is not NP-Complete.

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What do we know so far

1. **Independent Set \leq_P Clique, Clique \leq_P Independent Set.**
 \implies Clique \cong_P Independent Set.
2. **Vertex Cover \leq_P Independent Set, Independent Set \leq_P Vertex Cover.**
 \implies Independent Set \cong_P Vertex Cover.
3. **3SAT \leq_P SAT, SAT \leq_P 3SAT \implies 3SAT \cong_P SAT.**
4. **3SAT \leq_P Independent Set .**
Exercise (or Cook-Levin theorem): **Independent Set \leq_P SAT**
 \implies 3SAT \cong_P Independent Set.
5. **SAT \leq_P Hamiltonian Cycle**
Exercise (or Cook-Levin theorem): **Hamiltonian Cycle \leq_P 3SAT**
 \implies Hamiltonian Cycle \cong_P 3SAT
6. **Clique \cong_P Independent Set \cong_P Vertex Cover \cong_P 3SAT**
 \cong_P SAT \cong_P Hamiltonian Cycle

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1. **Independent Set \leq_P Clique, Clique \leq_P Independent Set.**
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NP Completeness

Clique \cong_P **Independent Set** \cong_P **Vertex Cover** \cong_P **3SAT** \cong_P **SAT** \cong_P
Hamiltonian Cycle

All these problems are in **NP**.

SAT is **NPC**.

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24.2

Circuit SAT

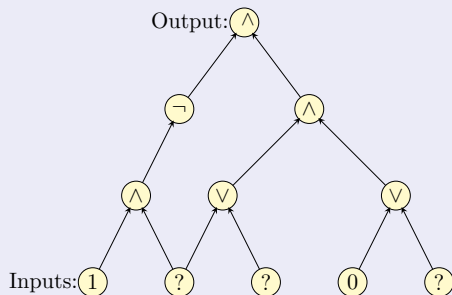
24.2.1

The circuit satisfiability (CSAT) problem

Circuits

Definition 24.1.

A circuit is a directed acyclic graph with



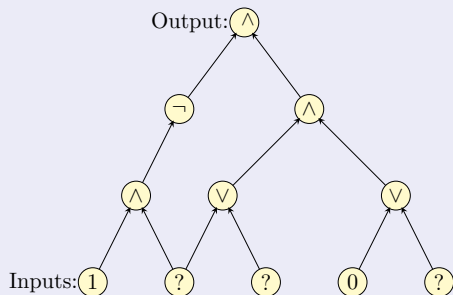
1. **Input** vertices (without incoming edges) labelled with **0**, **1** or a distinct variable.
2. Every other vertex is labelled \vee , \wedge or \neg .
3. Single node **output** vertex with no outgoing edges.

Can safely assume every node has at most two incoming edges.

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CSAT: Circuit Satisfaction

Definition 24.2 (Circuit Satisfaction (CSAT)).

Given a circuit as input, is there an assignment to the input variables that causes the output to get value **1**?

Claim 24.3.

CSAT is in NP.

1. **Certificate:** Assignment to input variables.
2. **Certifier:** Evaluate the value of each gate in a topological sort of DAG and check the output gate value.

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Circuit SAT vs SAT

CNF formulas are a rather restricted form of Boolean formulas.

Circuits are a much more powerful (and hence easier) way to express Boolean formulas

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Converting a CNF formula into a Circuit

3SAT \leq_P CSAT

Given 3CNF formula φ with n variables and m clauses, create a Circuit C .

- ▶ Inputs to C are the n boolean variables x_1, x_2, \dots, x_n
- ▶ Use NOT gate to generate literal $\neg x_i$ for each variable x_i
- ▶ For each clause $(\ell_1 \vee \ell_2 \vee \ell_3)$ use two OR gates to mimic formula
- ▶ Combine the outputs for the clauses using AND gates to obtain the final output

Example

3SAT \leq_P CSAT

$$\varphi = (x_1 \vee x_3 \vee x_4) \wedge (x_1 \vee \neg x_2 \vee \neg x_3) \wedge (\neg x_2 \vee \neg x_3 \vee x_4)$$

Example

3SAT \leq_P CSAT

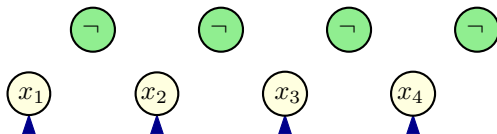
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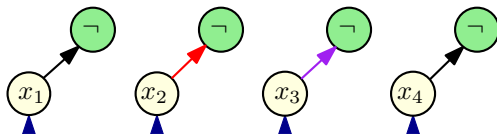
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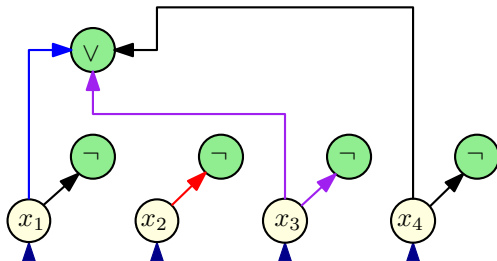
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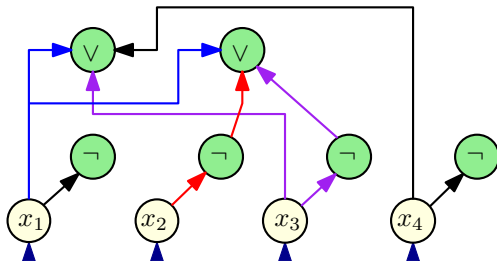
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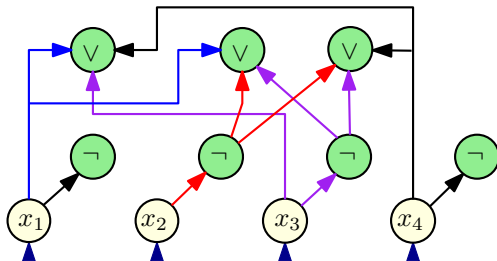
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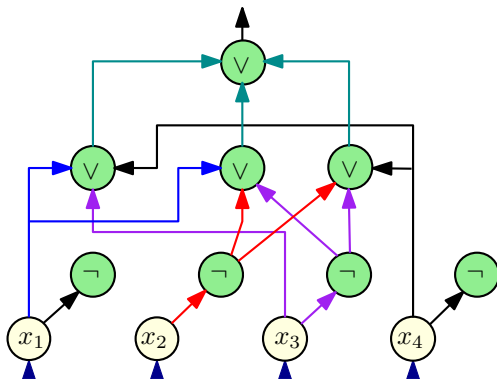
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$$3\text{SAT} \leq_P \text{CSAT}$$

Lemma 24.4.

$$\text{SAT} \leq_P 3\text{SAT} \leq_P \text{CSAT}.$$

24.2.2

Towards reducing CSAT to 3SAT

Converting $z = x \wedge y$ to 3SAT

z	x	y						
0	0	0						
0	0	1						
0	1	0						
0	1	1						
1	0	0						
1	0	1						
1	1	0						
1	1	1						

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$					
0	0	0	1					
0	0	1	1					
0	1	0	1					
0	1	1	0					
1	0	0	0					
1	0	1	0					
1	1	0	0					
1	1	1	1					

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$				
0	0	0	1	1	1	1	1
0	0	1	1	1	1	1	1
0	1	0	1	1	1	1	1
0	1	1	0	0	1	1	1
1	0	0	0	1	0	1	1
1	0	1	0	1	1	0	1
1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$			
0	0	0	1	1	1	1	1
0	0	1	1	1	1	1	1
0	1	0	1	1	1	1	1
0	1	1	0	0	1	1	1
1	0	0	0	1	0	1	1
1	0	1	0	1	1	0	1
1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$	$\bar{z} \vee x \vee y$		
0	0	0	1	1	1	1	1
0	0	1	1	1	1	1	1
0	1	0	1	1	1	1	1
0	1	1	0	0	1	1	1
1	0	0	0	1	0	1	1
1	0	1	0	1	1	0	1
1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$	$\bar{z} \vee x \vee y$	$\bar{z} \vee x \vee \bar{y}$	
0	0	0	1	1	1	1	1
0	0	1	1	1	1	1	1
0	1	0	1	1	1	1	1
0	1	1	0	0	1	1	1
1	0	0	0	1	0	1	1
1	0	1	0	1	1	0	1
1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$	$\bar{z} \vee x \vee y$	$\bar{z} \vee x \vee \bar{y}$	$\bar{z} \vee \bar{x} \vee y$
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1	0	0	0	1	0	1	1
1	0	1	0	1	1	0	1
1	1	0	0	1	1	1	0
1	1	1	1	1	1	1	1

Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$	$\bar{z} \vee x \vee y$	$\bar{z} \vee x \vee \bar{y}$	$\bar{z} \vee \bar{x} \vee y$
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Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	$z \vee \bar{x} \vee \bar{y}$	$\bar{z} \vee x \vee y$	$\bar{z} \vee x \vee \bar{y}$	$\bar{z} \vee \bar{x} \vee y$
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$$(z = x \wedge y)$$

$$\equiv$$

$$(z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee x \vee \bar{y}) \wedge (\bar{z} \vee \bar{x} \vee y)$$

Converting $z = x \wedge y$ to 3SAT

z	x	y			
0	0	0			
0	0	1			
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Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$		
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0	0	1	1		
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0	1	1	0		
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Converting $z = x \wedge y$ to 3SAT

z	x	y	$z = x \wedge y$	clauses
0	0	0	1	
0	0	1	1	
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0	1	1	0	
1	0	0	0	
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z	x	y	$z = x \wedge y$	clauses
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$$(z = x \wedge y)$$

\equiv

$$(z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee x \vee \bar{y}) \wedge (\bar{z} \vee \bar{x} \vee y)$$

Converting $z = x \wedge y$ to 3SAT

Simplify further if you want to

1. Using that $(x \vee y) \wedge (x \vee \bar{y}) = x$, we have that:

$$1.1 \ (\bar{z} \vee x \vee u) \wedge (\bar{z} \vee x \vee \bar{y}) = (\bar{z} \vee x)$$

$$1.2 \ (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee \bar{x} \vee y) = (\bar{z} \vee y)$$

2. Using the above two observation, we have that our formula

$$\psi \equiv (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee x \vee \bar{y}) \wedge (\bar{z} \vee \bar{x} \vee y)$$

$$\text{is equivalent to } \psi \equiv (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$$

Lemma 24.5.

$$(z = x \wedge y) \quad \equiv \quad (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$$

Converting $z = x \wedge y$ to 3SAT

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2. Using the above two observation, we have that our formula

$$\psi \equiv (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee x \vee \bar{y}) \wedge (\bar{z} \vee \bar{x} \vee y)$$

is equivalent to $\psi \equiv (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$

Lemma 24.5.

$$(z = x \wedge y) \quad \equiv \quad (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$$

Converting $z = x \wedge y$ to 3SAT

Simplify further if you want to

1. Using that $(x \vee y) \wedge (x \vee \bar{y}) = x$, we have that:

$$1.1 \ (\bar{z} \vee x \vee u) \wedge (\bar{z} \vee x \vee \bar{y}) = (\bar{z} \vee x)$$

$$1.2 \ (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee \bar{x} \vee y) = (\bar{z} \vee y)$$

2. Using the above two observations, we have that our formula

$$\psi \equiv (\bar{z} \vee x \vee \bar{y}) \wedge (\bar{z} \vee x \vee y) \wedge (\bar{z} \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee \bar{x} \vee y)$$

$$\text{is equivalent to } \psi \equiv (\bar{z} \vee x) \wedge (\bar{z} \vee y)$$

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Converting $z = x \vee y$ to 3SAT

z	x	y			
0	0	0			
0	0	1			
0	1	0			
0	1	1			
1	0	0			
1	0	1			
1	1	0			
1	1	1			

Converting $z = x \vee y$ to 3SAT

z	x	y	$z = x \vee y$		
0	0	0	1		
0	0	1	0		
0	1	0	0		
0	1	1	0		
1	0	0	0		
1	0	1	1		
1	1	0	1		
1	1	1	1		

Converting $z = x \vee y$ to 3SAT

z	x	y	$z = x \vee y$	clauses
0	0	0	1	
0	0	1	0	
0	1	0	0	
0	1	1	0	
1	0	0	0	
1	0	1	1	
1	1	0	1	
1	1	1	1	

Converting $z = x \vee y$ to 3SAT

z	x	y	$z = x \vee y$	clauses
0	0	0	1	
0	0	1	0	$z \vee x \vee \bar{y}$
0	1	0	0	$z \vee \bar{x} \vee y$
0	1	1	0	$z \vee \bar{x} \vee \bar{y}$
1	0	0	0	$\bar{z} \vee x \vee y$
1	0	1	1	
1	1	0	1	
1	1	1	1	

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z	x	y	$z = x \vee y$	clauses
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0	0	1	0	$z \vee x \vee \bar{y}$
0	1	0	0	$z \vee \bar{x} \vee y$
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1	0	0	0	$\bar{z} \vee x \vee y$
1	0	1	1	
1	1	0	1	
1	1	1	1	

$$(z = x \vee y)$$

$$\equiv$$

$$(z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y)$$

Converting $z = x \vee y$ to 3SAT

Simplify further if you want to

$$(z = x \vee y) \equiv (z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y)$$

1. Using that $(x \vee y) \wedge (x \vee \bar{y}) = x$, we have that:

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$$1.2 \quad (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) = z \vee \bar{x}$$

2. Using the above two observation, we have the following.

Lemma 24.6.

The formula $z = x \vee y$ is equivalent to the CNF formula

$$(z = x \vee y) \quad \equiv \quad (z \vee \bar{y}) \wedge (z \vee \bar{x}) \wedge (\bar{z} \vee x \vee y)$$

Converting $z = x \vee y$ to 3SAT

Simplify further if you want to

$$(z = x \vee y) \equiv (z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y)$$

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$$1.1 (z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee \bar{y}) = z \vee \bar{y}.$$

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Converting $z = x \vee y$ to 3SAT

Simplify further if you want to

$$(z = x \vee y) \equiv (z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y)$$

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Converting $z = x \vee y$ to 3SAT

Simplify further if you want to

$$(z = x \vee y) \equiv (z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x \vee y)$$

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 - 1.1 $(z \vee x \vee \bar{y}) \wedge (z \vee \bar{x} \vee \bar{y}) = z \vee \bar{y}.$
 - 1.2 $(z \vee \bar{x} \vee y) \wedge (z \vee \bar{x} \vee \bar{y}) = z \vee \bar{x}$
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The formula $z = x \vee y$ is equivalent to the CNF formula

$$(z = x \vee y) \equiv (z \vee \bar{y}) \wedge (z \vee \bar{x}) \wedge (\bar{z} \vee x \vee y)$$

Converting $z = \bar{x}$ to CNF

Lemma 24.7.

$$z = \bar{x} \quad \equiv \quad (z \vee x) \wedge (\bar{z} \vee \bar{x}).$$

Summary of formulas we derived

Lemma 24.8.

The following identities hold:

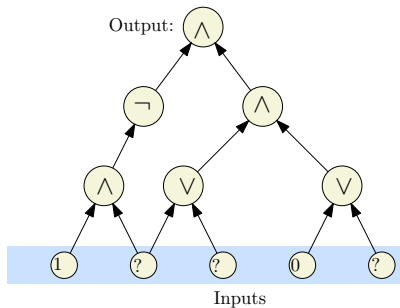
1. $z = \bar{x} \quad \equiv \quad (z \vee x) \wedge (\bar{z} \vee \bar{x}) .$
2. $(z = x \vee y) \quad \equiv \quad (z \vee \bar{y}) \wedge (z \vee \bar{x}) \wedge (\bar{z} \vee x \vee y)$
3. $(z = x \wedge y) \quad \equiv \quad (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$

24.2.3

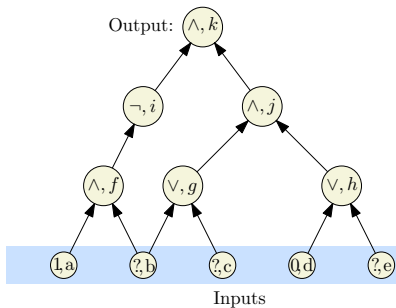
Reduction from CSAT to SAT

Converting a circuit into a CNF formula

Label the nodes



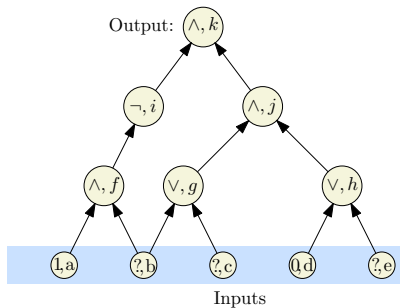
(A) Input circuit



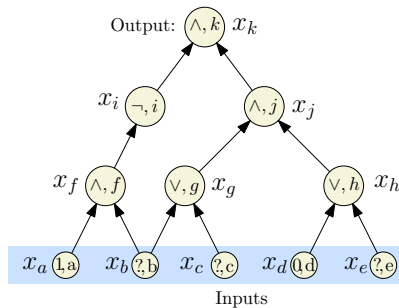
(B) Label the nodes.

Converting a circuit into a CNF formula

Introduce a variable for each node



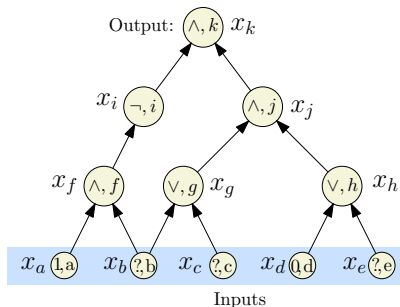
(B) Label the nodes.



(C) Introduce var for each node.

Converting a circuit into a CNF formula

Write a sub-formula for each variable that is true if the var is computed correctly.



(C) Introduce var for each node.

x_k (Demand a sat' assignment!)

$$x_k = x_i \wedge x_j$$

$$x_j = x_g \wedge x_h$$

$$x_i = \neg x_f$$

$$x_h = x_d \vee x_e$$

$$x_g = x_b \vee x_c$$

$$x_f = x_a \wedge x_b$$

$$x_d = 0$$

$$x_a = 1$$

(D) Write a sub-formula for each variable that is true if the var is computed correctly.

Converting a circuit into a CNF formula

Convert each sub-formula to an equivalent CNF formula

x_k	x_k
$x_k = x_i \wedge x_j$	$(\neg x_k \vee x_i) \wedge (\neg x_k \vee x_j) \wedge (x_k \vee \neg x_i \vee \neg x_j)$
$x_j = x_g \wedge x_h$	$(\neg x_j \vee x_g) \wedge (\neg x_j \vee x_h) \wedge (x_j \vee \neg x_g \vee \neg x_h)$
$x_i = \neg x_f$	$(x_i \vee x_f) \wedge (\neg x_i \vee \neg x_f)$
$x_h = x_d \vee x_e$	$(x_h \vee \neg x_d) \wedge (x_h \vee \neg x_e) \wedge (\neg x_h \vee x_d \vee x_e)$
$x_g = x_b \vee x_c$	$(x_g \vee \neg x_b) \wedge (x_g \vee \neg x_c) \wedge (\neg x_g \vee x_b \vee x_c)$
$x_f = x_a \wedge x_b$	$(\neg x_f \vee x_a) \wedge (\neg x_f \vee x_b) \wedge (x_f \vee \neg x_a \vee \neg x_b)$
$x_d = 0$	$\neg x_d$
$x_a = 1$	x_a

From **Lemma 24.8** :

- $z = \bar{x} \quad \equiv \quad (z \vee x) \wedge (\bar{z} \vee \bar{x})$
- $(z = x \vee y) \quad \equiv \quad (z \vee \bar{y}) \wedge (z \vee \bar{x}) \wedge (\bar{z} \vee x \vee y)$
- $(z = x \wedge y) \quad \equiv \quad (z \vee \bar{x} \vee \bar{y}) \wedge (\bar{z} \vee x) \wedge (\bar{z} \vee y)$

Converting a circuit into a CNF formula

Convert each sub-formula to an equivalent CNF formula

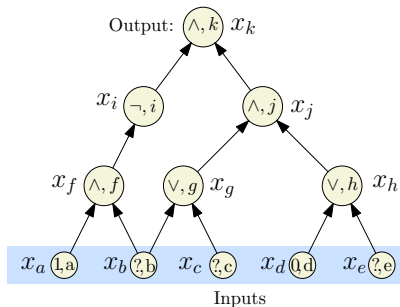
x_k	x_k
$x_k = x_i \wedge x_j$	$(\neg x_k \vee x_i) \wedge (\neg x_k \vee x_j) \wedge (x_k \vee \neg x_i \vee \neg x_j)$
$x_j = x_g \wedge x_h$	$(\neg x_j \vee x_g) \wedge (\neg x_j \vee x_h) \wedge (x_j \vee \neg x_g \vee \neg x_h)$
$x_i = \neg x_f$	$(x_i \vee x_f) \wedge (\neg x_i \vee \neg x_f)$
$x_h = x_d \vee x_e$	$(x_h \vee \neg x_d) \wedge (x_h \vee \neg x_e) \wedge (\neg x_h \vee x_d \vee x_e)$
$x_g = x_b \vee x_c$	$(x_g \vee \neg x_b) \wedge (x_g \vee \neg x_c) \wedge (\neg x_g \vee x_b \vee x_c)$
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Converting a circuit into a CNF formula

Take the conjunction of all the CNF sub-formulas



$$\begin{aligned} & x_k \wedge (\neg x_k \vee x_i) \wedge (\neg x_k \vee x_j) \\ & \wedge (x_k \vee \neg x_i \vee \neg x_j) \wedge (\neg x_j \vee x_g) \\ & \wedge (\neg x_j \vee x_h) \wedge (x_j \vee \neg x_g \vee \neg x_h) \\ & \wedge (x_i \vee x_f) \wedge (\neg x_i \vee \neg x_f) \\ & \wedge (x_h \vee \neg x_d) \wedge (x_h \vee \neg x_e) \\ & \wedge (\neg x_h \vee x_d \vee x_e) \wedge (x_g \vee \neg x_b) \\ & \wedge (x_g \vee \neg x_c) \wedge (\neg x_g \vee x_b \vee x_c) \\ & \wedge (\neg x_f \vee x_a) \wedge (\neg x_f \vee x_b) \\ & \wedge (x_f \vee \neg x_a \vee \neg x_b) \wedge (\neg x_d) \wedge x_a \end{aligned}$$

We got a CNF formula that is satisfiable if and only if the original circuit is satisfiable.

Correctness of Reduction

Need to show circuit \mathbf{C} is satisfiable if and only if $\varphi_{\mathbf{C}}$ is satisfiable

\Rightarrow Consider a satisfying assignment \mathbf{a} for \mathbf{C}

1. Find values of all gates in \mathbf{C} under \mathbf{a}
2. Give value of gate \mathbf{v} to variable $\mathbf{x_v}$; call this assignment $\mathbf{a'}$
3. $\mathbf{a'}$ satisfies $\varphi_{\mathbf{C}}$ (exercise)

\Leftarrow Consider a satisfying assignment \mathbf{a} for $\varphi_{\mathbf{C}}$

1. Let $\mathbf{a'}$ be the restriction of \mathbf{a} to only the input variables
2. Value of gate \mathbf{v} under $\mathbf{a'}$ is the same as value of $\mathbf{x_v}$ in \mathbf{a}
3. Thus, $\mathbf{a'}$ satisfies \mathbf{C}

The result

Lemma 24.9.

$\text{CSAT} \leq_P \text{SAT} \leq_P \text{3SAT}$.

Theorem 24.10.

CSAT is NP-Complete.

The result

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$\text{CSAT} \leq_P \text{SAT} \leq_P \text{3SAT}$.

Theorem 24.10.

CSAT is **NP-Complete**.

24.3

NP-Completeness of Graph Coloring

24.3.1

The coloring problem

Graph Coloring

Problem: Graph Coloring

Instance: $G = (V, E)$: Undirected graph, integer k .

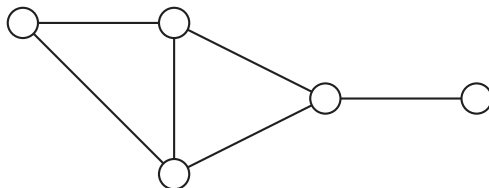
Question: Can the vertices of the graph be colored using k colors so that vertices connected by an edge do not get the same color?

Graph 3-Coloring

Problem: 3 Coloring

Instance: $G = (V, E)$: Undirected graph.

Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?

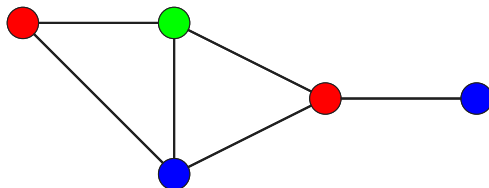


Graph 3-Coloring

Problem: 3 Coloring

Instance: $G = (V, E)$: Undirected graph.

Question: Can the vertices of the graph be colored using 3 colors so that vertices connected by an edge do not get the same color?



Graph Coloring

1. **Observation:** If G is colored with k colors then each color class (nodes of same color) form an independent set in G .
2. G can be partitioned into k independent sets $\iff G$ is k -colorable.
3. Graph 2-Coloring can be decided in polynomial time.
4. G is 2-colorable $\iff G$ is bipartite.
5. There is a linear time algorithm to check if G is bipartite using **BFS** (we saw this earlier).

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24.3.2

Problems related to graph coloring

Register allocation during compilation

1. When a compiler generates the assembly/VM code it needs to allocate registers to values being handled.
2. Need to make sure registers are not in conflict.
3. Build a conflict graph.
4. Color the conflict graph.
5. Every color is a register.
6. If not enough registers, then use memory/stack to store values.
7. CISC v.s. RISC.

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Graph Coloring and Register Allocation

Register Allocation

Assign variables to (at most) k registers such that variables needed at the same time are not assigned to the same register

Interference Graph

Vertices are variables, and there is an edge between two vertices, if the two variables are “live” at the same time.

Observations

- ▶ [Chaitin] Register allocation problem is equivalent to coloring the interference graph with k colors
- ▶ Moreover, $3\text{-COLOR} \leq_P k\text{-Register Allocation}$, for any $k \geq 3$

Class Room Scheduling

1. Given n classes and their meeting times, are k rooms sufficient?
2. Reduce to Graph k -Coloring problem
3. Create graph G
 - ▶ a node v_i for each class i
 - ▶ an edge between v_i and v_j if classes i and j conflict
4. Exercise: G is k -colorable \iff k rooms are sufficient.

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Frequency Assignments in Cellular Networks

1. Cellular telephone systems that use Frequency Division Multiple Access (FDMA) (example: GSM in Europe and Asia and AT&T in USA)
 - ▶ Breakup a frequency range $[a, b]$ into disjoint bands of frequencies $[a_0, b_0], [a_1, b_1], \dots, [a_k, b_k]$
 - ▶ Each cell phone tower (simplifying) gets one band
 - ▶ Constraint: nearby towers cannot be assigned same band, otherwise signals will interfere
2. **Problem:** given k bands and some region with n towers, is there a way to assign the bands to avoid interference?
3. Can reduce to k -coloring by creating interference/conflict graph on towers.

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24.3.3

Showing NP-Completeness of **3** **COLORING**

24.3.3.1

The variable assignment gadget

3-Coloring is NP-Complete

- ▶ **3-Coloring** is in **NP**.
 - ▶ **Certificate**: for each node a color from $\{1, 2, 3\}$.
 - ▶ **Certifier**: Check if for each edge (u, v) , the color of u is different from that of v .
- ▶ **Hardness**: We will show $3\text{-SAT} \leq_P 3\text{-Coloring}$.

Reduction idea

1. φ : Given **3SAT** formula (i.e., **3CNF** formula).
2. φ : variables x_1, \dots, x_n and clauses C_1, \dots, C_m .
3. Create graph G_φ s.t. G_φ 3-colorable $\iff \varphi$ satisfiable.
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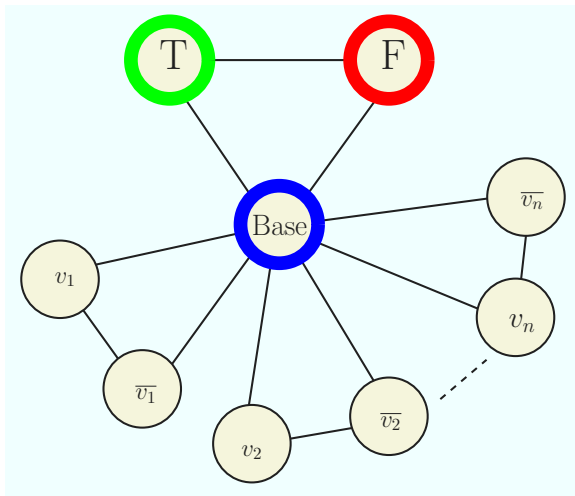
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Assignment encoding using **3**-coloring



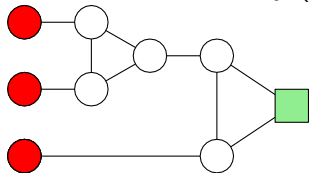
24.3.3.2

The clause gadget

3 color this gadget.

Clicker question

You are given three colors: red, green and blue. Can the following graph be three colored in a valid way (assuming the two nodes are already colored as indicated).



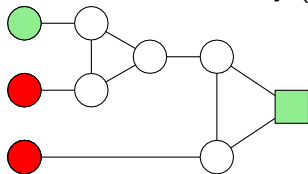
(A) Yes.

(B) No.

3 color this gadget II

Clicker question

You are given three colors: red, green and blue. Can the following graph be three colored in a valid way (assuming the two nodes are already colored as indicated).



(A) Yes.

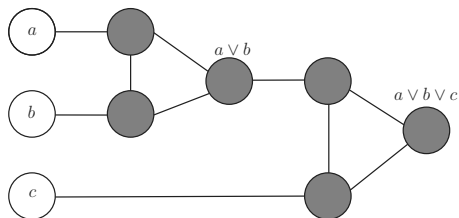
(B) No.

Clause Satisfiability Gadget

1. For each clause $C_j = (\mathbf{a} \vee \mathbf{b} \vee \mathbf{c})$, create a small gadget graph
 - ▶ gadget graph connects to nodes corresponding to $\mathbf{a}, \mathbf{b}, \mathbf{c}$
 - ▶ needs to implement OR
2. OR-gadget-graph:

Clause Satisfiability Gadget

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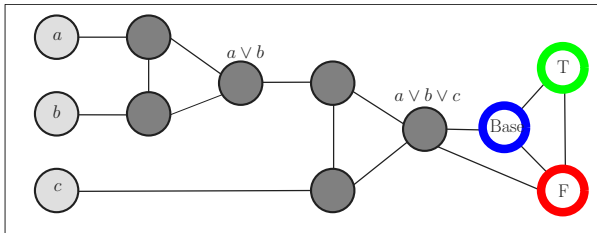
OR-Gadget Graph

Property: if **a, b, c** are colored False in a 3-coloring then output node of OR-gadget has to be colored False.

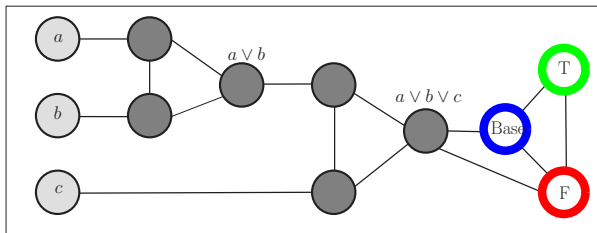
Property: if one of **a, b, c** is colored True then OR-gadget can be 3-colored such that output node of OR-gadget is colored True.

Reduction

- ▶ create triangle with nodes True, False, Base
- ▶ for each variable x_i two nodes v_i and \bar{v}_i connected in a triangle with common Base
- ▶ for each clause $C_j = (a \vee b \vee c)$, add OR-gadget graph with input nodes a, b, c and connect output node of gadget to both False and Base



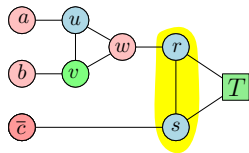
Reduction



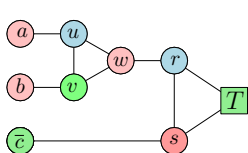
Claim 24.1.

No legal **3**-coloring of above graph (with coloring of nodes **T**, **F**, **B** fixed) in which **a**, **b**, **c** are colored False. If any of **a**, **b**, **c** are colored True then there is a legal **3**-coloring of above graph.

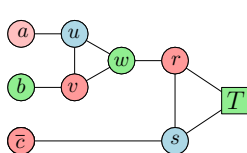
3 coloring of the clause gadget



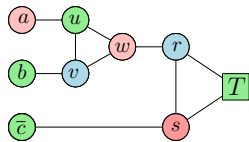
FFF - **BAD**



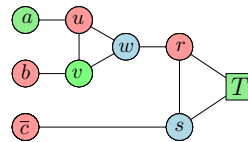
FFT



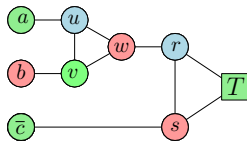
FTF



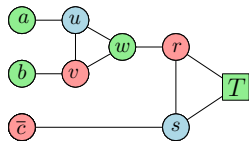
FTT



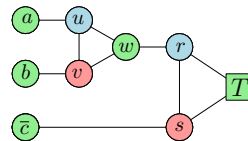
TFF



TFT



TTF

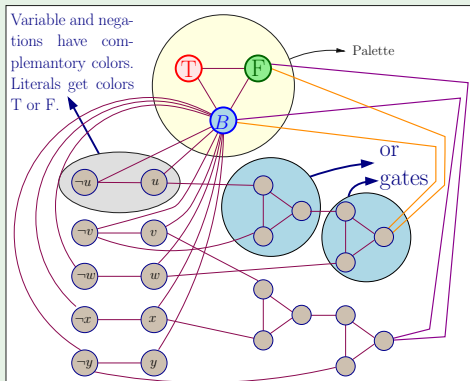


TTT

Reduction Outline

Example 24.2.

$$\varphi = (u \vee \neg v \vee w) \wedge (v \vee x \vee \neg y)$$



Correctness of Reduction

φ is satisfiable implies G_φ is 3-colorable

- ▶ if x_i is assigned True, color v_i True and \bar{v}_i False
- ▶ for each clause $C_j = (a \vee b \vee c)$ at least one of a, b, c is colored True. OR-gadget for C_j can be 3-colored such that output is True.

G_φ is 3-colorable implies φ is satisfiable

- ▶ if v_i is colored True then set x_i to be True, this is a legal truth assignment
- ▶ consider any clause $C_j = (a \vee b \vee c)$. it cannot be that all a, b, c are False. If so, output of OR-gadget for C_j has to be colored False but output is connected to Base and False!

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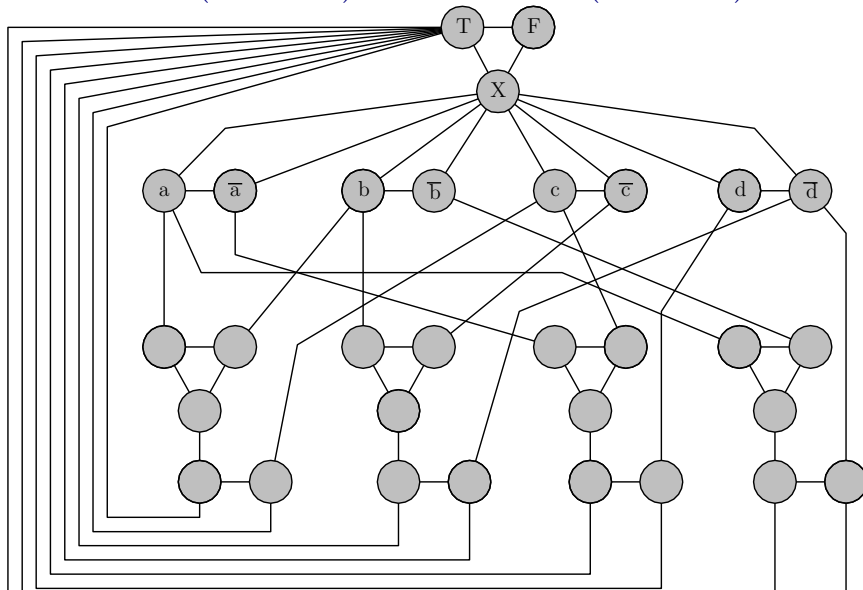
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Graph generated in reduction...

... from 3SAT to 3COLOR

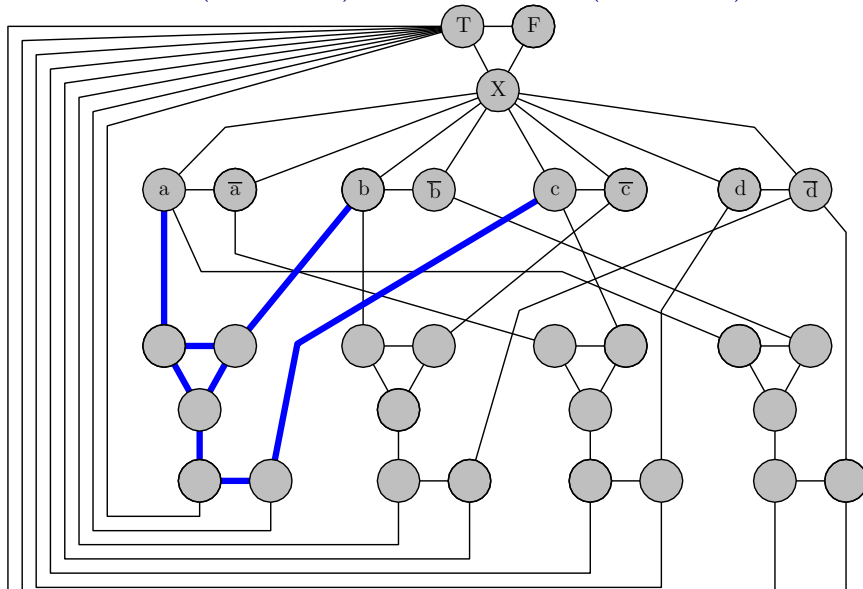
$$(a \vee b \vee c) \wedge (b \vee \bar{c} \vee \bar{d}) \wedge (\bar{a} \vee c \vee d) \wedge (a \vee \bar{b} \vee \bar{d})$$



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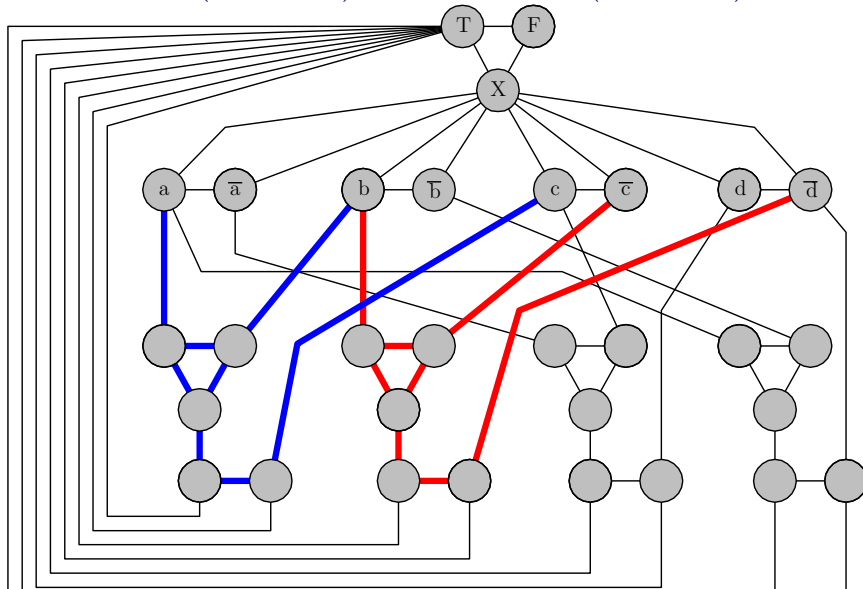
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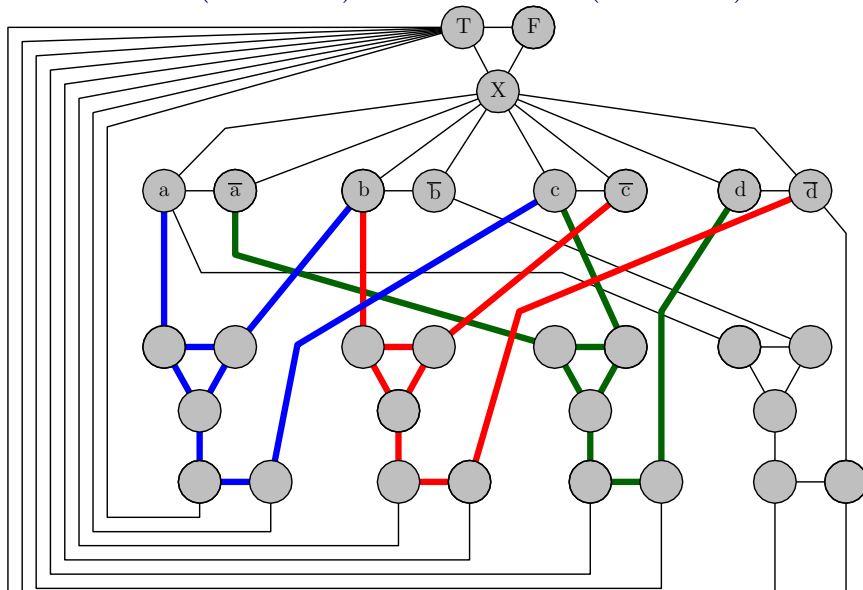
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Graph generated in reduction...

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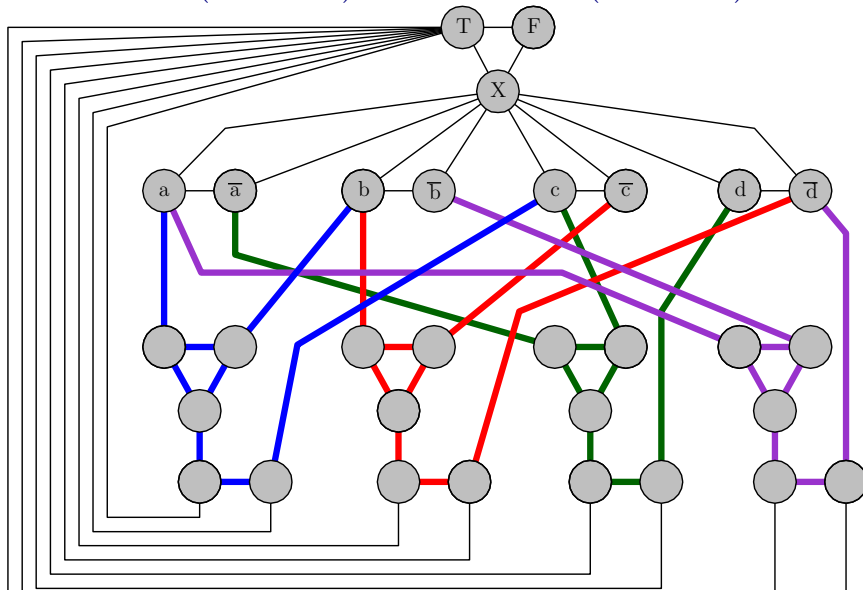
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Graph generated in reduction...

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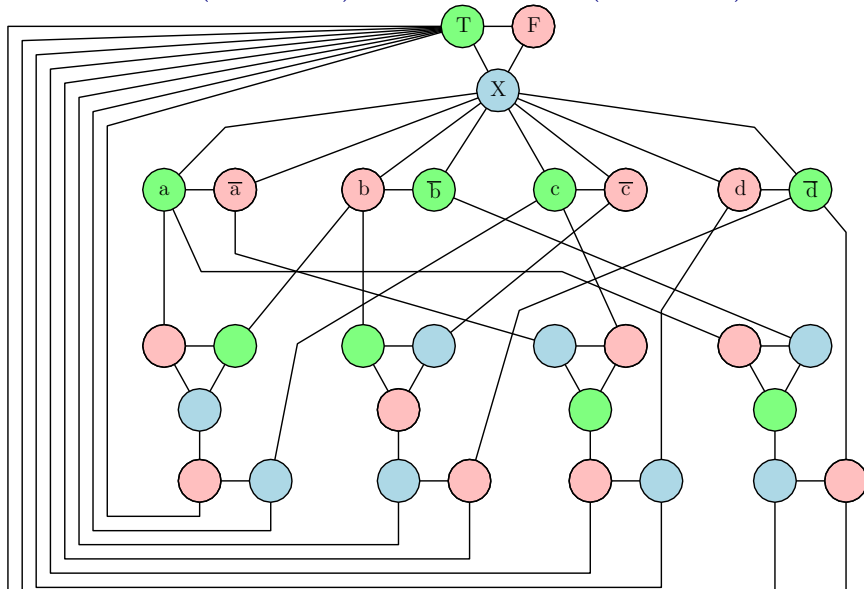
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24.4

Proof of Cook-Levin Theorem

24.4.1

Statement and sketch of idea for the proof

Cook-Levin Theorem

Theorem 24.1 (Cook-Levin).

SAT is **NP-Complete**.

We have already seen that **SAT** is in **NP**.

Need to prove that every language $L \in \mathbf{NP}$, $L \leq_P \mathbf{SAT}$

Difficulty: Infinite number of languages in **NP**. Must simultaneously show a generic reduction strategy.

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The plot against SAT

High-level plan to proving the Cook-Levin theorem

What does it mean that $L \in NP$?

$L \in NP$ implies that there is a non-deterministic TM M and polynomial $p()$ such that

$$L = \{x \in \Sigma^* \mid M \text{ accepts } x \text{ in at most } p(|x|) \text{ steps}\}$$

Input: M, x, p .

Question: Does M stop on input x after $p(|x|)$ steps?

Describe a reduction R that computes from M, x, p a SAT formula φ .

- ▶ R takes as input a string x and outputs a SAT formula φ
- ▶ R runs in time polynomial in $|x|, |M|$
- ▶ $x \in L$ if and only if φ is satisfiable

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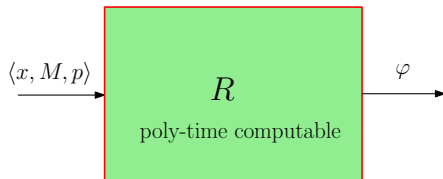
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The plot against SAT continued



φ is satisfiable if and only if $\mathbf{x} \in \mathbf{L}$

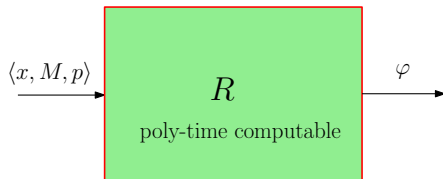
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BIG IDEA

- ▶ φ will express “ \mathbf{M} on input \mathbf{x} accepts in $\mathbf{p}(|\mathbf{x}|)$ steps”
- ▶ φ will encode a computation history of \mathbf{M} on \mathbf{x}

φ : CNF formula s.t if we have a satisfying assignment to it \implies accepting computation of \mathbf{M} on \mathbf{x} down to the last details (where the head is, what transition is chosen, what the tape contents are, at each step, etc).

The plot against SAT continued



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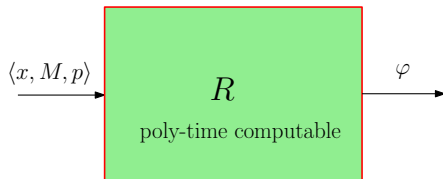
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The plot against SAT continued



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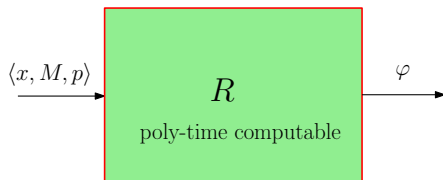
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The Matrix Executions

Tableau of Computation

M runs in time $p(|x|)$ on **x**. Entire computation of **M** on **x** can be represented by a “tableau”

		tape cell position					
		1	2	3	4	-----	$p(x)$
time	0	<u>1</u>	0	0	1	blanks	state q_0
	1	0	<u>0</u>	0	1	blanks	state q_2
	2						
	3						
	⋮						
	$p(x)$						

Row **i** gives contents of all cells at time **i**

At time **0** tape has input **x** followed by blanks

Each row long enough to hold all cells **M** might ever have scanned.

Variables of φ

Four types of variables to describe computation of M on x

- ▶ $T(b, h, i)$: tape cell at position h holds symbol b at time i .
For $h = 1, \dots, p(|x|)$, $b \in \Gamma$, $i = 0, \dots, p(|x|)$.
- ▶ $H(h, i)$: read/write head is at position h at time i .
For $h = 1, \dots, p(|x|)$, and $i = 0, \dots, p(|x|)$
- ▶ $S(q, i)$ state of M is q at time i .
For all $q \in Q$ and $i = 0, \dots, p(|x|)$.
- ▶ $I(j, i)$ instruction number j is executed at time i
 M is non-deterministic, need to specify transitions in some way. Number transitions as $1, 2, \dots, \ell$ where j th transition is $\langle q_j, b_j, q'_j, b'_j, d_j \rangle$ indication $(q'_j, b'_j, d_j) \in \delta(q_j, b_j)$, direction $d_j \in \{-1, 0, 1\}$.

Number of variables is $O(p(|x|)^2 |M|^2)$

Notation

Some abbreviations for ease of notation

$\bigwedge_{k=1}^m x_k$ means $x_1 \wedge x_2 \wedge \dots \wedge x_m$

$\bigvee_{k=1}^m x_k$ means $x_1 \vee x_2 \vee \dots \vee x_m$

$\oplus(x_1, x_2, \dots, x_k)$ is a formula that means **exactly one** of x_1, x_2, \dots, x_m is true. Can be converted to **CNF** form

CNF formula showing making sure that at most one variable is assigned value **1**:

$$\bigwedge_{1 \leq i < j \leq k} (\bar{x}_i \vee \bar{x}_j)$$

Making sure that one of the variables is true: $\bigvee_{i=1}^k x_i$.

$$\oplus(x_1, x_2, \dots, x_k) = \bigwedge_{1 \leq i < j \leq k} (\bar{x}_i \vee \bar{x}_j) \wedge (x_1 \vee x_2 \vee \dots \vee x_k).$$

Notation

Some abbreviations for ease of notation

$\bigwedge_{k=1}^m x_k$ means $x_1 \wedge x_2 \wedge \dots \wedge x_m$

$\bigvee_{k=1}^m x_k$ means $x_1 \vee x_2 \vee \dots \vee x_m$

$\bigoplus(x_1, x_2, \dots, x_k)$ is a formula that means **exactly one** of x_1, x_2, \dots, x_m is true. Can be converted to **CNF** form

CNF formula showing making sure that at most one variable is assigned value **1**:

$$\bigwedge_{1 \leq i < j \leq k} (\bar{x}_i \vee \bar{x}_j)$$

Making sure that one of the variables is true: $\bigvee_{i=1}^k x_i$.

$$\bigoplus(x_1, x_2, \dots, x_k) = \bigwedge_{1 \leq i < j \leq k} (\bar{x}_i \vee \bar{x}_j) \wedge (x_1 \vee x_2 \vee \dots \vee x_k).$$

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Clauses of φ

φ is the conjunction of 8 clause groups:

$$\varphi = \bigwedge_{i=1}^{12} \varphi_i$$

where each φ_i is a CNF formula. Described in subsequent slides.

Property: φ is satisfied \iff there is an execution of **M** on **x** that accepts the language in $\mathbf{p}(|\mathbf{x}|)$ time.

24.4.2

The consistency of execution

The variables of φ

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: j th instruction of M

$I(j, i)$: Instruction j was issued at time i .

$H(h, i)$: The head is at location h at time i .

$T(c, h, i)$: The tape at location h at time i stored the character c .

φ_1 : The input is encoded correctly

φ_1 asserts (is true iff) the variables are set T/F indicating that **M** starts in state **q₀** at time **0** with tape contents containing **x** followed by blanks. Let **x** = **x₁x₂ . . . x_n**

$$\begin{aligned}\varphi_1 = & \text{S}(\mathbf{q}_0, \mathbf{0}) && // \text{ state at time } \mathbf{0} \text{ is } \mathbf{q}_0 \\ & \bigwedge_{h=1}^n \text{T}(\mathbf{x}_h, \mathbf{h}, \mathbf{0}) && // \text{ at time } \mathbf{0} \text{ cells } \mathbf{1} \text{ to } \mathbf{n} \text{ have value } \mathbf{x}_1 \text{ to } \mathbf{x}_n \\ & \wedge \bigwedge_{h=n+1}^{p(n)} \text{T}(_, \mathbf{h}, \mathbf{0}) && // \text{ all remaining cells are blank} \\ & \wedge \text{H}(\mathbf{1}, \mathbf{0}) && // \text{ The head is at time } \mathbf{0} \text{ at start of tape}\end{aligned}$$

φ_2 : **M** is in exactly one state at any point in time

φ_2 asserts **M** in exactly one state at any time **i**:

$$\varphi_2 = \bigwedge_{i=0}^{p(|x|)} \left(\bigoplus (S(q_0, i), S(q_1, i), \dots, S(q_{|Q|}, i)) \right)$$

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: **j**th instruction of **M**

I(j, i): Instruction **j** was issued at time **i**.

H(h, i): The head is at location **h** at time **i**.

T(c, h, i): The tape at location **h** at time **i** stored the character **c**.

φ_3 : Each tape cell holds a unique symbol at any time

φ_3 asserts that each tape cell holds a unique symbol at any given time.

$$\varphi_3 = \bigwedge_{i=0}^{p(|x|)} \bigwedge_{h=1}^{p(|x|)} \oplus (T(b_1, h, i), T(b_2, h, i), \dots, T(b_{|\Gamma|}, h, i))$$

For each time i and for each cell position h exactly one symbol $b \in \Gamma$ at cell position h at time i

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: j th instruction of M

$I(j, i)$: Instruction j was issued at time i .

$H(h, i)$: The head is at location h at time i .

$T(c, h, i)$: The tape at location h at time i stored the character c .

φ_4 : tape head of **M** is in exactly one position at any time **i**

φ_4 asserts that the read/write head of **M** is in exactly one position at any time **i**

$$\varphi_4 = \bigwedge_{i=0}^{p(|x|)} (\oplus (\mathbf{H}(1, i), \mathbf{H}(2, i), \dots, \mathbf{H}(p(|x|), i)))$$

Variables:

$\langle \mathbf{q}_j, \mathbf{b}_j, \mathbf{q}'_j, \mathbf{b}'_j, \mathbf{d}_j \rangle$: **j**th instruction of **M**

I(j, i): Instruction **j** was issued at time **i**.

H(h, i): The head is at location **h** at time **i**.

T(c, h, i): The tape at location **h** at time **i** stored the character **c**.

φ_5 : **M** accepts the input

φ_5 asserts that **M** accepts

- ▶ Let q_a be unique accept state of **M**
- ▶ without loss of generality assume **M** runs all $p(|x|)$ steps

$$\varphi_5 = S(q_a, p(|x|))$$

State at time $p(|x|)$ is q_a the accept state.

If we don't want to make assumption of running for all steps

$$\varphi_5 = \bigvee_{i=1}^{p(|x|)} S(q_a, i)$$

which means **M** enters accepts state at some time.

φ_6 : **M** executes a unique instruction at each time

φ_6 asserts that **M** executes a unique instruction at each time

$$\varphi_6 = \bigwedge_{i=0}^{p(|x|)} \oplus (I(1, i), I(2, i), \dots, I(m, i))$$

where **m** is max instruction number.

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: **j**th instruction of **M**

I(j, i): Instruction **j** was issued at time **i**.

H(h, i): The head is at location **h** at time **i**.

T(c, h, i): The tape at location **h** at time **i** stored the character **c**.

φ_7 : Tape changes only because of the head writing something

φ_7 ensures that variables don't allow tape to change from one moment to next if the read/write head was not there.

"If head is **not** at position **h** at time **i** then at time **i + 1** the symbol at cell **h** must be unchanged"

$$\varphi_7 = \bigwedge_i \bigwedge_h \bigwedge_{b \neq c} \left(\overline{H(h, i)} \Rightarrow \overline{T(b, h, i) \wedge T(c, h, i + 1)} \right)$$

since $A \Rightarrow B$ is same as $\neg A \vee B$, rewrite above in **CNF** form

$$\varphi_7 = \bigwedge_i \bigwedge_h \bigwedge_{b \neq c} (H(h, i) \vee \neg T(b, h, i) \vee \neg T(c, h, i + 1))$$

φ_8 : Transitions are done from correct states

j th instruction of M : $\langle q_j, b_j, q'_j, b'_j, d_j \rangle$

$$\varphi_8 = \bigwedge_i \bigwedge_j (I(j, i) \Rightarrow S(q_j, i))$$

If instruction j is executed at time i then state at time i must be q_j .

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: j th instruction of M

$I(j, i)$: Instruction j was issued at time i .

$H(h, i)$: The head is at location h at time i .

$T(c, h, i)$: The tape at location h at time i stored the character c .

φ_9 : Transitions are done into correct state

j th instruction of M : $\langle q_j, b_j, q'_j, b'_j, d_j \rangle$

$$\varphi_9 = \bigwedge_i \bigwedge_j (I(j, i) \Rightarrow S(q'_j, i + 1))$$

If instruction j was performed at time i , then state at time $i + 1$ must be q'_j .

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: j th instruction of M

$I(j, i)$: Instruction j was issued at time i .

$H(h, i)$: The head is at location h at time i .

$T(c, h, i)$: The tape at location h at time i stored the character c .

φ_{10} : The character written on tape that triggered an instruction, is the correct one

$$\varphi_{10} = \bigwedge_i \bigwedge_h \bigwedge_j [(I(j, i) \wedge H(h, i)) \Rightarrow T(b_j, h, i)]$$

If instruction **j** was executed at time **i** and head was at position **h**, then cell **h** has the symbol needed to issue instruction **j** is written under the head location on the tape.

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: **j**th instruction of **M**

I(j, i): Instruction **j** was issued at time **i**.

H(h, i): The head is at location **h** at time **i**.

T(c, h, i): The tape at location **h** at time **i** stored the character **c**.

φ_{11} : The correct symbol was written to the tape at time **i**

$$\varphi_{11} = \bigwedge_i \bigwedge_j \bigwedge_h [(I(j, i) \wedge H(h, i)) \Rightarrow T(b'_j, h, i + 1)]$$

If instruction **j** was executed time **i** with head at **h**, then at next time step symbol **b'_j** was written in position **h**

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: **j**th instruction of **M**

I(j, i): Instruction **j** was issued at time **i**.

H(h, i): The head is at location **h** at time **i**.

T(c, h, i): The tape at location **h** at time **i** stored the character **c**.

φ_{12} : Head was moved in the right direction at time i

$$\varphi_{12} = \bigwedge_i \bigwedge_j \bigwedge_h [(I(j, i) \wedge H(h, i)) \Rightarrow H(h + d_j, i + 1)]$$

The head is moved properly according to instr j .

Variables:

$\langle q_j, b_j, q'_j, b'_j, d_j \rangle$: j th instruction of M

$I(j, i)$: Instruction j was issued at time i .

$H(h, i)$: The head is at location h at time i .

$T(c, h, i)$: The tape at location h at time i stored the character c .

24.4.3

Proof of correctness

Proof of Correctness

(Sketch)

- ▶ Given M , x , poly-time algorithm to construct φ
- ▶ if φ is satisfiable then the truth assignment completely specifies an accepting computation of M on x
- ▶ if M accepts x then the accepting computation leads to an "obvious" truth assignment to φ . Simply assign the variables according to the state of M and cells at each time i .

Thus M accepts x if and only if φ is satisfiable

24.5

NP-Complete problems to know and remember

List of NP-Complete Problems to Remember

Problems

1. **SAT**
2. **3SAT**
3. **CircuitSAT**
4. **Independent Set**
5. **Clique**
6. **Vertex Cover**
7. **Hamilton Cycle** and **Hamilton Path** in both directed and undirected graphs
8. **3Color** and **Color**